

**FP11**

DIAGNOSTIC NO. 1  
**MD-11-DFFPA-A**

EP-DFFPA-A-DL-A  
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**digital**  
MADE IN USA

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IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DEFFPA-A-D  
 PRODUCT NAME: PDP-11/34 FPP DIAGNOSTIC PART 1  
 DATE: DECEMBER 1976  
 AUTHOR: ANTHONY VEZZA

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1. ABSTRACT

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THE THREE PROGRAMS:

DFFPA DFFPB DFFPC

ARE DESIGN TO DETECT AND REPORT LOGIC FAULTS IN THE PDP 11/34 FPP-A FLOATING POINT PROCESSOR. THE DESIGN IS AN ATTEMPT TO REACH ALL ROM STATES, TAKE ALL BRANCH MICRO TESTS (BUT'S) AND VERIFY ALL THE LOGIC. THEY CONSIST OF 155 (OCT) INDIVIDUAL TESTS SEQUENCED TO DETECT AND ATTEMPT TO IDENTIFY FAULTS WITH A MINIMUM HARDWARE OR SOFTWARE LEVEL. THE TESTS ARE PARTIONED INTO THREE STAND-ALONE PROGRAMS DESCRIBED BELOW.

NOTE THAT ERROR REPORTS IN THESE PROGRAMS ARE BASED UPON THE KNOWLEDGE THAT ALL PREVIOUS TESTS HAVE BEEN RUN AND IN MOST CASE THAT THERE IS ONLY A SINGLE POINT FAULT IN THE FPP-A. IF THE PROGRAMS OR TESTS ARE NOT RUN IN ORDER THEN ERROR MESSAGES MAY NOT BE ACCURATE.

A. DFFPA

DFFPA TESTS:

LDFPS  
STFPS  
CFCC  
SETF, SETD, SETI AND SETL  
STST  
LDF AND LDD (ALL SOURCE MODES)  
STD (MODE 0 AND 1)  
ADDF, ADDD AND SUBD (MOST CONDITIONS)

B. DFFPB

DFFPB TESTS:

ADDF, ADDD AND SUBD (ALL CONDITIONS NOT TESTED IN DFFPA)  
CMPD AND CMPF  
DIVD AND DIVF  
MLLD AND MLDF  
MODD AND MODF

C. DFFPC

DFFPC TESTS:

STF AND STD (ALL MODES)  
STCFD AND STCDF  
CLRD AND CLRF  
NEGF AND NEG0

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ABSF AND ABSO  
TSTF AND TSTD  
NEGF, ABSF AND TSTF (ALL SOURCE MODES)  
NEGF, ABSF AND TSTF (ALL SOURCE MODES)  
LDFFS (ALL SOURCE MODES)  
LDCTF AND LDCLF  
LDCID AND LDCLD  
LDEXP  
STFPS (ALL DESTINATION MODES)  
STCFL AND STCFI  
STCOL AND STCDI  
STEXP  
STST

2. REQUIREMENTS

2.1 EQUIPMENT

A PDP 11/34 (WITH OR WITHOUT CONSOLE), LA30 (OR EQUIVALENT) AND AN FP11-A FLOATING POINT PROCESSOR. NOTE THAT A SPECIAL INTERRUPTS TEST MODULE IS BEING DESIGNED FOR USE IN THE MANUFACTURING ENVIRONMENT. WHEN THIS DEVICE IS PRESENT THE PROGRAM DFFPB WILL MAKE USE OF IT TO TEST THE FPP INTERRUPT ON BUS REQUEST FUNCTIONS.

2.2 STORAGE

ALL THREE PROGRAM REQUIRE A MEMORY SYSTEM OF AT LEAST 16K TO LOAD AND RUN.

2.3 PRELIMINARY PROGRAMS

THESE THREE DIAGNOSTICS WILL ASSUME THAT THE PDP 11/34 CENTRAL PROCESSOR IS FAULTLESS. THEREFORE WHEN IN DOUBT RUN THE PDP 11/34 PROCESSOR DIAGNOSTICS BEFORE THESE FP11-A DIAGNOSTICS.

3. LOADING PROCEDURE

THE PROGRAMS WILL BE SUPPLIED ON THE 11/34 DIAGNOSTIC MEDIA. REFER TO THE XXDP OPERATING MANUAL FOR FURTHER INFORMATION.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE SECTION 5.1

4.2 PROGRAM AND OPERATOR ACTION

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1. LOAD PROGRAM INTO MEMORY
2. LOAD ADDRESS 200
3. SET CONSOLE SWITCHES (IF CONSOLE IS PRESENT)
4. PRESS START  
ON FIRST PASS THE PROGRAM WILL IDENTIFY ITSELF. NOTE THAT IF THERE IS NO PHYSICAL CONSOLE THE PROGRAM WILL REQUEST THE OPERATOR FOR INITIAL VALUE FOR THE SOFTWARE SWITCH REGISTER (SEE SECTION 8.5). IF RUNNING UNDER ACT, APT OR CHAIN THIS DOES NOT APPLY.
5. THE PROGRAM WILL LOOP AND AN END OF PASS AND ERROR SUMMARY WILL BE TYPED AT THE END OF EVERY PASS.

5. OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

THE SWITCH SETTING ARE:

	OCTAL	
SW<15>=1...	10000	HALT ON ERROR
SW<14>=1...	4000	LOOP ON CURRENT TEST
SW<13>=1...	2000	INHIBIT ERROR TYPE OUTS
SW<12>=1...	1000	INHIBIT 7-BIT TRAPPING
SW<11>=1...	400	INHIBIT ITERATIONS
SW<10>=1...	200	RING TTY BELL ON ERROR
SW<9>=1....	100	LOOP ON ERROR
SW<8>=1....	40	LOOP ON TEST SPECIFIED IN SW<6> THROUGH SW<0>
SW<7>=1....	20	PRINT ERROR SUMMARY EVEN IF SW<13>=1. THIS APPLIES ONLY TO PROGRAM DFFPA.
SW<7>=1....	20	DESELECT CORRECT INTERRUPT TEST IN PROGRAM DFFPB. NOTE THAT THIS TEST WILL AUTOMATICALLY BE DESELECTED BY THE ABSENCE OF THE SPECIAL TEST EQUIPMENT DESIGNED TO CONDUCT THIS TEST. IF THIS EQUIPMENT IS NOT INSTALLED THERE IS NO NEED TO DESELECT THIS TEST. THIS APPLIES ONLY TO PROGRAM DFFPB!

6. ERRORS

6.1 SUMMARIES

IN PROGRAM DFFPA TESTS 1 AND 11 HAVE A SPECIAL ERROR SUMMARY FEATURE. THESE TWO TEST RUN MANY TEST PATTERNS THROUGH THE LOGIC. AFTER AN ERROR IS ENCOUNTERED, ONLY THE FIRST FIVE ERRORS ARE REPORTED

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(TYPED ON THE TTY). EVERY ERROR THOUGH IS LOGGED AND AN ERROR SUMMARY IS PRINTED WHEN THE TEST IS COMPLETE. NOTE THAT IF SW<13>=1 THIS SUMMARY WILL NOT BE TYPED UNLESS SW<7>=1. IN OTHER WORDS TO GET JUST AN ERROR SUMMARY FROM EITHER OF THESE TWO TESTS 1 AND 11 IN PROGRAM DFFPA BOTH SWITCHES 13 AND 7 MUST = 1.

## 6.2 ERROR RECOVERY

SW<15:9>=0... MOST ERRORS WILL CAUSE EXECUTION TO GO TO THE START OF THE NEXT TEST AFTER THE MESSAGE IS TYPED. A FEW TESTS ARE IN SECTIONS. IN THESE TESTS AN ERROR WILL CAUSE EXECUTION TO GO TO THE NEXT SECTION AFTER THE MESSAGE IS TYPED.

SW<15>=1... THE PROGRAM WILL HALT AFTER TYPING THE ERROR MESSAGE. PRESSING THE CONSOLE CONTINUE WILL CAUSE THE PROGRAM TO CONTINUE AS IF SW<15>=0.

## 7. RESTRICTIONS

NONE

## 8. MISCELLANEOUS

### 8.1 EXECUTION TIMES

LESS THAN 10 SECONDS FOR EACH PROGRAM ON ANY PASS.

### 8.2 STACK POINTER

THE STACK POINTER IS INITIALIZED TO 1100 IN EACH OF THE THREE PROGRAMS.

### 8.3 PASS COUNT

THE PROGRAM MAKES ONE PASS FOR EACH END OF PASS MESSAGE TYPED. THE END OF PASS MESSAGE DESCRIBES THE TOTAL NUMBER OF PASSES COMPLETED AND THE TOTAL NUMBER OF ERRORS SINCE THE LAST END OF PASS MESSAGE.

### 8.4 T-BIT TRAPPING

IF SW<12>=0 EACH PROGRAM WILL RUN WITH TRACE TRAPS ON EVERY OTHER PASS. FIRST PASS WILL NOT ENABLE TRACE TRAPS. NOTE SW<12>=1 DISABLES T-BIT TRAPS.

### 8.5 SOFTWARE SWITCH REGISTER

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EACH OF THE THREE PROGRAMS WILL RUN WITH OR WITHOUT A CONSOLE SWITCH REGISTER. IF A PHYSICAL CONSOLE SWITCH REGISTER IS PRESENT ON THE SYSTEM, THEN THESE PROGRAMS WILL GO AHEAD AND USE IT FOR THE SWITCH FUNCTIONS DESCRIBED IN 5.1 ABOVE. IF HOWEVER THERE IS NO CONSOLE SWITCH REGISTER ON THE SYSTEM A SOFTWARE SWITCH REGISTER WILL BE USED. THIS SOFTWARE SWITCH REGISTER CAN BE EXAMINED OR MODIFIED AT ANY TIME BY THE USER IF HE TYPES CONTROL G WHILE THE PROGRAM IS RUNNING. THIS CONTROL G WILL CAUSE THE CONTENTS OF THE SOFTWARE SWITCH REGISTER TO BE TYPED ON THE TTY AND ASK THE USER FOR A NEW VALUE. WHEN THE USER TYPES A VALUE AND CARRIAGE RETURN THEN THE PROGRAM WILL RESUME TESTING AT THE SAME POINT AT WHICH IT LEFT OFF WHEN THE USER TYPED CONTROL G. NOTE THAT WHEN NOT RUNNING UNDER ACT, APT OR CHAIN THE USER WILL BE ASKED FOR A SOFTWARE SWITCH REGISTER VALUE AFTER LOADING ADDRESS 200 AND STARTING THE PROGRAM THE FIRST TIME THE PROGRAM IS RUN AFTER LOADING (ONLY IF NO CONSOLE SWITCH REGISTER IS ON THE SYSTEM).

8.6 INTERRUPTS TEST

IN PROGRAM DFFPB THERE IS A SPECIAL TEST FOR CHECKING THE CORRECT FLOWS OF THE FPP. THIS TEST CAN BE RUN ONLY IF A SPECIAL TEST MODULE IS IN THE SYSTEM. THIS MODULE WILL PROBABLY ONLY BE USED IN MANUFACTURING. IF THIS MODULE IS NOT IN THE SYSTEM THIS TEST WILL AUTOMATICALLY BE DESELECTED. IF THIS TEST MODULE IS ON THE SYSTEM AND SW<7>=0 THIS TEST WILL BE RUN. IF SW<7>=1 THIS TEST WILL BE DESELECTED.

8.7 ACT, APT AND XXDP COMPATIBILITY

THESE PROGRAMS ARE FULLY COMPATIBLE WITH:  
APT  
ACT  
XXDP MONITOR AND CHAIN PROGRAMS.

9. PROGRAM DESCRIPTION  
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RECEIVED NOV 11 1976 21 03  
COMMUNICATIONS SECTION  
GENERAL INVESTIGATIVE  
DIVISION  
FEDERAL BUREAU OF  
INVESTIGATION  
WASHINGTON, D.C. 20535

TEST 1 LDFPS, STFPS AND DATA PATHS TEST  
-----

THIS IS A TEST OF THE LDFPS (LOAD FLOATING POINT STATUS) AND STFPS (STORE FLOATING POINT STATUS) INSTRUCTIONS. A COUNT PATTERN IS GENERATED AND RUN THROUGH THE FLOATING POINT STATUS REGISTER. THIS WILL TEST THE 16-BIT TRI STATE BUS WHICH CONNECTS THE CPU WITH THE FPP AND ALSO RUNS INTERNALLY WITHIN THE FPP. ONLY DMO AND SMO ARE USED. NOTE THAT A MASK MUST BE USED BECAUSE SOME OF THE FPS BITS CANNOT BE SET.

ONLY THE FIRST FIVE ERRORS WILL BE REPORTED INDIVIDUALLY. THIS IS TO PREVENT LOCKING OUT THE COMPLETION OF THE TEST BECAUSE OF VIRTUALLY ENDLESS NUMBER OF ERRORS. ONLY FIVE INDIVIDUAL ERRORS WILL BE REPORTED THEN THE TEST WILL BE COMPLETED AND AN ERROR SUMMARY GIVEN (SEE NOTE BELOW).

NOTE THAT THIS TEST KEEPS A DYNAMIC RECORD OF THE LOGICAL 'AND' AND 'OR' OF THE FAILING DATA PATTERNS. THESE CAN BE VERY USEFUL IN DETERMINING STUCK BITS. IF THE USER HAS THE INHIBIT ERROR TYPE OUT SWITCH (SW13) OFF, THEN THE USER WILL RECEIVE EACH INDIVIDUAL ERROR MESSAGE PLUS AN ERROR SUMMARY AT THE END OF THE TEST. INHIBITING ERROR PRINT OUT WILL INHIBIT ERROR SUMMARY PRINT OUT, EXCEPT IN THE CASE DESCRIBED BELOW. TO GET JUST THE ERROR SUMMARY WITH NO INDIVIDUAL ERROR REPORTS, SET SWITCH REGISTER BIT 13 AND SWITCH REGISTER BIT 7 BOTH ON.

TEST 2 CFCC TEST  
-----

THIS IS A TEST OF THE COPY CONDITION CODES INSTRUCTION, CFCC.

TEST 3 SETF, SETD, SETI AND SETL TEST  
-----

THIS IS A TEST OF THE SETF, SETD, SETI AND SETL INSTRUCTIONS. EACH INSTRUCTION IS EXECUTED WITH THE FPS CONTAINING ALL ONES AND ALSO WITH THE FPS CLEAR. THE RESULT OF EACH SITUATION IS CHECKED.

TEST 4 ILLEGAL FPP OP CODES AND STST TEST  
-----

THIS IS A TEST OF THE FPP OPERATION CODES:

170004

170010  
170013  
170014

170077

THESE ARE ILLEGAL INSTRUCTIONS AND WITH INTERRUPTS ENABLED SHOULD CAUSE A TRAP TO 244. ALSO TESTED HERE IS THE INSTRUCTION: STST R1, WHICH SHOULD PUT THE FEC CODE 2 IN R1, AFTER ANY OF THE ABOVE OP CODES IS EXECUTED.

TEST 5 FID, INTERRUPT DISABLE, BIT TEST

THIS IS A TEST OF FPS BIT 14 (FID) OR FLOATING INTERRUPT DISABLE. AN ILLEGAL INSTRUCTION IS EXECUTED WITH FID=1. NO INTERRUPT SHOULD OCCUR.

TEST 6 LDD AND STD, WITH SRC AND DST MODE 1, TEST

THIS IS A TEST OF BOTH THE INSTRUCTION:  
LDD (RD),ACD  
AND THE INSTRUCTION:  
STD ACD,(RD) MOST OF THE FAILURES ARE ISOLATED TO THE SRC OR DST FLOWS. NOTE THAT THE INTEGRITY OF ACD HAS NOT BEEN ASSURED. THIS MEANS THAT IN SOME CASES IT WILL BE IMPOSSIBLE TO ISOLATE CERTAIN DATA PATTERN FAILURES TO EITHER THE FLOWS OR THIS ACCUMULATOR.

TEST 7 FSRC MODE 0 TEST

THIS IS A TEST OF FSRC MODE ZERO USING THE LDD AND LDF INSTRUCTIONS.

TEST 10 FDST MODE 0 TEST

THIS IS A TEST OF THE STORE INSTRUCTIONS. STD AND STF, WITH FDST MODE 0.

TEST 11 ACCUMULATORS DATA PATTERNS TEST

THIS IS A TEST OF THE FLOATING POINT PROCESSOR ACCUMULATORS.

EACH ACCUMULATOR IS TESTED IN TWO WAYS:  
1 TEST PATTERN GENERATED BY FLOATING A ONE ACROSS A FIELD OF ZEROES.

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2 TEST PATTERN GENERATED BY FLOATING A  
 ZERO ACROSS A FIELD OF ONES.  
 EACH OF ACCUMULATORS ACC THROUGH ACS IS TESTED.

NOTE THAT THIS TEST KEEPS A DYNAMIC RECORD OF THE LOGICAL 'AND' AND 'OR' OF THE FAILING DATA PATTERNS. THESE CAN BE VERY USEFUL IN DETERMINING STUCK BITS. IF THE USER HAS THE INHIBIT ERROR TYPE OUT SWITCH (SWR13) OFF, THEN THE USER WILL RECEIVE EACH INDIVIDUAL ERROR MESSAGE PLUS AN ERROR SUMMARY AT THE END OF THE TEST. INHIBITING ERROR PRINT OUT WILL INHIBIT ERROR SUMMARY PRINT OUT, EXCEPT IN THE CASE DESCRIBED BELOW. TO GET JUST THE ERROR SUMMARY WITH NO INDIVIDUAL ERROR REPORTS, SET SWITCH REGISTER BIT13 AND SWITCH REGISTER BIT7 BOTH ON.

THE FOLLOWING PROCEDURE IS PRESENTED TO AID THE TROUBLE SHOOTER IN SITUATIONS WHERE AM2901 CHIP ISOLATION IS ATTEMPTED.

WARNING: THIS PROCEDURE ASSUMES THAT THE FAULT IS IN ONE OF THE AM2901 CHIPS. THIS ASSUMPTION IS NOT NECESSARILY VALID IN ALL SITUATIONS. IT REMAINS TO BE SEEN WHAT NUMBER OF FAILURES CAN PROBABILISTICALLY ASSOCIATED WITH THEM. NOTE ALSO THAT THIS INFORMATION SHOULD NOT BE TAKEN AS ABSOLUTE, THAT IS THIS INFORMATION IS THE AUTHOR'S SUGGESTION FOR ACHIEVING ISOLATION WHEN CHIP LEVEL REPAIR IS NECESSARY.

WHEN THIS TEST HAS FINISHED RUNNING, IF ERRORS HAVE OCCURRED, AN ERROR SUMMARY WILL BE TYPED. THIS SUMMARY WILL CONSIST OF TWO IMPORTANT QUANTITIES:

- A. FOUR SIXTEEN BIT NUMBERS LABELED THE LOGICAL 'AND' ('\*') OF THE FAILING DATA PATTERNS.
- B. FOUR SIXTEEN BIT NUMBERS LABELED THE LOGICAL 'OR' ('+') OF THE FAILING DATA PATTERNS.

A BIT STUCK HIGH IN THE HARDWARE WILL SHOW UP AS A 0 IN THAT BIT POSITION OF THE 'OR' OF THE FAILING DATA PATTERNS.

A BIT STUCK LOW IN THE HARDWARE WILL SHOW UP AS A 1 IN THAT BIT POSITION OF THE 'AND' OF THE FAILING DATA PATTERNS.

THUS IF A FAILURE OCCURS:

- A. STUCK HIGHS WILL SHOW AS 0'S IN THE 'OR' PATTERN.
- B. STUCK LOWS WILL SHOW AS 1'S IN THE 'AND' PATTERN.

IF THE FAILURE IS INTERMITTANT THEN THIS PROCEDURE WILL

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STILL APPLY!! IF THE FAILURE MOVES FROM ONE BIT TO ANOTHER, OR FROM ONE GROUP OF BITS TO ANOTHER GROUP OF BITS THEN THE FAULT WILL PROBABLY NOT SHOW UP IN THE 'AND' OR THE 'OR' PATTERNS; IN THIS CASE THE 'AND' PATTERN WILL BE ALL 0'S AND THE 'OR' PATTERN WILL BE ALL 1'S. WHEN THIS OCCURS SOME OTHER METHOD OF REPAIR MUST BE FOUND (SUCH AS INSPECTION OF EACH INDIVIDUAL ERROR REPORT RATHER THAN USING THE SUMMARY).

MAP THE FOLLOWING NOTATION ONTO EACH BIT POSITION IN THE 'AND' AND THE 'OR' PATTERNS WHICH ARE TYPED IN THE ERROR SUMMARY.

A15,A14,...A1,A0      B15,B14,...B1,B0  
 C15,C14,...C1,C0      D15,D14,...D1,D0

IN THIS NOTATION A15 THROUGH A0 IS THE FIRST OF THE FOUR 16 BIT OCTAL NUMBERS TYPED, B15 THROUGH B0 IS THE SECOND, ETC.

THIS TABLE SHOWS THE CORRESPONDING AM2901 CHIP ('E' NUMBER) WHICH IS RESPONSIBLE FOR EACH BIT POSITION USING THE ABOVE NOTATION. NOTE THAT ECO'S TO THE HARDWARE MIGHT MAKE THIS TABLE OBSOLETE IF IT IS NOT UP DATED. NOTE ALSO THAT THERE ARE FOUR BITS FOR EACH AM2901 CHIP:

BITS ----	AM2901 CHIP NUMBER -----
A15,A14,A13,A12	E61
A11,A10,A9,A8	E62
A7,A6,A5,A4	E90
A3,A2,A1,A0	E81
B15,B14,B13,B12	E86
B11,B10,B9,B8	E85
B7,B6,B5,B4	E83
B3,B2,B1,B0	E88
C15,C14,C13,C12	E79
C11,C10,C9,C8	E84
C7,C6,C5,C4	E89
C3,C2,C1,C0	E87
D15,D14,D13,D12	E78
D11,D10,D9,D8	E77
D7,D6,D5,D4	E82
D3,D2,D1,D0	E80

NOW FIVE IMPORTANT CASES WHICH WILL ARRISE WHEN A FAULTY AM2901 IS PRESENT CAN BE DESCRIBED:

- 1.) IF ONLY ONE BIT OF THE 64 BITS IS INCORRECT

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THE CHIP INDICATED IN THE ABOVE TABLE IS MOST PROBABLY AT FAULT. BUT IF THAT CHIP IS REPLACED AND THE ERROR PERSISTS THEN SUPPOSE THAT BIT IS.

LN WHERE 'L' IS A, B, C OR D  
AND N IS 15, 14, ... OR 0

THEN IN GENERAL ANY OF THE FOUR CHIPS RESPONSIBLE FOR AN, BN, CN OR DN COULD BE AT FAULT, WITH LN BEING MOST PROBABLE.

FOR EXAMPLE IF BIT C12 IS FAULTY, THEN CHIP E79 IS THE MOST PROBABLE SOURCE OF THE ERROR. IF REPAIRING THAT CHIP DOES NOT REMOVE THE FAULT THEN TRY EACH OF THE CHIPS ASSOCIATED WITH BITS A12, B12 AND D12 SHOULD BE TRIED WITH EQUAL PROBABILITY OF THE FAULT BEING IN ANY ONE OF THESE OTHER THREE CHIPS, TRY CHIPS E62, E66 AND E78.

2.) IF THERE ARE FOUR CONSECUTIVE BITS IN ERROR, FOLLOWING THE PATTERN:

LN, LN+1, LN+2 AND LN+3 WHERE 'L' IS A, B, C OR D  
N=0, 4, 8 OR 12

THEN THE ABOVE TABLE SHOULD DIRECTLY IDENTIFY THE FAILING CHIP.

3.) IF FOUR BITS ARE DROPPED WHICH FIT THE PATTERN:

AN, BN, CN AND DN WHERE N=15, 14, ... OR 0  
OR 0

THEN ANY ONE OF THE FOUR CHIPS ASSOCIATED WITH EACH OF THE BITS AN, BN, CN AND DN COULD BE AT FAULT WITH EQUAL PROBABILITY.

4.) IF 16 BITS ARE IN ERROR, FITTING THE PATTERN:

AN, AN+1, AN+2, AN+3 WHERE N=0, 4, 8 OR 12  
BN, BN+1, BN+2, BN+3  
CN, CN+1, CN+2, CN+3  
DN, DN+1, DN+2, DN+3

THEN ANY ONE OF THE FOUR CHIPS ASSOCIATED WITH THESE BITS COULD BE AT FAULT WITH EQUAL PROBABILITY.

5.) IF THE FAILING BIT PATTERNS DISPLAYED IN THE 'AND' AND THE 'OR' DATA TYPED IN THE SUMMARY DOES NOT CONFORM EXPLICITELY TO ANY OF THE

# NO1

ABOVE PATTERNS, THEN THE TROUBLE SHOOTER  
MUST INTUITIVELY TRY TO FIND WHICH OF THE  
ABOVE CASES (1 THROUGH 4) IS A BEST FIT OF  
THE SYMPTOMS.

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TEST 12 FPP ACCUMULATORS DUAL ADDRESS TEST  
-----

THIS TEST PERFORMS A DUAL ADDRESSING TEST ON THE  
FLOATING ACCUMULATORS. NOTE THAT ACCUMULATOR ZERO  
IS USED TO ACCESS ALL THE OTHERS.

TEST 13 FSRC MODE 0 WITH ILLEGAL ACCUMULATOR TEST  
-----

THIS IS A TEST OF FSRC MODE 0 WITH ACCUMULATORS 6  
AND 7. USE OF EITHER OF THESE NON-EXISTENT  
ACCUMULATORS SHOULD RESULT IN A TRAP TO 244 WITH  
FEC=2 (ILLEGAL FPP INSTRUCTION).

TEST 14 FSRC MODE 2 TEST  
-----

THIS IS A TEST OF FSRC MODE 2, AUTO INCREMENT MODE.

TEST 15 FSRC MODE 4 TEST  
-----

THIS IS A TEST OF FSRC MODE 4, AUTO DECREMENT MODE.

TEST 16 FSRC MODE 2, WITH FD=0, TEST  
-----

THIS IS A TEST OF FSRC MODE 2 WITH FD=0. (AUTO  
INCREMENT)

TEST 17 FSRC MODE 2 WITH GR7, IMMEDIATE MODE, TEST  
-----

THIS IS A TEST OF FSRC MODE 2 USING GR7 (THE PC).  
THIS IS IMMEDIATE MODE.

TEST 20 FSRC MODE 3 TEST  
-----

THIS IS A TEST OF FSRC MODE 3, AUTO INCREMENT  
DEFERRED

TEST 21 FSRC MODE 5 TEST  
-----

THIS IS A TEST OF FSRC MODE 5, AUTO DECREMENT  
DEFERRED.

TEST 22 FSRC MODE 6 TEST



THIS IS A TEST OF THE ADD AND ADDF INSTRUCTIONS AND THE ALIGN AC ALGORITHM FLOWS. THE CONSTANT (25 FOR FLOATING, 57 FOR DOUBLE) USED IS CHECKED. THEN SIMPLE AND WORST CASE ALIGNMENT SITUATIONS ARE TRIED. NOTE E(AC) IS LESS THEN E(FSRC)

TEST 33 ADDF AND ADD WITH E(AC) GREATER THAN E(FSRC) TEST

THIS IS A TEST OF THE ADD AND ADDF INSTRUCTIONS AND THE ALIGN FSRC ALGORITHM FLOWS. FIRST THE CONSTANT USED IS CHECKED. THEN SIMPLE AND WORST CASE ALIGNMENT SITUATIONS ARE TRIED. NOTE E(AC) IS GREATER THAN E(FSRC).

TEST 34 ADD WITH NEGATIVE OPRANDS TEST

THIS IS A TEST OF THE ADD INSTRUCTION WITH NEGATIVE OPRANDS. EVERY COMBINATION OF OPRAND SIGNS IS TRIED.

TEST 35 SUBD TEST

THIS IS A TEST OF THE SUBD INSTRUCTION. BOTH A POSITIVE AND A NEGATIVE NUMBER IS SUBTRACTED FROM IT SELF

TEST 36 NORMALIZE ALGORITHM TEST

THIS IS A TEST OF THE NORMALIZE FLOW ALGORITHM. TWO PATTERNS ARE USED. FIRST THE MINIMUM SITUATION REQUIRING ONE LEFT SHIFT AND THEN THE MAXIMUM SITUATION REQUIRING 56 SHIFTS.

Vertical text on the left margin, possibly a page number or document identifier.





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040000  
020000  
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004000  
002000  
001000  
000400  
000200  
000100

```
$SWMSK=200
TAB=11
CRLF=15

.SBTTL BASIC DEFINITIONS

:*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT,ERROR      ::BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE      ::BASIC DEFINITION OF SCOPE CALL

:*MISCELLANEOUS DEFINITIONS
HT= 11                ::CODE FOR HORIZONTAL TAB
LF= 12                ::CODE FOR LINE FEED
CR= 15                ::CODE FOR CARRIAGE RETURN
CRLF= 200             ::CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776           ::PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774        ::STACK LIMIT REGISTER
PIRQ= 177772          ::PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570         ::HARDWARE SWITCH REGISTER
DDISP= 177570        ::HARDWARE DISPLAY REGISTER

:*GENERAL PURPOSE REGISTER DEFINITIONS
R0= %0                ::GENERAL REGISTER
R1= %1                ::GENERAL REGISTER
R2= %2                ::GENERAL REGISTER
R3= %3                ::GENERAL REGISTER
R4= %4                ::GENERAL REGISTER
R5= %5                ::GENERAL REGISTER
R6= %6                ::GENERAL REGISTER
R7= %7                ::GENERAL REGISTER
SP= %6                ::STACK POINTER
PC= %7                ::PROGRAM COUNTER

:*PRIORITY LEVEL DEFINITIONS
PR0= 0                ::PRIORITY LEVEL 0
PR1= 40               ::PRIORITY LEVEL 1
PR2= 100              ::PRIORITY LEVEL 2
PR3= 140              ::PRIORITY LEVEL 3
PR4= 200              ::PRIORITY LEVEL 4
PR5= 240              ::PRIORITY LEVEL 5
PR6= 300              ::PRIORITY LEVEL 6
PR7= 340              ::PRIORITY LEVEL 7

:*"SWITCH REGISTER" SWITCH DEFINITIONS
SW15= 100000
SW14= 40000
SW13= 20000
SW12= 10000
SW11= 4000
SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
SW06= 100
```

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000060

000040  
000020  
000010  
000004  
000002  
000001

100000  
040000  
020000  
010000  
004000  
002000  
001000  
000400  
000200  
000100  
000040  
000020  
000010  
000004  
000002  
000001

SW05= 40  
SW04= 20  
SW03= 10  
SW02= 5  
SW01= 2  
SW00= 1  
.EQUIV SW09,SW9  
.EQUIV SW08,SW8  
.EQUIV SW07,SW7  
.EQUIV SW06,SW6  
.EQUIV SW05,SW5  
.EQUIV SW04,SW4  
.EQUIV SW03,SW3  
.EQUIV SW02,SW2  
.EQUIV SW01,SW1  
.EQUIV SW00,SW0

.\*DATA BIT DEFINITIONS (BIT00 TO BIT15)

BIT15= 100000  
BIT14= 40000  
BIT13= 20000  
BIT12= 10000  
BIT11= 4000  
BIT10= 2000  
BIT09= 1000  
BIT08= 400  
BIT07= 200  
BIT06= 100  
BIT05= 40  
BIT04= 20  
BIT03= 10  
BIT02= 4  
BIT01= 2  
BIT00= 1  
.EQUIV BIT09,BIT9  
.EQUIV BIT08,BIT8  
.EQUIV BIT07,BIT7  
.EQUIV BIT06,BIT6  
.EQUIV BIT05,BIT5  
.EQUIV BIT04,BIT4  
.EQUIV BIT03,BIT3  
.EQUIV BIT02,BIT2  
.EQUIV BIT01,BIT1  
.EQUIV BIT00,BIT0

.\*BASIC "CPU" TRAP VECTOR ADDRESSES

ERRVEC= 4 : TIME OUT AND OTHER ERRORS  
RESVEC= 10 : RESERVED AND ILLEGAL INSTRUCTIONS  
TBITVEC= 14 : "T" BIT  
TRTVEC= 14 : TRACE TRAP  
BPTVEC= 14 : BREAKPOINT TRAP (BPT)  
IOTVEC= 20 : INPUT/OUTPUT TRAP (IOT) \*\*SCOPE\*\*  
PWRVEC= 24 : POWER FAIL  
EMTVEC= 30 : EMULATOR TRAP (EMT) \*\*ERROR\*\*  
TRAPVEC= 34 : "TRAP" TRAP  
TKYVEC= 60 : TTY KEYBOARD VECTOR

```

1009      000064      TPVEC= 64          ::TTY PRINTER VECTOR
1010      000240      PIRQVEC=240      ::PROGRAM INTERRUPT REQUEST VECTOR
1011      .SBTTL FPP REGISTER DEFINITIONS
1012      000000      AC0          =%0
1013      000001      AC1          =%1
1014      000002      AC2          =%2
1015      000003      AC3          =%3
1016      000004      AC4          =%4
1017      000005      AC5          =%5
1018      000006      AC6          =%6
1019      000007      AC7          =%7
1020
1021      .SBTTL TRAP CATCHER
1022
1023      000000      .=0
1024      ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
1025      ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
1026      ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
1027
1028      000174      000174      DISPREG: .WORD 0          ::SOFTWARE DISPLAY REGISTER
1029      000176      000000      SWREG:   .WORD 0          ::SOFTWARE SWITCH REGISTER
1030
1031      000200      000137      003606      .SBTTL STARTING ADDRESS(ES)
1032      JMP      2*START ;;JUMP TO STARTING ADDRESS OF PROGRAM

```

.SBTTL COMMON TAGS

::\*\*\*\*\*  
: THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS  
: \*JSEC IN THE PROGRAM.

1032		
1033		
1034		
1035		
1036		
1037		
1038		001100
1039	001100	
1040	001100	000000
1041	001100	000
1042	001100	000
1043	001100	000000
1044	001100	000000
1045	001110	000000
1046	001110	000000
1047	001114	000
1048	001115	001
1049	001116	000000
1050	001120	000000
1051	001120	000000
1052	001124	000000
1053	001126	000000
1054	001128	000000
1055	001132	000000
1056	001134	000
1057	001136	000
1058	001136	000000
1059	001140	177570
1060	001142	177570
1061	001144	177560
1062	001146	177562
1063	001150	177564
1064	001152	177566
1065	001154	000
1066	001155	002
1067	001156	012
1068	001157	000
1069	001160	000000
1070		
1071	001162	000000
1072	001164	000000
1073	001166	000000
1074	001170	000000
1075	001172	000000
1076	001174	000000
1077	001176	000000
1078	001200	000000
1079	001202	000000
1080	001204	000000
1081	001206	000000
1082	001210	000000
1083	001212	000000
1084	001214	000000
1085	001216	000000
1086	001220	000000
1087	001222	000000

.SBTTL	.=1100
\$CMTAG:	.WORD 0
\$STNM:	.BYTE 00
\$ERFLG:	.BYTE 00
\$ICNT:	.WORD 00
\$LPADR:	.WORD 00
\$LPERR:	.WORD 00
\$ERTTL:	.WORD 00
\$ITEMB:	.BYTE 00
\$ERMAX:	.BYTE 01
\$ERRPC:	.WORD 00
\$GDADR:	.WORD 00
\$BDADR:	.WORD 00
\$GDADR:	.WORD 00
\$BDADR:	.WORD 00
\$AUTOB:	.BYTE 00
\$INTAG:	.BYTE 00
\$AR:	.WORD 0
DISPLAY:	.WORD DCISP
\$TKS:	177560
\$TKB:	177562
\$TPS:	177564
\$TPB:	177566
\$NULL:	.BYTE 0
\$FILLS:	.BYTE 2
\$FILLC:	.BYTE 12
\$TPFLG:	.BYTE 0
\$REGAD:	.WORD 0
\$REG0:	.WORD 0
\$REG1:	.WORD 0
\$REG2:	.WORD 0
\$REG3:	.WORD 0
\$REG4:	.WORD 0
\$REG5:	.WORD 0
\$REG6:	.WORD 0
\$REG7:	.WORD 0
\$REG10:	.WORD 0
\$REG11:	.WORD 0
\$REG12:	.WORD 0
\$REG13:	.WORD 0
\$REG14:	.WORD 0
\$REG15:	.WORD 0
\$REG16:	.WORD 0
\$REG17:	.WORD 0
\$REG20:	.WORD 0

:: START OF COMMON TAGS

:: CONTAINS THE TEST NUMBER

:: CONTAINS ERROR FLAG

:: CONTAINS SUBTEST ITERATION COUNT

:: CONTAINS SCOPE LOOP ADDRESS

:: CONTAINS SCOPE RETURN FOR ERRORS

:: CONTAINS TOTAL ERRORS DETECTED

:: CONTAINS ITEM CONTROL BYTE

:: CONTAINS MAX. ERRORS PER TEST

:: CONTAINS PC OF LAST ERROR INSTRUCTION

:: CONTAINS ADDRESS OF 'GOOD' DATA

:: CONTAINS ADDRESS OF 'BAD' DATA

:: CONTAINS 'GOOD' DATA

:: CONTAINS 'BAD' DATA

:: RESERVED--NOT TO BE USED

:: AUTOMATIC MODE INDICATOR

:: INTERRUPT MODE INDICATOR

:: ADDRESS OF SWITCH REGISTER

:: ADDRESS OF DISPLAY REGISTER

:: TTY KBD STATUS

:: TTY KBD BUFFER

:: TTY PRINTER STATUS REG. ADDRESS

:: TTY PRINTER BUFFER REG. ADDRESS

:: CONTAINS NULL CHARACTER FOR FILLS

:: CONTAINS # OF FILLER CHARACTERS REQUIRED

:: INSERT FILL CHARS. AFTER A "LINE FEED"

:: "TERMINAL AVAILABLE" FLAG (BIT-07)=0=YES)

:: CONTAINS THE ADDRESS FROM WHICH (\$REG0) WAS OBTAINED

:: CONTAINS ((\$REGAD)+0)

:: CONTAINS ((\$REGAD)+2)

:: CONTAINS ((\$REGAD)+4)

:: CONTAINS ((\$REGAD)+6)

:: CONTAINS ((\$REGAD)+10)

:: CONTAINS ((\$REGAD)+12)

:: CONTAINS ((\$REGAD)+14)

:: CONTAINS ((\$REGAD)+16)

:: CONTAINS ((\$REGAD)+20)

:: CONTAINS ((\$REGAD)+22)

:: CONTAINS ((\$REGAD)+24)

:: CONTAINS ((\$REGAD)+26)

:: CONTAINS ((\$REGAD)+30)

:: CONTAINS ((\$REGAD)+32)

:: CONTAINS ((\$REGAD)+34)

:: CONTAINS ((\$REGAD)+36)

:: CONTAINS ((\$REGAD)+40)

1088 001224 000000  
1089 001226 000000  
1090 001230 000000  
1091 001232 000000  
1092 001234 000000  
1093 001236 000000  
1094 001240 000000  
1095 001242 000000  
1096 001244 000000  
1097 001246 000000  
1098 001250 000000  
1099 001252 000000  
1100 001254 000000  
1101 001256 000000  
1102 001258 000000  
1103 001262 000000  
1104 001264 000000  
1105 001266 000000  
1106 001270 000000  
1107 001272 000000  
1108 001274 000000  
1109 001276 000000  
1110 001300 000000  
1111 001302 000000  
1112 001304 000000  
1113 001306 177607 000377  
1114 001312 077  
1115 001313 015  
1116 001314 000012  
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1121  
1122 001316  
1123 001316 000000  
1124 001320 000000  
1125 001322 000000  
1126 001324 000000  
1127 001326 000000  
1128 001330 000000  
1129 001332 000000  
1130 001334 000000  
1131 001336 000  
1132 001336 000  
1133 001337 000  
1134 001340 000000  
1135 001342 000000  
1136 001344 000000  
1137  
1138  
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1141  
1142  
1143 001346 000

\$REG21: .WORD 00 ::CONTAINS ((\$REGAD,+42)  
\$REG22: .WORD 00 ::CONTAINS ((\$REGAD,+44)  
\$REG23: .WORD 00 ::CONTAINS ((\$REGAD,+46)  
\$TMP0: .WORD 00 ::USER DEFINED  
\$TMP1: .WORD 00 ::USER DEFINED  
\$TMP2: .WORD 00 ::USER DEFINED  
\$TMP3: .WORD 00 ::USER DEFINED  
\$TMP4: .WORD 00 ::USER DEFINED  
\$TMP5: .WORD 00 ::USER DEFINED  
\$TMP6: .WORD 00 ::USER DEFINED  
\$TMP7: .WORD 00 ::USER DEFINED  
\$TMP10: .WORD 00 ::USER DEFINED  
\$TMP11: .WORD 00 ::USER DEFINED  
\$TMP12: .WORD 00 ::USER DEFINED  
\$TMP13: .WORD 00 ::USER DEFINED  
\$TMP14: .WORD 00 ::USER DEFINED  
\$TMP15: .WORD 00 ::USER DEFINED  
\$TMP16: .WORD 00 ::USER DEFINED  
\$TMP17: .WORD 00 ::USER DEFINED  
\$TMP20: .WORD 00 ::USER DEFINED  
\$TMP21: .WORD 00 ::USER DEFINED  
\$TMP22: .WORD 00 ::USER DEFINED  
\$TMP23: .WORD 00 ::USER DEFINED  
\$TIMES: 0 ::MAX. NUMBER OF ITERATIONS  
\$ESCAPE: 0 ::ESCAPE ON ERROR ADDRESS  
\$BELL: .ASCIZ <207><377><377> ::CODE FOR BELL  
\$QUES: .ASCII /?/? ::QUESTION MARK  
\$CRLF: .ASCII <15> ::CARRIAGE RETURN  
\$LF: .ASCIZ <12> ::LINE FEED  
:\*\*\*\*\*  
\$BTTL APT MAILEX-ETABLE  
:\*\*\*\*\*  
\$EVEN  
\$MAIL: ::APT MAILBOX  
\$MSGTY: .WORD AMSGTY ::MESSAGE TYPE CODE  
\$FATAL: .WORD AFATAL ::FATAL ERROR NUMBER  
\$TESTN: .WORD ATESTN ::TEST NUMBER  
\$PASS: .WORD APASS ::PASS COUNT  
\$DEVCT: .WORD ADEVCT ::DEVICE COUNT  
\$JUNIT: .WORD AUNIT ::I/O UNIT NUMBER  
\$MSGAD: .WORD AMSGAD ::MESSAGE ADDRESS  
\$MSGLG: .WORD AMSGLG ::MESSAGE LENGTH  
\$ETABLE: ::APT ENVIRONMENT TABLE  
\$ENV: .BYTE AENV ::ENVIRONMENT BYTE  
\$ENVM: .BYTE AENVM ::ENVIRONMENT MODE BITS  
\$SWREG: .WORD ASWREG ::APT SWITCH REGISTER  
\$USWR: .WORD AUSWR ::USER SWITCHES  
\$CPUOP: .WORD ACPUGP ::CPU TYPE, OPTIONS  
: \*  
: \* BIT 15-11=CPU TYPE  
: \* 11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05  
: \* 11/70=06, PDQ=07, Q=10  
: \*  
: \* BIT 10=REAL TIME CLOCK  
: \* BIT 9=FLCATING POINT PROCESSOR  
: \*  
: \* BIT 8=MEMORY MANAGEMENT  
\$MAMS1: .BYTE AMAMS1 ::HIGH ADDRESS, M.S. BYTE

```

1144 001347 000 SMTYP1: .BYTE AMTYP1 ;;MEM. TYPE, BLK#1
1145          000          * MEM. TYPE BYTE -- (HIGH BYTE)
1146          000          * 900 NSEC CORE=001
1147          000          * 300 NSEC BIPOLAR=002
1148          000          * 500 NSEC MOS=003
1149 001350 000000 $MADR1: .WORD AMADR1 ;;HIGH ADDRESS, BLK#1
1150          000          * MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
1151 001352 000 $MAMS2: .BYTE AMAMS2 ;;HIGH ADDRESS, M.S. BYTE
1152 001353 000 SMTYP2: .BYTE AMTYP2 ;;MEM. TYPE, BLK#2
1153 001354 000000 $MADR2: .WORD AMADR2 ;;MEM. LAST ADDRESS, BLK#2
1154 001356 000 $MAMS3: .BYTE AMAMS3 ;;HIGH ADDRESS, M.S. BYTE
1155 001357 000 SMTYP3: .BYTE AMTYP3 ;;MEM. TYPE, BLK#3
1156 001360 000000 $MADR3: .WORD AMADR3 ;;MEM. LAST ADDRESS, BLK#3
1157 001362 000 $MAMS4: .BYTE AMAMS4 ;;HIGH ADDRESS, M.S. BYTE
1158 001363 000 SMTYP4: .BYTE AMTYP4 ;;MEM. TYPE, BLK#4
1159 001364 000000 $MADR4: .WORD AMADR4 ;;MEM. LAST ADDRESS, BLK#4
1160 001366 000000 $VECT1: .WORD AVECT1 ;;INTERRUPT VECTOR#1, BUS PRIORITY#1
1161 001370 000000 $VECT2: .WORD AVECT2 ;;INTERRUPT VECTOR#2, BUS PRIORITY#2
1162 001372 000000 $BASE: .WORD ABASE ;;BASE ADDRESS OF EQUIPMENT UNDER TEST
1163 001374 000000 $DEVN: .WORD ADEVN ;;DEVICE MAP
1164 001376 000000 $CDW1: .WORD ACDW1 ;;CONTROLLER DESCRIPTION WORD#1
1165 001400 000000 $CDW2: .WORD ACDW2 ;;CONTROLLER DESCRIPTION WORD#2
1166 001402 000000 $DDW0: .WORD ADDW0 ;;DEVICE DESCRIPTOR WORD#0
1167 001404 000000 $DDW1: .WORD ADDW1 ;;DEVICE DESCRIPTOR WORD#1
1168 001406 000000 $DDW2: .WORD ADDW2 ;;DEVICE DESCRIPTOR WORD#2
1169 001410 000000 $DDW3: .WORD ADDW3 ;;DEVICE DESCRIPTOR WORD#3
1170 001412 000000 $DDW4: .WORD ADDW4 ;;DEVICE DESCRIPTOR WORD#4
1171 001414 000000 $DDW5: .WORD ADDW5 ;;DEVICE DESCRIPTOR WORD#5
1172 001416 000000 $DDW6: .WORD ADDW6 ;;DEVICE DESCRIPTOR WORD#6
1173 001420 000000 $DDW7: .WORD ADDW7 ;;DEVICE DESCRIPTOR WORD#7
1174 001422 000000 $DDW8: .WORD ADDW8 ;;DEVICE DESCRIPTOR WORD#8
1175 001424 000000 $DDW9: .WORD ADDW9 ;;DEVICE DESCRIPTOR WORD#9
1176 001426 000000 $DDW10: .WORD ADDW10 ;;DEVICE DESCRIPTOR WORD#10
1177 001430 000000 $DDW11: .WORD ADDW11 ;;DEVICE DESCRIPTOR WORD#11
1178 001432 000000 $DDW12: .WORD ADDW12 ;;DEVICE DESCRIPTOR WORD#12
1179 001434 000000 $DDW13: .WORD ADDW13 ;;DEVICE DESCRIPTOR WORD#13
1180 001436 000000 $DDW14: .WORD ADDW14 ;;DEVICE DESCRIPTOR WORD#14
1181 001440 000000 $DDW15: .WORD ADDW15 ;;DEVICE DESCRIPTOR WORD#15
1182
1183
1184 001442 SE*END:
1185

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1186  
1187  
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.SETTL ERROR POINTER TABLE

: \*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.  
: \*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN  
: \*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.  
: \*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).  
: \*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

: \* EM ::POINTS TO THE ERROR MESSAGE  
: \* DH ::POINTS TO THE DATA HEADER  
: \* DT ::POINTS TO THE DATA  
: \* DF ::POINTS TO THE DATA FORMAT

\$ERRPTB:  
;ITEM 1 .WORD EM1,DH1,DT1,DF1  
;ITEM 2 .WORD EM2,DH2,DT2,DF2  
;ITEM 3 .WORD EM3,DH3,DT3,DF3  
;ITEM 4 .WORD EM4,DH4,DT4,DF4  
;ITEM 5 .WORD EM5,DH5,DT5,DF5  
;ITEM 6 .WORD EM6,DH6,DT6,DF6  
;ITEM 7 .WORD EM7,DH7,DT7,DF7  
;ITEM 10 .WORD EM10,DH10,DT10,DF10  
;ITEM 11 .WORD EM11,DH11,DT11,DF11  
;ITEM 12 .WORD EM12,DH12,DT12,DF12  
;ITEM 13 .WORD EM13,DH13,DT13,DF13  
;ITEM 14 .WORD EM14,DH14,DT14,DF14  
;ITEM 15 .WORD EM15,DH15,DT15,DF15  
;ITEM 16 .WORD EM16,DH16,DT16,DF16

001442  
001442 043025 063250 067664  
001450 066611  
001452 043062 063340 067706  
001460 066621  
001462 043126 063433 067730  
001470 066631  
001472 043173 063524 067730  
001500 066631  
001502 043233 063620 067752  
001510 066641  
001512 043267 063620 070004  
001520 066641  
001522 043321 063620 070004  
001530 066641  
001532 043233 063620 070004  
001540 066641  
001542 043354 063620 070004  
001550 066641  
001552 000000 000000 070026  
001560 066655  
001562 000000 000000 070114  
001570 066707  
001572 043435 063620 070004  
001600 066641  
001602 043560 063620 070004  
001610 066641  
001612 043703 063660 070146



1242	001620	066723				
1243					: ITEM 17	
1244	001622	043754	063740	067730	.WORD	EM17, DH17, DT17, DF17
1245	001630	066631				
1246					: ITEM 20	
1247	001632	044207	064030	070166	.WORD	EM20, DH20, DT20, DF20
1248	001640	066732				
1249					: ITEM 21	
1250	001642	044365	063620	070210	.WORD	EM21, DH21, DT21, DF21
1251	001650	066742				
1252					: ITEM 22	
1253	001652	044516	064116	070222	.WORD	EM22, DH22, DT22, DF22
1254	001660	066746				
1255					: ITEM 23	
1256	001662	044516	064153	070250	.WORD	EM23, DH23, DT23, DF23
1257	001670	066760				
1258					: ITEM 24	
1259	001672	044516	064311	070272	.WORD	EM24, DH24, DT24, DF24
1260	001700	066770				
1261					: ITEM 25	
1262	001702	044603	064450	070316	.WORD	EM25, DH25, DT25, DF25
1263	001710	066777				
1264					: ITEM 26	
1265	001712	044716	064512	070366	.WORD	EM26, DH26, DT26, DF26
1266	001720	067023				
1267					: ITEM 27	
1268	001722	044716	064512	070442	.WORD	EM27, DH27, DT27, DF27
1269	001730	067050				
1270					: ITEM 30	
1271	001732	044764	000000	070504	.WORD	EM30, DH30, DT30, DF30
1272	001740	067070				
1273					: ITEM 31	
1274	001742	045036	064512	070366	.WORD	EM31, DH31, DT31, DF31
1275	001750	067023				
1276					: ITEM 32	
1277	001752	045036	064512	070442	.WORD	EM32, DH32, DT32, DF32
1278	001760	067050				
1279					: ITEM 33	
1280	001762	045104	064600	070536	.WORD	EM33, DH33, DT33, DF33
1281	001770	067104				
1282					: ITEM 34	
1283	001772	045145	064600	070614	.WORD	EM34, DH34, DT34, DF34
1284	002000	067132				
1285					: ITEM 35	
1286	002002	045247	064600	070614	.WORD	EM35, DH35, DT35, DF35
1287	002010	067132				
1288					: ITEM 36	
1289	002012	045351	064600	070614	.WORD	EM36, DH36, DT36, DF36
1290	002020	067132				
1291					: ITEM 37	
1292	002022	045452	064600	070614	.WORD	EM37, DH37, DT37, DF37
1293	002030	067132				
1294					: ITEM 40	
1295	002032	045553	064600	070536	.WORD	EM40, DH40, DT40, DF40
1296	002040	067156				
1297					: ITEM 41	

1298	002042	045724	000000	070666	.WORD	EM41,DM41,DT41,DF41
1299	002050	067204				
1300					;ITEM 42	
1301	002052	045761	064703	070720	.WORD	EM42,DM42,DT42,DF42
1302	002060	067220				
1303					;ITEM 43	
1304	002062	046102	064703	070720	.WORD	EM43,DM43,DT43,DF43
1305	002070	067220				
1306					;ITEM 44	
1307	002072	046223	000000	070776	.WORD	EM44,DM44,DT44,DF44
1308	002100	067246				
1309					;ITEM 45	
1310	002102	046223	065005	071046	.WORD	EM45,DM45,DT45,DF45
1311	002110	067271				
1312					;ITEM 46	
1313	002112	046266	065024	071122	.WORD	EM46,DM46,DT46,DF46
1314	002120	067316				
1315					;ITEM 47	
1316	002122	046344	065005	071210	.WORD	EM47,DM47,DT47,DF47
1317	002130	067350				
1318					;ITEM 50	
1319	002132	046462	065050	070614	.WORD	EM50,DM50,DT50,DF50
1320	002140	067365				
1321					;ITEM 51	
1322	002142	046560	065050	071242	.WORD	EM51,DM51,DT51,DF51
1323	002150	067411				
1324					;ITEM 52	
1325	002152	046621	063620	071210	.WORD	EM52,DM52,DT52,DF52
1326	002160	067350				
1327					;ITEM 53	
1328	002162	046742	064512	071300	.WORD	EM53,DM53,DT53,DF53
1329	002170	067427				
1330					;ITEM 54	
1331	002172	047137	065122	071320	.WORD	EM54,DM54,DT54,DF54
1332	002200	067436				
1333					;ITEM 55	
1334	002202	047203	063620	071210	.WORD	EM55,DM55,DT55,DF55
1335	002210	067350				
1336					;ITEM 56	
1337	002212	047324	064512	071300	.WORD	EM56,DM56,DT56,DF56
1338	002220	067427				
1339					;ITEM 57	
1340	002222	047521	065122	071320	.WORD	EM57,DM57,DT57,DF57
1341	002230	067436				
1342					;ITEM 60	
1343	002232	047565	064512	071300	.WORD	EM60,DM60,DT60,DF60
1344	002240	067427				
1345					;ITEM 61	
1346	002242	047762	065122	071320	.WORD	EM61,DM61,DT61,DF61
1347	002250	067436				
1348					;ITEM 62	
1349	002252	050026	065122	071320	.WORD	EM62,DM62,DT62,DF62
1350	002260	067436				
1351					;ITEM 63	
1352	002262	050220	065122	071320	.WORD	EM63,DM63,DT63,DF63
1353	002270	067436				

1374	002302	050412	065232	071356	ITEM 64	.WORD	EM64,DM64,DT64,DF64
1375	002302	067454					
1376	002302	050412	065163	071356	: ITEM 65	.WORD	EM65,DM65,DT65,DF65
1377	002302	067454					
1378	002302	050546	065122	071320	: ITEM 66	.WORD	EM66,DM66,DT66,DF66
1379	002302	067436					
1380	002322	050611	063620	070210	: ITEM 67	.WORD	EM67,DM67,DT67,DF67
1381	002330	066742					
1382	002332	051042	063620	071376	: ITEM 70	.WORD	EM70,DM70,DT70,DF70
1383	002342	067463					
1384	002342	051165	064450	071376	: ITEM 71	.WORD	EM71,DM71,DT71,DF71
1385	002350	067463					
1386	002352	051267	064512	071444	: ITEM 72	.WORD	EM72,DM72,DT72,DF72
1387	002360	067505					
1388	002362	051343	065122	071320	: ITEM 73	.WORD	EM73,DM73,DT73,DF73
1389	002370	067436					
1390	002372	051403	063620	070210	: ITEM 74	.WORD	EM74,DM74,DT74,DF74
1391	002400	066742					
1392	002402	051634	063620	071376	: ITEM 75	.WORD	EM75,DM75,DT75,DF75
1393	002410	067463					
1394	002412	051757	064450	071376	: ITEM 76	.WORD	EM76,DM76,DT76,DF76
1395	002420	067463					
1396	002422	052061	064512	071444	: ITEM 77	.WORD	EM77,DM77,DT77,DF77
1397	002430	067505					
1398	002432	052135	065122	071320	: ITEM 100	.WORD	EM100,DM100,DT100,DF100
1399	002440	067436					
1400	002442	052175	063620	071376	: ITEM 101	.WORD	EM101,DM101,DT101,DF101
1401	002450	067463					
1402	002452	052321	064512	071376	: ITEM 102	.WORD	EM102,DM102,DT102,DF102
1403	002460	067505					
1404	002462	052373	064450	071376	: ITEM 103	.WORD	EM103,DM103,DT103,DF103
1405	002470	067463					
1406	002472	052476	065122	071320	: ITEM 104	.WORD	EM104,DM104,DT104,DF104
1407	002500	067436					
1408	002502	052537	063620	071376	: ITEM 105	.WORD	EM105,DM105,DT105,DF105
1409	002510	067463					
1410	002512	052664	064512	071444	: ITEM 106	.WORD	EM106,DM106,DT106,DF106

002522  
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002500	067505								
002522	052737	064450	071376	:ITEM 107	.WORD	EM107,DM107,DT107,DF107			
002530	067463								
002532	053043	065122	071320	:ITEM 110	.WORD	EM110,DM110,DT110,DF110			
002540	067436								
002542	053105	064450	071464	:ITEM 111	.WORD	EM111,DM111,DT111,DF111			
002550	067514								
002552	053105	065320	071464	:ITEM 112	.WORD	EM112,DM112,DT112,DF112			
002560	067514								
002562	053207	064450	071464	:ITEM 113	.WORD	EM113,DM113,DT113,DF113			
002570	067514								
002572	053207	065320	071464	:ITEM 114	.WORD	EM114,DM114,DT114,DF114			
002600	067514								
002602	053105	065537	071464	:ITEM 115	.WORD	EM115,DM115,DT115,DF115			
002610	067514								
002612	053207	065537	071464	:ITEM 116	.WORD	EM116,DM116,DT116,DF116			
002620	067514								
002622	053311	063740	067730	:ITEM 117	.WORD	EM117,DM117,DT117,DF117			
002630	066631								
002632	053445	066023	067730	:ITEM 120	.WORD	EM120,DM120,DT120,DF120			
002640	066631								
002642	053601	063620	071210	:ITEM 121	.WORD	EM121,DM121,DT121,DF121			
002650	067350								
002652	053720	065050	070614	:ITEM 122	.WORD	EM122,DM122,DT122,DF122			
002660	067365								
002662	054017	065050	071242	:ITEM 123	.WORD	EM123,DM123,DT123,DF123			
002670	067411								
002672	054060	063740	071476	:ITEM 124	.WORD	EM124,DM124,DT124,DF124			
002700	067520								
002702	054153	063740	071476	:ITEM 125	.WORD	EM125,DM125,DT125,DF125			
002710	067520								
002712	054243	063620	071464	:ITEM 126	.WORD	EM126,DM126,DT126,DF126			
002720	067514								
002722	054452	065122	071464	:ITEM 127	.WORD	EM127,DM127,DT127,DF127			
002730	067514								
002732	054665	066023	067730	:ITEM 130	.WORD	EM130,DM130,DT130,DF130			
002740	066631								
				:ITEM 131					

1466	002742	054765	065122	071562	.WORD	EM131,DH131,DT131,DF131
1467	002750	067551			:ITEM 132	
1468	002752	055025	065122	071562	.WORD	EM132,DH132,DT132,DF132
1469	002760	067551			:ITEM 133	
1470	002762	055065	066113	071624	.WORD	EM133,DH133,DT133,DF133
1471	002770	067571			:ITEM 134	
1472	002772	055124	066113	071624	.WORD	EM134,DH134,DT134,DF134
1473	003000	067571			:ITEM 135	
1474	003002	055163	066113	071624	.WORD	EM135,DH135,DT135,DF135
1475	003010	067571			:ITEM 136	
1476	003012	055222	066113	071624	.WORD	EM136,DH136,DT136,DF136
1477	003020	067571			:ITEM 137	
1478	003022	055065	066223	071676	.WORD	EM137,DH137,DT137,DF137
1479	003030	067615			:ITEM 140	
1480	003032	055124	066223	071676	.WORD	EM140,DH140,DT140,DF140
1481	003040	067615			:ITEM 141	
1482	003042	055163	066223	071676	.WORD	EM141,DH141,DT141,DF141
1483	003050	067615			:ITEM 142	
1484	003052	055222	066223	071676	.WORD	EM142,DH142,DT142,DF142
1485	003060	067615			:ITEM 143	
1486	003062	055261	066113	071624	.WORD	EM143,DH143,DT143,DF143
1487	003070	067571			:ITEM 144	
1488	003072	055314	066113	071624	.WORD	EM144,DH144,DT144,DF144
1489	003100	067571			:ITEM 145	
1490	003102	055261	066223	071676	.WORD	EM145,DH145,DT145,DF145
1491	003110	067615			:ITEM 146	
1492	003112	055314	066223	071676	.WORD	EM146,DH146,DT146,DF146
1493	003120	067615			:ITEM 147	
1494	003122	055347	065122	071624	.WORD	EM147,DH147,DT147,DF147
1495	003130	067571			:ITEM 150	
1496	003132	055347	066413	071624	.WORD	EM150,DH150,DT150,DF150
1497	003140	067571			:ITEM 151	
1498	003142	055347	066223	071676	.WORD	EM151,DH151,DT151,DF151
1499	003150	067615			:ITEM 152	
1500	003152	055401	066113	071624	.WORD	EM152,DH152,DT152,DF152
1501	003160	067571			:ITEM 153	
1502	003162	055401	066223	071676	.WORD	EM153,DH153,DT153,DF153
1503	003170	067615				

1522					: ITEM 154	
1523	003172	055433	066504	071716	.WORD	EM154,DM154,DT154,DF154
1524	003200	067625				
1525					: ITEM 155	
1526	003202	055665	066504	071716	.WORD	EM155,DM155,DT155,DF155
1527	003210	067625				
1528					: ITEM 156	
1529	003212	056120	065122	071624	.WORD	EM156,DM156,DT156,DF156
1530	003220	067571				
1531					: ITEM 157	
1532	003222	056335	065122	071624	.WORD	EM157,DM157,DT157,DF157
1533	003230	067571				
1534					: ITEM 160	
1535	003232	056554	065122	071624	.WORD	EM160,DM160,DT160,DF160
1536	003240	067571				
1537					: ITEM 161	
1538	003242	056761	065122	071624	.WORD	EM161,DM161,DT161,DF161
1539	003250	067631				
1540					: ITEM 162	
1541	003252	057166	065122	071624	.WORD	EM162,DM162,DT162,DF162
1542	003260	067571				
1543					: ITEM 163	
1544	003262	057233	065122	071624	.WORD	EM163,DM163,DT163,DF163
1545	003270	067631				
1546					: ITEM 164	
1547	003272	057300	063740	067730	.WORD	EM164,DM164,DT164,DF164
1548	003300	066631				
1549					: ITEM 165	
1550	003302	057345	063740	067730	.WORD	EM165,DM165,DT165,DF165
1551	003310	066631				
1552					: ITEM 166	
1553	003312	057412	065122	071624	.WORD	EM166,DM166,DT166,DF166
1554	003320	067571				
1555					: ITEM 167	
1556	003322	057522	065122	071624	.WORD	EM167,DM167,DT167,DF167
1557	003330	067571				
1558					: ITEM 170	
1559	003332	057761	065122	071624	.WORD	EM170,DM170,DT170,DF170
1560	003340	067631				
1561					: ITEM 171	
1562	003342	060071	065122	071624	.WORD	EM171,DM171,DT171,DF171
1563	003350	067631				
1564					: ITEM 172	
1565	003352	060330	065122	071624	.WORD	EM172,DM172,DT172,DF172
1566	003360	067571				
1567					: ITEM 173	
1568	003362	060567	065122	071624	.WORD	EM173,DM173,DT173,DF173
1569	003370	067571				
1570					: ITEM 174	
1571	003372	061026	065122	071624	.WORD	EM174,DM174,DT174,DF174
1572	003400	067631				
1573					: ITEM 175	
1574	003402	061265	065122	071624	.WORD	EM175,DM175,DT175,DF175
1575	003410	067631				
1576					: ITEM 176	
1577	003412	061524	065122	071624	.WORD	EM176,DM176,DT176,DF176

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1578	003420	067571				
1579					: ITEM 177	
1580	003422	061661	065122	071624	.WORD	EM177,DH177,DT177,DF177
1581	003430	067571				
1582					: ITEM 200	
1583	003432	062016	065122	071624	.WORD	EM200,DH200,DT200,DF200
1584	003440	067571				
1585					: ITEM 201	
1586	003442	062153	065122	071624	.WORD	EM201,DH201,DT201,DF201
1587	003450	067571				
1588					: ITEM 202	
1589	003452	062310	065122	071624	.WORD	EM202,DH202,DT202,DF202
1590	003460	067571				
1591					: ITEM 203	
1592	003462	062445	065122	071624	.WORD	EM203,DH203,DT203,DF203
1593	003470	067571				
1594					: ITEM 204	
1595	003472	062602	065122	071624	.WORD	EM204,DH204,DT204,DF204
1596	003500	067571				
1597					: ITEM 205	
1598	003502	062737	063740	067730	.WORD	EM205,DH205,DT205,DF205
1599	003510	066631				
1600					: ITEM 206	
1601	003512	063004	065122	071624	.WORD	EM206,DH206,DT206,DF206
1602	003520	067571				
1603					: ITEM 207	
1604	003522	063051	065122	071624	.WORD	EM207,DH207,DT207,DF207
1605	003530	067571				
1606					: ITEM 210	
1607	003532	063173	065122	071624	.WORD	EM210,DH210,DT210,DF210
1608	003540	067571				
1609					: ITEM 211	
1610	003542	043233	066544	071730	.WORD	EM211,DH211,DT211,DF211
1611	003550	067655				
1612					: ITEM 212	
1613	003552	043267	063620	071746	.WORD	EM212,DH212,DT212,DF212
1614	003560	067655				
1615					: ITEM 213	
1616	003562	043321	063620	071746	.WORD	EM213,DH213,DT213,DF213
1617	003570	067655				

```

1618
1619
1620 .SBTTL ACT11 HOOKS
1621
1622 ::*****
1623 ;HOOKS REQUIRED BY ACT11
1624         $SVPC=          ;SAVE PC
1625         =46
1626 000046 034770 $SENDAD      ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOP
1627         =52
1628 000052 000000 .WORD 0      ;;2)SET LOC.52 TO ZERO
1629         = $SVPC        ;; RESTORE PC
1630 .SBTTL APT PARAMETER BLOCK
1631
1632 ::*****
1633 ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
1634

```

1634  
1635 003572  
1636 000024  
1637 000024 000200  
1638 000044  
1639 000044 003572  
1640 003572  
1641  
1642  
1643  
1644  
1645 003572  
1646 003572 000000  
1647 003574 001316  
1648 003576 000000  
1649 003600 000040  
1650 003602 000000  
1651 003604 000052  
1652  
1653  
1654 003606  
1655  
1656  
1657 003606 012706 001100  
1658 003612 005026  
1659 003614 022706 001140  
1660 003620 001374  
1661 003622 012706 001100  
1662  
1663 003626 012737 035050 000020  
1664 003634 012737 000340 000022  
1665 003642 012737 035330 000030  
1666 003650 012737 000340 000032  
1667 003656 012737 037276 000034  
1668 003664 012737 000340 000036  
1669 003672 012737 037362 000024  
1670 003700 012737 000340 000026  
1671 003706 016767 030700 030670  
1672 003714 005067 175362  
1673 003720 005067 175360  
1674 003724 112767 000001 175163  
1675  
1676  
1677 003732 012737 035034 000014  
1678 003740 012737 000340 000016  
1679 003746 012767 000002 031060  
1680 003754 012737 004002 000010  
1681 003762 005046  
1682 003764 012746 003772  
1683 003770 000006  
1684 003772 012767 000006 031034 64\$:  
1685 004000 000402  
1686 004002 062706 000010 65\$:  
1687 004006 012737 000012 000010 66\$:  
1688 004014 005067 031022  
1689 004020 012767 004020 175060

```
*****
.SX=.      ::SAVE CURRENT LOCATION
.=24      ::SET POWER FAIL TO POINT TO START OF PROGRAM
200       ::FOR APT START UP
.=44      ::POINT TO APT INDIRECT ADDRESS PNTR.
$APTHDR   ::POINT TO APT HEADER BLOCK
.=.SX     ::RESET LOCATION COUNTER
*****
:SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
:INTERFACE SPEC.

$APTHDR:
$SHFTS: .WORD 0      ::TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
$MBAOR: .WORD $MAIL  ::ADDRESS OF APT MAILBOX (BITS 0-15)
$STMT:  .WORD 10     ::RUN TIM OF LONGEST TEST
$PASTM: .WORD 40     ::RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
$UNITM: .WORD 0      ::ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
        .WORD $ETENC-$MAIL/2 ::LENGTH MAILBOX-ETABLE(WORDS)

START:
.SBTTL INITIALIZE THE COMMON TAGS
::CLEAR THE COMMON TAGS ($CMTAG) AREA
MOV      $CMTAG,R6      ::FIRST LOCATION TO BE CLEARED
CLR      (R6)+          ::CLEAR MEMORY LOCATION
CMP      $SWR,R6      ::DONE?
BNE     -.6            ::LOOP BACK IF NO
MOV      $STACK,SP     ::SETUP THE STACK POINTER
::INITIALIZE A FEW VECTORS
MOV      $SSCOPE,$IOTVEC ::IOT VECTOR FOR SCOPE ROUTINE
MOV      $340,$IOTVEC+2 ::LEVEL 7
MOV      $ERROR,$EMTVEC  ::EMT VECTOR FOR ERROR ROUTINE
MOV      $340,$EMTVEC+2 ::LEVEL 7
MOV      $STRAP,$TRAPVEC  ::TRAP VECTOR FOR TRAP CALLS
MOV      $340,$TRAPVEC+2 ::LEVEL 7
MOV      $SPWRDN,$PWRVEC  ::POWER FAILURE VECTOR
MOV      $340,$PWRVEC+2  ::LEVEL 7
MOV      $ENDPT,$SEOPCT  ::SETUP END-OF-PROGRAM COUNTER
CLR      $TIMES          ::INITIALIZE NUMBER OF ITERATIONS
CLR      $ESCAPE        ::CLEAR THE ESCAPE ON ERROR ADDRESS
MOV      $1,$SERMAX     ::ALLOW ONE ERROR PER TEST
::INITIALIZE THE "T-BIT" TRAP VECTOR. THEN LOAD LOCATION "$RTRN". IN
::THE "END-OF-PASS" ($EOP) ROUTINE, WITH A "RTI" OR "RTT".
MOV      $RTRN,$TBITVEC  ::SET "T" BIT VECTOR TO $RTRN
MOV      $340,$TBITVEC+2 ::LEVEL 7
MOV      $RTI,$RTRN      ::SET $RTRN TO A RTI
MOV      $65$,$RESVEC    ::TRY TO DO A RTT
CLR      -(SP)           ::DUMMY PS
MOV      $64$,-(SP)     ::AND PC
RTT      ::TRY THE RTT
MOV      $RTT,$RTRN     ::RTT IS LEGAL--SET $RTRN TO A RTT
BR      66$
MOV      $10,SP        ::RTT ILLEGAL--CLEAN OFF THE STACK
MOV      $RESVEC+2,$RESVEC ::RESTORE TRAP CATCHER
CLR      $TBIT         ::CLEAR "T" BIT SWITCH
MOV      $.,$LPAOR      ::INITIALIZE THE LOOP ADDRESS FOR SCOPE
```



```

1690 004026 012767 004026 175054      MOV      #, $LPERR      ;; SETUP THE ERROR LOOP ADDRESS
1691      ;; SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
1692      ;; EQUAL TO A "-1". SETUP FOR A SOFTWARE SWITCH REGISTER.
1693 004034 013746 000004      MOV      2,ERRVEC,-(SP) ;; SAVE ERROR VECTOR
1694 004040 012737 004074 000004      MOV      #67$,2,ERRVEC ;; SET UP ERROR VECTOR
1695 004046 012767 177570 175064      MOV      #DSWR,SWR      ;; SETUP FOR A HARDWARE SWICH REGISTER
1696 004054 012767 177570 175060      MOV      #DDISP,DISPLAY ;; AND A HARDWARE DISPLAY REGISTER
1697 004062 022777 177777 175050      CMP      #-1,2SWR      ;; TRY TO REFERENCE HARDWARE SWR
1698 004070 001012      BNE      69$           ;; BRANCH IF NO TIMEOUT TRAP OCCURRED
1699      ;; AND THE HARDWARE SWR IS NOT = -1
1700 004072 000403      BR      68$           ;; BRANCH IF NO TIMEOUT
1701 004074 012716 004102 67$: MOV      #68$, (SP)    ;; SET UP FOR TRAP RETURN
1702 004100 000002      RTI
1703 004102 012767 000176 175030 68$: MOV      #SWREG,SWR   ;; POINT TO SOFTWARE SWR
1704 004110 012767 000174 175024      MOV      #DISPREG,DISPLAY
1705 004116 012637 000004 69$: MOV      (SP)+,2,ERRVEC ;; RESTORE ERROR VECTOR
1706
1707 004122 005067 175176      CLR      $PASS        ;; CLEAR PASS COUNT
1708 004126 132767 000200 175203      BITB    #APTSIZE,$ENVM ;; TEST USER SIZE UNDER APT
1709 004134 001403      BEQ      70$          ;; YES, USE NON-APT SWITCH
1710 004136 012767 001340 174774      MOV      #SSWREG,SWR  ;; NO, USE APT SWITCH REGISTER
1711 004144
1712
1713      .SBTTL  TYPE PROGRAM NAME
1714 004144 005227 177777      ;; TYPE THE NAME OF THE PROGRAM IF FIRST PASS
1715 004150 001055      INC      #-1          ;; FIRST TIME?
1716 004152 022737 034770 000042      BNE      71$          ;; BRANCH IF NO
1717 004160 001451      CMP      #SENDAD,2*42 ;; ACT-11?
1718 004162 104401 004230      BEQ      71$          ;; BRANCH IF YES
1719      .SBTTL  TYPE 72$          ;; TYPE ASCIZ STRING
1720 004166 005737 000042      GET VALUE FOR SOFTWARE SWITCH REGISTER
1721 004172 001012      TST      2*42        ;; ARE WE RUNNING UNDER XXDP/ACT?
1722 004174 126727 175136 000001      BNE      73$          ;; BRANCH IF YES
1723 004202 001406      CMPB    $ENV,#1      ;; ARE WE RUNNING UNDER APT?
1724 004204 026727 174730 000176      BEQ      73$          ;; BRANCH IF YES
1725 004212 001005      CMP      SWR,#SWREG  ;; SOFTWARE SWITCH REG SELECTED?
1726 004214 104405      BNE      74$          ;; BRANCH IF NO
1727 004216 000453      GTSWR           ;; GET SOFT-SWR SETTINGS
1728 004220 112767 000001 174706 73$: MOV      #1,$AUTOB   ;; SET AUTO-MODE INDICATOR
1729 004226      74$:
1730 004226 000426      BR      71$          ;; GET OVER THE ASCIZ
1731      ;; 72$: .ASCIZ <CRLF>*DFPRA, FPII-A 11/34 FPP DIAGNOSTIC PART 1* <CRLF>
1732 004304      71$:
1733
1734 004304      LOOP:

```

```

1735
1736
1737
1738
1739
1740      ;;*****
1741      ;;TEST 1      LDFPS, STFPS AND DATA PATHS TEST
1742      ;;
1743      ;;THIS IS A TEST OF THE LDFPS (LOAD FLOATING POINT STATUS) AND STFPS
1744      ;;(STORE FLOATING POINT STATUS) INSTRUCTIONS. A COUNT PATTERN IS GENERATED
1745      ;;AND RUN THROUGH THE FLOATING POINT STATUS REGISTER.

```

# H03

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: *THIS WILL TEST THE 16-BIT TRI STATE BUS WHICH CONNECTS THE CPU
: *WITH THE FPP AND ALSO RUNS INTERNALLY WITHIN THE FPP. ONLY DMC AND
: *SMO ARE USED.
: *NOTE THAT A MASK MUST BE USED BECAUSE SOME OF THE FPS BITS CANNOT
: *BE SET.
: *
: *ONLY THE FIRST FIVE ERRORS WILL BE REPORTED INDIVIDUALLY.
: *THIS IS TO PREVENT LOCKING OUT THE COMPLETION OF THE TEST BECAUSE
: *OF VIRTUALLY ENDLESS NUMBER OF ERRORS. ONLY FIVE INDIVIDUAL ERRORS
: *WILL BE REPORTED THEN THE TEST WILL BE COMPLETED AND AN ERROR
: *SUMMARY GIVEN (SEE NOTE BELOW).
: *
: *NOTE THAT THIS TEST KEEPS A DYNAMIC RECORD OF THE LOGICAL 'AND' AND 'OR'
: *OF THE FAILING DATA PATTERNS. THESE CAN BE VERY USEFUL IN DETERMINING
: *STUCK BITS. IF THE USER HAS THE INHIBIT ERROR TYPE OUT SWITCH (SWR13)
: *OFF, THEN THE USER WILL RECEIVE EACH INDIVIDUAL ERROR MESSAGE PLUS
: *AN ERROR SUMMARY AT THE END OF THE TEST. INHIBITING ERROR PRINT OUT
: *WILL INHIBIT ERROR SUMMARY PRINT OUT, EXCEPT IN THE CASE DESCRIBED BELOW.
: *TO GET JUST THE ERROR SUMMARY WITH NO INDIVIDUAL ERROR REPORTS,
: *SET SWITCH REGISTER BIT13 AND SWITCH REGISTER BIT7 BOTH ON.
: *
: *****
  
```

```

1768 004304 000004
1769 004306 005037 004560
1770 004312 104413
1771 004314 012700 177777
1772 004320 012737 004562 000244
1773 004326 012737 004574 000010
1774 004334 005002
1775 004336 005102
1776 004340 005003
1777 004342 012737 004626 000004
1781 004350
1782 004350 010004
1783 004352 042704 030020
1784 004356 170104
1786 004360 012701 177777
1787 004364 170201
1788 004366 012737 040200 000244
1789 004374 010004
1790 004376 042704 030020
1791 004402 012737 040232 000004
1792 004410 012737 040250 000010
1793 004416 020401
1795 004420 001002
1797 004422 077026
1798 004424 000425
1800 004426 005237 004560
1801 004432 050003
  
```

```

TEST1: SCOPE
        CLR      0#AERFLG
        LPERR
        MOV      #-1,R0
        MOV      #AERR1,0#FPVECT
        MOV      #AERR2,0#10
        CLR      R2
        COM      R2
        CLR      R3
        MOV      #AERR3,0#ERRVECT
        ;SET UP THE LOOP ON ERROR ADDRESS.
        ;INITIALIZE THE COUNT PATTERN.
        ;SET UP FOR UNABLE TO DECODE
        ;FPP INSTRUCTION TRAP TC 244 OR 10.
        ;R2 IS THE 'AND' OF BAD DATA.
        ;R3 IS THE 'OR' OF BAD DATA.
        ;IF EITHER INSTRUCTION
        ;FAILS TO GO THROUGH THE
        ;CORRECT SRC OR DST MODE AN
        ;ODC ADDRESS TRAP WILL OCCUR.

A1:
A11:   MOV      R0,R4
        BIC      #30020,R4
        LDFPS   R4
        ;TEST INSTRUCTION.

A12:   MOV      #-1,R1
        STFPS   R1
        MOV      #FPSPUR,0#FPVECT
        MOV      R0,R4
        BIC      #30020,R4
        MOV      #CPSPUR,0#ERRVECT
        MOV      #CPTWO,0#10
        CMP     R4,R1
        ;COMPARE DATA EXPECTED WITH
        ;THE DATA READ.
        ;IF NOT EQUAL GO REPORT ERROR.

A2:    SOB     R0,A1
        BR      A5
        ;OTHERWISE DECREMENT COUNT PATTERN
        ;UNTIL IT IS ZERO.

A3:    INC     0#AERFLG
        BIS     R0,R3
        ;RECORD ERROR.
        ;COMPUTE 'OR' OF FAILING PATTERNS.
  
```

```

1802 004434 010005      MOV      R0,R5      ;COMPUTE 'AND' OF FAILING PATTERNS.
1803 004436 005105      COM      R5
1804 004440 040502      BIC      R5,R2
1805
1806 004442 022737 000005 004560      CMP      #5,@AERFLG  ;SEE IF MORE THAN 5 ERRORS HAVE
1807 004450 103412      BLC      A05        ;OCCURRED. BR IF YES.
1808
1809
1810 004452 012737 004350 001236      MOV      #A1,@STMP2  ;OTHERWISE
1811 004460 010037 001240      MOV      R0,@STMP3  ;REPORT ERROR.
1812 004464 010137 001242      MOV      R1,@STMP4
1813 004470 010437 001244      MOV      R4,@STMP5
1814 004474 104001      A4:      ERROR      1
1815
1816 004476 000751      A05:     BR        A2      ;CONTINUE TESTING.
1817
1818 004500 005737 004560      A5:      TST      @AERFLG  ;SEE IF ANY ERRORS OCCURRED.
1819 004504 001471      BEQ      ADONE      ;IF NOT GO TO NEXT TEST.
1820 004506 032777 020000 174424      BIT      #SW13,@SWR  ;OTHERWISE SEE IF A SUMMARY
1821 004514 001404      BEQ      A6         ;SHOULD BE TYPED.
1822 004516 032777 000200 174414      BIT      #SW7,@SWR
1823 004524 001461      BEQ      ADONE
1824
1825 004526      A6:      ;TYPE ERROR SUMMARY.
1826 004526 010237 001236      MOV      R2,@STMP2
1827 004532 010337 001240      MOV      R3,@STMP3
1828 004536 012737 004552 001116      MOV      #A7,@SERRPC
1829 004544 112737 000002 001114      MOVB     #2,@SITEMB
1830 004552 004737 037546      JSR      PC,@ERTYPE
1831 004556 000444      BR      ADONE
1832
1833 004560 000000      AERFLG: .WORD      0
1834
1835      ;UNABLE TO DECODE FPP INSTRUCTION. TRAPPED TO 244.
1836 004562 011637 001236      AERR1:  MOV      (SP),@STMP2  ;SAVE PC OF TRAP.
1837 004566 022626      CMP      (SP)+,(SP)+
1838 004570 104010      IS:     ERROR      10
1839 004572 000436      BR      ADONE
1840
1841      ;UNABLE TO DECODE INSTRUCTION. TRAPPED TO 10.
1842 004574 021627 004352      AERR2:  CMP      (SP),#A11+2  ;DID TRAP OCCUR OF FPP INSTRUCTION?
1843 004600 001405      BEQ      IS
1844 004602 021627 004366      CMP      (SP),#A12+2
1845 004606 001402      BEQ      IS
1846 004610 000137 040250      JMP      @CPTWO      ;IF NOT FPP INSTRUCTION THEN
1847
1848
1849
1850 004614 011637 001236      IS:     MOV      (SP),@STMP2  ;OTHERWISE REPORT IR DECIDE ERROR.
1851 004620 022626      CMP      (SP)+,(SP)+
1852 004622 104011      2S:     ERROR      11
1853 004624 000421      BR      ADONE
1854
1855      ;TRAP TO 4 HANDLER:
1856 004626 021627 004352      AERR3:  CMP      (SP),#A11+2  ;DID THE TRAP OCCUR ON THE
1857 004632 001405      BEQ      IS          ;LDFPS INSTRUCTION?
1858 004634 021627 004366      CMP      (SP),#A12+2  ;OR THE STFPS INSTRUCTION?

```

# J03

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1858 004640 001407 BEQ 25
1859 004642 000137 040232 JMP 25BCPSPUR ;IF NEITHER THEN REPORT
1860 ;UNEXPECTED TRAP TO 4.
1861
1862 004646 011637 001236 15: MOV (SP),2#STMP2
1863 004652 022626 CMP (SP)+,(SP)+
1864 004654 104014 155: ERROR 14
1865 004656 000404 BR ADONE
1866
1867 004660 011637 001236 25: MOV (SP),2#STMP2
1868 004664 022626 CMP (SP)+,(SP)+
1869 004666 104015 255: ERROR 15
1870
1871 004670 ADONE:
1872 004670 104412 RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
1873 ;SEE IF THE USER HAS EXPRESSED
1874 ;THE DESIRE TO CHANGE THE SOFTWARE
1875 ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
1876 ;THE USER TYPED CONTROL G?).
1877
1878
1879
1880 ;*****
1881 ;*TEST 2 CFCC TEST
1882 ;*
1883 ;*THIS IS A TEST OF THE COPY CONDITION CODES INSTRUCTION, CFCC.
1884 ;*
1885 ;*****
1886 004672 000004 ST2: SCOPE
1887 004674 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
1888 004676 012700 000017 MOV #17;R0 ;R0 CONTAINS TO TEST PATTERN.
1889
1890 004702 B1:
1891 004702 170100 LDFPS R0 ;LOAD THE TEST PATTERN
1892
1893 004704 B2:
1894 004704 170000 CFCC ;COPY CONDITION CODES.
1895
1896 004706 013703 177776 MOV 2#PSW,R3 ;SEE IF PATTERN TRANSFERED.
1897 004712 042703 177760 BIC #177760,R3
1898 004716 020003 CMP R0,R3
1899 004720 001002 BNE BERR
1900
1901 004722 B3:
1902 004724 000422 SOB R0,B1
1903 BR BDONE
1904
1905 004726 BERR:
1906 004726 170201 STFPS R1 ;WAS FPS MODIFIED BY CFCC?
1907 004730 012737 004704 001236 MOV #B2,2#STMP2
1908
1909 004736 020001 CMP R0,R1
1910 004740 001006 BNE BERR1
1911
1912 004742 010337 001240 MOV R3,2#STMP3
1913 004746 010037 001242 MOV R0,2#STMP4
1914 004752 104003 15: ERROR 3
1915 004754 000762 BR B3

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K03

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1914
1915 004756 BERR1:
1916 004756 010037 001240 MOV R0,2*STMP3
1917 004762 010137 001242 MOV R1,2*STMP4
1918 004766 104004 1S: ERROR 4
1919 004770 000754 BR B3
1920
1921 004772 BDONE:
1922 004772 104412 RSETUP ;GO INITIALIZE THE FPS AND STACK; -AND
1923 ;SEE IF THE USER HAS EXPRESSED
1924 ;THE DESIRE TO CHANGE THE SOFTWARE
1925 ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
1926 ;THE USER TYPED CONTROL G?).
1927
1928
1929 *****
1930 *TEST 3 SETF, SETD, SETI AND SETL TEST
1931 *
1932 *THIS IS A TEST OF THE SETF, SETD, SETI AND SETL INSTRUCTIONS.
1933 *EACH INSTRUCTION IS EXECUTED WITH THE FPS CONTAINING
1934 *ALL ONES AND ALSO WITH THE FPS CLEAR. THE RESULT OF EACH
1935 *SITUATION IS CHECKED.
1936 *
1937 *****
1938 004774 000004 TST3: SCOPE
1939 004776 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
1940 005000 012737 000760 001244 MOV #760,2*STMP5
1941 005006 012737 000202 001250 C1: MOV #202,2*STMP7
1942 005014 012737 041352 001252 MOV #SETF1,2*STMP10
1943 005022 005000 CLR R0
1944 005024 170100 LDFPS R0 ;CLEAR THE FPS.
1945 005026 012737 005034 001236 MOV #C15,2*STMP2
1946
1947 005034 170001 C15: SETF ;TEST INSTRUCTION.
1948
1949 005036 170201 STFPS R1 ;GET RESULT.
1950 005040 005002 CLP R2
1951 005042 020201 CMP R2,R1 ;DID AN ERROR OCCUR?
1952 005044 001402 BEQ 1S
1953 005046 004737 005432 JSR PC,2*CERR1
1954
1955 005052 1S:
1956 005052 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
1957 005054 012700 147757 C2: MOV #147757,R0
1958
1959 005060 170100 LDFPS R0 ;PUT 147757 IS FPS
1960 005062 012737 005070 001236 MOV #C25,2*STMP2
1961 005070 170001 C25: SETF ;CLEAR FD BIT.
1962
1963 005072 170201 STFPS R1 ;GET RESULT
1964 005074 012702 147557 MOV #147557,R2
1965 005100 020102 CMP R1,R2 ;RESULT CORRECT.
1966 005102 001402 BEQ 1S
1967 005104 004737 005530 JSR PC,2*CERP2
1968
1969 005110 1S:

```

# L03

MAINDEC-11-DFFPA-A PDP 11.34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 37  
 DFFPAA.P11 01-NOV-76 21:03 T3 SETF. SETD, SETI AND SETL TEST

1970	005110	104413				LPERR			;SET UP THE LOOP ON ERROR ADDRESS.
1971	005112	012737	000203	001250	C3:	MOV	#203,2#STMP7		
1972	005120	012737	041360	001252		MOV	#SETD1,2#STMP10		
1973	005126	012700	147757			MOV	#147757,R0		
1974									
1975	005132	170100				LDFPS	R0		;LOAD 147757 INTO FPS.
1976	005134	012737	005142	001236		MOV	#C35,2#STMP2		
1977	005142	170011			C35:	SETD			;SETD FD BIT.
1978									
1979	005144	170201				STFPS	R1		
1980	005146	012702	147757			MOV	#147757,R2		
1981	005152	020102				CMP	R1,R2		;RESULT CORRECT?
1982	005154	001402				BEQ	1\$		
1983	005156	004737	005530			JSR	PC,2#CERR2		
1984									
1985	005162				1\$:				
1986	005162	104413				LPERR			;SET UP THE LOOP ON ERROR ADDRESS.
1987	005164	005000			C4:	CLR	R0		
1988	005166	170100				LDFPS	R0		;CLEAR FPS.
1989	005170	012737	005176	001236		MOV	#C45,2#STMP2		
1990									
1991	005176	170011			C45:	SETD			;SET FD BIT.
1992									
1993	005200	170201				STFPS	R1		;GET RESULT.
1994	005202	012702	000200			MOV	#200,R2		
1995	005206	020102				CMP	R1,R2		;RESULT CORRECT?
1996	005210	001402				BEQ	1\$		
1997	005212	004737	005432			JSR	PC,2#CERR1		
1998									
1999	005216				1\$:				
2000	005216	104413				LPERR			;SET UP THE LOOP ON ERROR ADDRESS.
2001	005220	012737	000204	001250	C5:	MOV	#204,2#STMP7		
2002	005222	012737	041366	001252		MOV	#SETI1,2#STMP10		
2003	005234	005000				CLR	R0		
2004									
2005	005236	170100				LDFPS	R0		;CLEAR FPS
2006	005240	012737	005246	001236		MOV	#C55,2#STMP2		
2007									
2008	005246	170002			C55:	SETI			;CLEAR FL BIT.
2009									
2010	005250	170201				STFPS	R1		;GET RESULT.
2011	005252	005002				CLR	R2		
2012	005254	020201				CMP	R2,R1		;RESULT CORRECT?
2013	005256	001402				BEQ	1\$		
2014	005260	004737	005432			JSR	PC,2#CERR1		
2015									
2016	005264				1\$:				
2017	005264	104413				LPERR			;SET UP THE LOOP ON ERROR ADDRESS.
2018	005266	012700	147757		C6:	MOV	#147757,R0		
2019	005272	170100				LDFPS	R0		;PUT 147757 INTO FPS
2020	005274	012737	005302	001236		MOV	#C65,2#STMP2		
2021									
2022	005302	170002			C65:	SETI			;CLEAR FL BIT.
2023									
2024	005304	170201				STFPS	R1		;GET THE RESULT.
2025	005306	012702	147657			MOV	#147657,R2		

# M03

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2026 005312 020102          CMP      R1,R2          ;RESULT CORRECT?
2027 005314 001402          BEQ      15
2028 005316 004737 005530      JSR      PC,2#CERR2
2029
2030 005322          15:
2031 005322 104413          LPERR          ;SET UP THE LOOP ON ERROR ADDRESS.
2032 005324 012737 000205 001250 C7:      MOV      #205,2#STMP7
2033 005332 012737 041374 001252      MOV      #SETL1,2#STMP10
2034 005340 012700 147757      MOV      #147757,R0
2035 005344 170100          LDFPS      R0          ;SET FPS TO 147757.
2036 005346 012737 005354 001236      MOV      #C75,2#STMP2
2037
2038 005354 170012          C75:      SETL          ;SET FL BIT.
2039
2040 005356 170201          STFPS      R1          ;GET THE RESULT.
2041 005360 012702 147757      MOV      #147757,R2
2042 005364 020102          CMP      R1,R2          ;RESULT CORRECT?
2043 005366 001402          BEQ      15
2044 005370 004737 005530      JSR      PC,2#CERR2
2045
2046 005374          15:
2047 005374 104413          LPERR          ;SET UP THE LOOP ON ERROR ADDRESS.
2048 005376 005000          C8:      CLR      R0
2049 005400 170100          LDFPS      R0          ;CLEAR FPS.
2050 005402 012737 005410 001236      MOV      #C85,2#STMP2
2051
2052 005410 170012          C85:      SETL          ;SET FL BIT.
2053
2054 005412 170201          STFPS      R1
2055 005414 012702 000100      MOV      #100,R2
2056 005420 020102          CMP      R1,R2          ;RESULT CORRECT.
2057 005422 001402          BEQ      15
2058 005424 004737 005432      JSR      PC,2#CERR1
2059
2060 005430 000522          15:      BR      CDONE
2061
2062          ;THESE ARE ERROR ANALYSIS ROUTINES:
2063 005432 010103          CERR1:      MOV      R1,R3
2064 005434 032703 177477      BIT      #177477,R3          ;ARE ANY OTHER BITS SET?
2065 005440 001401          BEQ      25
2066 005442 000503          15:      BR      CERR4
2067
2068 005444 022703 000300      25:      CMP      #300,R3          ;ARE BOTH FD AND FL SET?
2069 005450 001774          BEQ      15
2070 005452 032703 000300      BIT      #300,R3          ;ARE THEY BOTH CLEAR?
2071 005456 001771          BEQ      15
2072
2073 005460 032703 000200          BIT      #200,R3          ;IS FD SET?
2074 005464 001407          BEQ      35
2075 005466 012737 041360 001254      MOV      #SETD1,2#STMP11
2076 005474 012737 000203 001246      MOV      #203,2#STMP6
2077 005502 000452          BR      CERR3
2078
2079 005504 032703 000100      35:      BIT      #100,R3          ;IS FL SET
2080 005510 001754          BEQ      15
2081 005512 012737 041374 001254      MOV      #SETL1,2#STMP11
  
```

N03

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```

005520 012737 000205 001246      MOV      #205,2#STMP6
005526 000440                      BR       CERR3

005530 010103      CERR2:  MOV      R1,R3
005532 005103                      COM      R3

005534 032703 177477      BIT      #177477,R3
005540 001401                      BEQ     2$
005542 000443      1$:      BR       CERR4

005544 032703 000300      BIT      #300,R3
005550 001774                      BEQ     1$
005552 032701 000300      BIT      #300,R1
005556 001771                      BEQ     1$

005560 032701 000200      BIT      #200,R1
005564 001007                      BNE     3$
005566 012737 041352 001254      MOV      #SETF1,2#STMP11
005574 012737 000202 001246      MOV      #202,2#STMP6
005602 000412                      BR       CERR3

005604 032701 000100      3$:      BIT      #100,R1
005610 001354                      BNE     1$
005612 012737 041366 001254      MOV      #SETI1,2#STMP11
005620 012737 000204 001246      MOV      #204,2#STMP6
005626 000400                      BR       CERR3

```

```

:ARE ANY OTHER BITS SET?
:ARE BOTH FD AND FL SET?
:ARE THEY BOTH CLEAR?
:IS FD CLEAR?
:IS FL CLEAR.

```

:REPORT THE ERRORS:

```

CERR3:  MOV      R1,2#STMP3
        MOV      R2,2#STMP4
        MOV      (SP)+,2#CPC
1$:     ERROR   12
        JMP     @CPC

CERR4:  MOV      R1,2#STMP3
        MOV      R2,2#STMP4
        MOV      (SP)+,2#CPC
1$:     ERROR   13
        JMP     @CPC

```

CPC: .WORD 0

CDONE:

RSETUP

```

:GO INITIALIZE THE FPS AND STACK; AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER (HAS
:THE USER TYPED CONTROL G?).

```

```

:*****
:*TEST 4      ILLEGAL FPP OP CODES AND STST TEST
:*
:*THIS IS A TEST OF THE FPP OPERATION CODES:

```



170003  
170004  
:  
170010  
170013  
170014  
:  
170077

\*THESE ARE ILLEGAL INSTRUCTIONS AND (WITH INTERRUPTS ENABLED)  
\*SHOULD CAUSE A TRAP TO 244.  
\*ALSO TESTED HERE IS THE INSTRUCTION:  
\*STST R1  
\*WHICH SHOULD PUT THE FEC CODE 2 IN R1, AFTER ANY OF THE ABOVE  
\*OP CODES IS EXECUTED.

\*\*\*\*\*  
TST4: SCOPE

```

005700 000004
005702 104413
005704 012705 170003
005710 012737 006114 000004
005716 012737 006020 000244

005724 005000
005726 170100
005730 005002
005732 010537 005750
005736 010537 001244
005742 012737 005750 001236
005750 000000
005752 170000
005754 005202
005756 005202

005760 170201
005762 010137 001240
005766 104016

005770 022705 170010
005774 001003
005776 012705 170013
006002 000750

006004 022705 170077
006010 001001
006012 000452
006014 005205
006016 000742

006020 022716 005752
006024 001402
006026 000137 040200

006032 022626
006034 170201
006036 022701 100000
006042 001406

D1: CLR R0
LDFPS R0 ;CLEAR FPS.
CLR R2
MOV R5,2#D2 ;SET UP THE ILLEGAL INSTRUCTION.
MOV R5,2#STMP5
MOV #D2,2#STMP2

D2: .WORD 0
D3: CFCC
INC R2
D4: INC R2

IS: STFPS R1 ;REPORT FAILURE. DID NOT TRAP.
MOV R1,2#STMP3
ERROR 16

D5: CMP #170010,R5 ;COMPUTE NEXT OP CODE
BNE D6
MOV #170013,R5
BR D1

D6: CMP #170077,R5
BNE D7
BR DDONE

D7: INC R5
BR D1

DERP1: CMP #D3,(SP) ;DID TRAP OCCUR ON TEST INSTRUCTION?
BEQ IS
JMP 2#FPPSPJR

IS: CMP (SP)+,(SP)+
STFPS R1 ;GET THE FPS AND SEE IF IT IS
CMP #100000,R1 ;SET CORRECTLY.
BEQ 3$

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006044 012737 000000 00124C MOV #100000,2#STMP3
006050 010137 001242 MOV R1,2#STMP4
006056 104017 28: ERROR 17
006060 012737 000001 38: MOV #1,R4
006064 170304 08: STST R4
006066 022704 000002 CMP #2,R4
006072 031001 BNE D9
006074 000735 BR D5
006076 012737 006064 09: MOV #08,2#STMP3
006078 010437 001242 MOV R4,2#STMP4
006104 104020 18: ERROR 20
006112 000726 BR D5
006114 022716 006066 DERR2: CMP #08+2,(SP)
006120 001402 BEQ D10
006122 000137 JMP #CPSPUR
006126 011637 001236 D10: MOV (SP),2#STMP2
006132 022626 CMP (SP)+,(SP)+
006134 104021 18: ERROR 21
006136 000714 BR D5
006140 104412 D00NE: RSET JP

```

:GET THE FEC CODE. NOTE THAT  
:IF THE DESTINATION MODE IS  
:IMPROPERLY DECODED AN OOO  
:ADDRESS TRAP TO 4 SHOULD OCCUR.  
:WAS FEC CORRECT?

:REPORT STST FAILURE

:DID THE TRAP OCCUR ON THE  
:STST INSTRUCTION?

:GO INITIALIZE THE FPS AND STACK; AND  
:SEE IF THE USER HAS EXPRESSED  
:THE DESIRE TO CHANGE THE SOFTWARE  
:VIRTUAL CONSOLE SWITCH REGISTER (HAS  
:THE USER TYPED CONTROL G?).

\*\*\*\*\*  
:TEST 5 FID, INTERRUPT DISABLE, BIT TEST  
:THIS IS A TEST OF FPS BIT 14 (FID) OR FLOATING INTERRUPT DISABLE.  
:AN ILLEGAL INSTRUCTION IS EXECUTED WITH FID=1. NO INTERRUPT SHOULD  
:OCCUR.  
\*\*\*\*\*

```

↑STS: SCOPE
LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV #EERR2,2#FPVECT ;SETUP FOR THE INTERRUPT.
E1: MOV #40000,R0
LDFPS R0 ;SET FID.
MOV #E3,2#STMP2
E2:
E3: .WORD 170020 ;ILLEGAL FPP INSTRUCTION.
E4: CFCC

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2304  
2305

006217 170201 140000  
006218 022701  
006219 001005  
006220 170304  
006221 022704 000002  
006222 001011  
006223 000431  
006224 170304  
006225 022704  
006226 001011  
006227 000431  
006228 010137 001240  
006229 012737 140000 001242  
006230 104022  
006231 000422  
006232 010537 001240  
006233 010437 001242  
006234 104023  
006235 000414  
006236 021627 006172  
006237 001402  
006238 000137 040200  
006239 011637 001236  
006240 022626  
006241 170201  
006242 010137 001240  
006243 104024  
006300 104412  
006302 000004

STFPS R1 ;SEE IF ERROR WAS DETECTED.  
CMP #140000,R1  
BNE EERR0  
STG7 R4 ;SEE IF FEC=2  
CMP #2,R4  
BNE EERR1  
BR EDONE  
EERR0: ;REPORT FPS INCORRECTLY SET.  
MOV R1,#STMP3  
MOV #40000,#STMP4  
IS: ERROR 22  
BR EDONE  
EERR1: ;REPORT FEC NOT 2.  
MOV R5,#STMP3  
MOV R4,#STMP4  
IS: ERROR 23  
BR EDONE  
EERR2: CMP (SP),#E4 ;DID THE ILLEGAL INSTRUCTION TRAP?  
BEQ IS  
JMP #FFSPUR  
IS: MOV (SP),#STMP2  
CMP (SP)+,(SP)+  
STFPS R1  
MOV R1,#STMP3  
2S: ERROR 24  
EDONE: RSETUP ;GO INITIALIZE THE FPS AND STACK; AND  
;SEE IF THE USER HAS EXPRESSED  
;THE DESIRE TO CHANGE THE SOFTWARE  
;VIRTUAL CONSOLE SWITCH REGISTER (HAS  
;THE USER TYPED CONTROL G?).

\*\*\*\*\*  
\*TEST 6 LDD AND STD. WITH SRC AND DST MODE 1, TEST  
\*  
\*THIS IS A TEST OF BOTH THE INSTRUCTION:  
\* LDD (RO),ACO  
\*AND THE INSTRUCTION:  
\* STD ACO,(RO)  
\*MOST OF THE FAILURES ARE ISOLATED TO THE SRC OR DST FLOWS. NOTE  
\*THAT THE INTEGRITY OF ACO HAS NOT BEEN ASSURED. THIS MEANS THAT  
\*IN SOME CASES IT WILL BE IMPOSSIBLE TO ISOLATE CERTAIN DATA PATTERN  
\*FAILURES TO EITHER THE FLOWS OR THIS ACCUMULATOR.  
\*  
\*\*\*\*\*  
\*TE: SCOPE

# E04

```

2336 006304          F1:          LPERR          ;SET UP THE LOOP ON ERROR ADDRESS.
2337 006304 104413          MOV          #F3,2#STMP2
2338 006306 012737 006356 001236          CLR          R0
2339 006314 005000          LD FPS       R0
2340 006316 170100          SETD
2341 006320 170011          MOV          #FDAT0,R1          ;SET UP THE LOAD DATA.
2342 006322 012701 010110          MOV          #FXDAT0,R2
2343 006326 012702 010154          MOV          #10,R3
2344 006332 012703 000010          MOV          #10,R3
2345 006336 012221          F2:          MOV          (R2)+,(R1)+
2346 006340 077302          SOB          R3,F2
2347 006342 012700 010120          MOV          #FDAT14,R0          ;SETUP R0 FOR THE LDD (R0),AC0.
2348 006346 012737 007574 000004          MOV          #FERR20,2#ERRVECT ;IF THE SRC FLOWS FAIL THEN
2349 006354 005003          CLR          R3          ;AN ODD ADDRESS MAY OCCUR.
2350 006356 172410          F3:          LDD          (R0),AC0
2351 006360 005203          F4:          INC          R3
2352 006362 005203          INC          R3
2353 006364 020027 010120          CMP          R0,#FDAT14          ;WAS R0 AFFECTED?
2354 006370 001402          BEQ          F5
2355 006372 000137 006740          JMP          2#FERR1
2356 006376 020327 000002          F5:          CMP          R3,#2
2357 006402 001402          BEQ          1$
2358 006404 000137 007036          JMP          2#FERR2          ;SEE IF THE PC WAS ADVERSELY
2359 006410 012701 010110          1$:          MOV          #FDAT10,R1          ;MAKE SURE THE SOURCE DATA WAS
2360 006414 012702 010154          MOV          #FXDAT0,R2          ;NOT AFFECTED.
2361 006420 012703 000010          MOV          #10,R3
2362 006424 022122          2$:          CMP          (R1)+,(R2)+
2363 006426 001402          BEQ          3$
2364 006430 000137 006702          JMP          2#FERR0
2365 006434 077305          3$:          SOB          R3,2$
2366 006436 170201          ST FPS       R1          ;MAKE SURE THE FPS IS CORRECT.
2367 006440 022701 000200          CMP          #200,R1
2368 006444 001402          BEQ          F6
2369 006446 000137 007554          JMP          2#FERR11
2370 006452          F6:          LPERR
2371 006452 104413          MOV          #F10,2#STMP2          ;SET UP THE LOOP ON ERROR ADDRESS.
2372 006454 012737 006516 001236          MOV
2373 006462 012703 177777          MOV          #-1,R3
2374 006466 012704 000010          MOV          #10,R4
2375 006472 012705 010132          MOV          #FDAT00,R5          ;SET UP THE OUTPUT DATA BUFFER.
2376 006476 010325          F7:          MOV          R3,(R5)+
2377 006500 077402          SOB          R4,F7
2378 006502 012700 010142          MOV          #FDAT04,R0          ;SET UP R0 FOR DST MODE 1 REG 0.
2379 006506 012737 007742 000004          MOV          #FERR25,2#ERRVECT ;IF THE DST FLOWS FAIL AN ODD
2380 /          ;ADDRESS COULD OCCUR.
  
```

# F04

NOV-11-05FA-A POP 11 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 44  
 05FA.F11 01-NOV-76 21:03 T6 LDC AND STD, WITH SRC AND DST MODE 1, TEST

2362	006514	005003		CLR	R3	
2363						
2364	006516	174010		F10: STD	AC0,(R0)	;TEST INSTRUCTION.
2365	006520	005203		F11: INC	R3	
2366	006522	005203			INC	R3
2367						
2368	006524	020027	010142	CMP	R0,#FDAT04	;WAS R0 MODIFIED?
2369	006530	001402		BEQ	F12	
2370	006532	000137	007076	JMP	2#FERR3	
2371						
2372	006536	020327	000002	F12: CMP	R3,#2	;WAS THE PC AFFECTED CORRECTLY?
2373	006542	001402		BEQ	F13	
2374	006544	000167	000320	JMP	FERR4	
2375						
2376	006550	012701	010132	F135: MOV	#FDAT00,R1	
2377	006554	012702	010154	MOV	#FXDAT0,R2	
2378						
2379	006560	022122		CMP	(R1)+,(R2)+	;SEE IF THE DATA WAS OUTPUT
2380	006562	001402		BEQ	F13	;TO THE TARGET AREA CORRECTLY.
2381	006564	000137	007174	JMP	2#FERR5	
2382						
2383	006570	022122		F13: CMP	(R1)+,(R2)+	
2384	006572	001402		BEQ	F14	
2385	006574	000137	007174	JMP	2#FERR5	
2386						
2387	006600	022122		F14: CMP	(R1)+,(R2)+	
2388	006602	001402		BEQ	F15	
2389	006604	000137	007174	JMP	2#FERR5	
2390						
2391	006610	022122		F15: CMP	(R1)+,(R2)+	
2392	006612	001402		BEQ	F16	
2393	006614	000137	007174	JMP	2#FERR5	
2394						
2395	006620	022122		F16: CMP	(R1)+,(R2)+	
2396	006622	001402		BEQ	F17	
2397	006624	000137	007520	JMP	2#FERR10	
2398						
2399	006630	022122		F17: CMP	(R1)+,(R2)+	
2400	006632	001402		BEQ	F20	
2401	006634	000137	007230	JMP	2#FERR6	
2402						
2403	006640	022122		F20: CMP	(R1)+,(R2)+	
2404	006642	001402		BEQ	F21	
2405	006644	000167	000514	JMP	FERR7	
2406						
2407	006650	022122		F21: CMP	(R1)+,(R2)+	
2408	006652	001402		BEQ	F22	
2409	006654	000137	007520	JMP	2#FERR10	
2410						
2411	006660	005001		F22: CLR	R1	
2412	006662	170201		STFPS	R1	;MAKE SURE FPS IS CORRECT.
2413	006664	022701	000200	CMP	#200,R1	
2414	006670	001402		BEQ	F23	
2415	006672	000137	007554	JMP	2#FERR11	
2416	006676	000137	010174	F23: JMP	2#FDONE	

G04

006702	012737	010154	001240	FERRO:	MOV	#FXDAT0,2#STMP3	:SOURCE DATA AFFECTED BY
006702	012737	010166	001242		MOV	#FXDAT0+12,2#STMP4	:THE LDD INSTRUCTION.
006710	012737	010110	001244		MOV	#FDAT10,2#STMP5	
006716	012737	010122	001246		MOV	#FDAT10+12,2#STMP6	
006724	104025			15:	ERROR	25	
006732	000137	010174			JMP	2#FDONE	
006740	012737	010120	001242	FERR1:	MOV	#FDAT14,2#STMP4	:FSRC FLOWS FAILURE.
006746	010037	001240			MOV	R0,2#STMP3	
006752	012737	000762	001244		MOV	#762,2#STMP5	
006760	012737	000321	001250		MOV	#321,2#STMP7	
006766	022700	010110			CMP	#FDAT10,R0	:FSRC MODE 4?
006772	001004				BNE	15	
006774	012737	000324	001246		MOV	#324,2#STMP6	
006782	000412				BR	45	
007004	022700	010130		15:	CMP	#FDAT14+10,R0	:FSRC MODE 2?
007010	001004				BNE	25	
007012	012737	000322	001246		MOV	#322,2#STMP6	
007020	000403				BR	45	
007022				25:			
007022	104027			35:	ERROR	27	
007024	000137	010174			JMP	2#FDONE	
007030				45:			
007030	104026			55:	ERROR	26	
007032	000137	010174			JMP	2#FDONE	
007036	012701	006360		FERR2:	MOV	#F4,R1	:THE PC WAS INCORRECTLY AFFECTED
007042	010137	001242					:DURING THE INSTRUCTION.
007046	162701	000004		FER2:	MOV	R1,2#STMP4	
007052	006303				SUB	#4,R1	
007054	060301				ASL	R3	
007056	010137	001240			ADD	R3,R1	
007062	104030			15:	MOV	R1,2#STMP3	
007064	000137	010174			ERROR	30	
					JMP	2#FDONE	
007070	012701	006520		FERR4:	MOV	#F11,R1	
007074	000762				BR	FER2	
007076	012737	010142	001242	FERR3:	MOV	#FDAT04,2#STMP4	:FAILURE IN THE FDST FLOWS.
007104	010037	001240			MOV	R0,2#STMP3	
007110	012737	000527	001244		MOV	#527,2#STMP5	
007116	012737	000641	001250		MOV	#641,2#STMP7	
007124	022700	010132			CMP	#FDAT00,R0	:DST MODE 4?
007130	001004				BNE	15	
007132	012737	000644	001246		MOV	#644,2#STMP6	
007140	000412				BR	45	
007142	022700	010152		15:	CMP	#FDAT04+10,R0	:DST MODE 2?
007146	001004				BNE	25	

# H04

25072	007150	012737	000642	001246	MOV	#642,2#STMP6	
25073	007156	000403			BR	45	
25074	007160				25:		
25075	007160	104032			35:	ERROR	32
25076	007162	000137	010174		JMP	2#FDONE	
25077	007166				45:		
25078	007166	104031			55:	ERROR	31
25079	007170	000137	010174		JMP	2#FDONE	
25080	007174				FERR5:		;FAILURE OF STD.
25081	007174	010037	001240		MOV	R0,2#STMP3	
25082	007200	012737	010132	001242	MOV	#FDAT00,2#STMP4	
25083	007206	012737	010150	001244	MOV	#FDAT07,2#STMP5	
25084	007214	012737	010154	001246	MOV	#FXDAT0,2#STMP6	
25085	007222	104033			15:	ERROR	33
25086	007224	000137	010174		JMP	2#FDONE	
25087	007230	012701	010144		FERR6:	MOV	#FDAT05,R1
25088	007234	012702	177777		MOV	#-1,R2	;DID (BUT GR7) FAIL IN THE FDST
25089	007240	012703	000003		MOV	#3,R3	;FLOWS?
25090	007244	020221			15:	CMP	R2,(R1)+
25091	007246	001017			BNE	55	
25092	007250	077303			SOB	R3,15	
25093							;REPORT FAILURE OF (BUT GR7) IN
25094	007252	010037	001240		MOV	R0,2#STMP3	;THE FDST FLOWS.
25095	007256	012737	000412	001244	MOV	#412,2#STMP5	
25096	007264	012737	000147	001246	MOV	#147,2#STMP6	
25097	007272	012737	000145	001250	MOV	#145,2#STMP7	
25098	007300	104034			25:	ERROR	34
25099	007302	000137	010174		JMP	2#FDONE	
25100	007306	012701	010144		55:	MOV	#FDAT05,R1
25101	007312	012703	000003		MOV	#3,R3	;DID (BUT GR7) FAIL IN THE SRC FLOWS?
25102	007316	005721			65:	TST	(R1)+
25103	007320	001402			BEQ	75	
25104	007322	000137	007520		JMP	2#FERR10	
25105	007326	077305			75:	SOB	R3,65
25106							;REPORT FAILURE OF (BUT GR7) IN
25107	007330	010037	001240		MOV	R0,2#STMP3	;THE FSRC FLOWS.
25108	007334	012737	000207	001244	MOV	#207,2#STMP5	
25109	007342	012737	000176	001246	MOV	#176,2#STMP6	
25110	007350	012737	000174	001250	MOV	#174,2#STMP7	
25111	007356	104035			105:	ERROR	35
25112	007360	000137	010174		JMP	2#FDONE	
25113							;DID (BUT FD) FAIL IN THE FDST FLOWS?
25114	007364	012701	010146		FERR7:	MOV	#FDAT06,R1
25115	007370	012702	177777		MOV	#-1,R2	
25116	007374	012703	000002		MOV	#2,R3	
25117	007400	020221			15:	CMP	R2,(R1)+
25118	007402	001017			BNE	55	
25119	007404	077303			SOB	R3,15	

```

2530
2531
2532 007406 010037 001240      MOV      R0,2#STMP3      ;REPORT FAILURE OF (BUT FD) IN THE
2533 007412 012737 000707 001244      MOV      #707,2#STMP5   ;FDST FLOWS.
2534 007420 012737 000244 001246      MOV      #244,2#STMP6
2535 007428 012737 000245 001250      MOV      #245,2#STMP7
2536 007434 104036      2S:      ERROR      36
2537 007436 000137 010174      JMP      2#FDONE
2538
2539 007442 012701 010146      5S:      MOV      #FDAT06,R1     ;DID (BUT FD) FAIL IN THE FSRC FLOWS?
2540 007446 012703 000002      MOV      #2,R3
2541 007452 005721      6S:      TST      (R1)+
2542 007454 001402      BEQ      7S
2543 007456 000137 007520      JMP      2#FERR10
2544 007462 077305      7S:      SOB      R3,6S
2545
2546
2547
2548 007464 010037 001240      MOV      R0,2#STMP3      ;REPORT FAILURE OF (BUT FD) IN THE
2549 007470 012737 000441 001244      MOV      #441,2#STMP5   ;FSRC FLOWS.
2550 007476 012737 000076 001246      MOV      #76,2#STMP6
2551 007504 012737 000077 001250      MOV      #77,2#STMP7
2552 007512 104037      10S:     ERROR     37
2553 007514 000137 010174      JMP      2#FDONE
2554
2555 FERR10:      ;REPORT DATA ERROR.
2556 007520 010037 001240      MOV      R0,2#STMP3
2557 007524 012737 010142 001242      MOV      #FDAT04,2#STMP4
2558 007532 012737 010150 001244      MOV      #FDAT07,2#STMP5
2559 007540 012737 010164 001246      MOV      #FXDAT4,2#STMP6
2560 007546 104040      1S:      ERROR      40
2561 007550 000137 010174      JMP      2#FDONE
2562
2563 FERR11:      ;REPORT BAD FPS.
2564 007554 010137 001240      MOV      R1,2#STMP3
2565 007560 012737 000200 001242      MOV      #200,2#STMP4
2566 007566 104041      1S:      ERROR      41
2567 007570 000137 010174      JMP      2#FDONE
2568
2569 FERR20:      ;THE EXECUTION OF THE LDD
2570 007574 012737 040411 001264      MOV      #NULL,2#STMP15 ;CAUSED A TRAP TO 4, BECAUSE
2571 007602 005037 001252      CLR      2#STMP10        ;A FSRC FLOW FAILURE RESULTED
2572 007606 011637 001236      MOV      (SP),2#STMP2    ;IN AN ODD ADDRESS.
2573 007612 012737 010120 001240      MOV      #FDAT14,2#STMP3
2574 007620 012737 000321 001250      MOV      #321,2#STMP7
2575 007626 012737 000762 001244      MOV      #762,2#STMP5
2576
2577 007634 021627 006362      CMP      (SP),#F4+2     ;SEE IF FSRC MODE 6 OR 7 WAS
2578 007640 001424      BEQ      FERR21         ;EXECUTED.
2579
2580 007642 020027 010116      CMP      R0,#FDAT13     ;FSRC MODE 5?
2581 007646 001006      BNE      2S
2582
2583 007650 012737 000325 001246      MOV      #325,2#STMP6   ;REPORT FSRC FLOW FAILURE TO
2584 007656 022626      1S:      CMP      (SP)+,(SP)+    ;MODE 5.
2585 007660 104042      ERROR      42
2586 007662 000544      BR      FDONE

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K04

2642 010110 177777  
2643 010112 177777  
2644 010114 177777  
2645 010116 177777  
2646 010120 177777  
2647 010122 177777  
2648 010124 177777  
2649 010126 177777  
2650 010130 177777  
2651 010132 177777  
2652 010134 177777  
2653 010136 177777  
2654 010140 177777  
2655 010142 177777  
2656 010144 177777  
2657 010146 177777  
2658 010150 177777  
2659 010152 177777  
2660 010154 177777  
2661 010156 177777  
2662 010160 177777  
2663 010162 177777  
2664 010164 052525  
2665 010166 031463  
2666 010170 007417  
2667 010172 000477  
2668  
2669  
2670 010174  
2671 010174 104412  
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2684 010176 000004  
2685 010200 104413  
2686  
2687 010202  
2688 010202 170011  
2689 010204 012700 011026  
2690 010210 012701 010776  
2691 010214 012702 000004  
2692 010220 012120  
2693 010222 077202  
2694  
2695 010224 012700 011026  
2696 010230 172510  
2697

FDATIO: -1  
FDAT1: -1  
FDAT2: -1  
FDAT3: -1  
FDAT4: -1  
FDAT5: -1  
FDAT6: -1  
FDAT7: -1  
FDAT00: -1  
FDAT01: -1  
FDAT02: -1  
FDAT03: -1  
FDAT04: -1  
FDAT05: -1  
FDAT06: -1  
FDAT07: -1  
FXDAT0: -1  
FXDAT1: -1  
FXDAT2: -1  
FXDAT3: -1  
FXDAT4: 052525  
FXDAT5: 031463  
FXDAT6: 007417  
FXDAT7: 000477

FDONE: RSETUP

:GO INITIALIZE THE FPS AND STACK; AND  
:SEE IF THE USER HAS EXPRESSED  
:THE DESIRE TO CHANGE THE SOFTWARE  
:VIRTUAL CONSOLE SWITCH REGISTER (HAS  
:THE USER TYPED CONTROL G?).

:\*\*\*\*\*  
:TEST 7 FSRC MODE 0 TEST  
:\*\*\*\*\*  
:THIS IS A TEST OF FSRC MODE ZERO USING THE LDD AND LDF INSTRUCTIONS.  
:\*\*\*\*\*

†ST7: SCOPE ;SET UP THE LOOP ON ERROR ADDRESS.  
LPERR

I1: SETD ;SET FD.  
MOV #IDATIO,RO  
MOV #IPATIO,R1  
MOV #4,R2  
I2: MOV (R1)+,(RO)+ ;SET UP THE INPUT DATA BUFFER.  
SOB R2,I2  
MOV #IDATIO,RO ;LOAD AC1  
LDD (RO),AC1

```

2698 010232 012700 011006      MOV      #IPAT20,RO      ;LOAD ACO
2699 010236 172410      LDD      (RO),ACO
2700
2701 010240 012701 000001      MOV      #1,R1          ;IN CASE THE FSRC FLOWS FAIL
2702 010244 012737 010576 000004      MOV      #IERR0,2#ERRVECT ;AN ODD ADDRESS TRAP TO 4 MAY OCCUR.
2703 010252 012737 010266 001236      MOV      #I3,2#STMP2
2704 010260 012737 042636 001240      MOV      #MS35,2#STMP3
2705 010266 172401      LDD      AC1,ACO        ;TEST INSTRUCTION.
2706 010270 000240      NOP
2707 010272 000240      NOP
2708
2709 010274 012700 011016      MOV      #IDAT00,RO
2710 010300 174010      STD      ACO,(RO)      ;GET ACO, THE RESULTS.
2711
2712 010302 012700 011016      MOV      #IDAT00,RO      ;SEE IF DATA IS CORRECT.
2713 010306 012701 011026      MOV      #IDATIO,R1
2714 010312 012702 000004      MOV      #4,R2
2715 010316 022021      CMP      (RO)+,(R1)+
2716 010320 001424      BEQ      I105
2717
2718 010322 012700 011022      MOV      #IDAT02,RO      ;SEE IF (BUT FD) FAILED.
2719 010326 012702 000002      MOV      #2,R2
2720 010332 005720      TST      (RO)+
2721 010334 001413      BEQ      I10
2722
2723 010336 012700 011022      MOV      #IDAT02,RO
2724 010342 012702 000002      MOV      #2,R2
2725 010346 022720 177777      CMP      #-1,(RO)+
2726 010352 001402      BEQ      25
2727 010354 000137 010660      JMP      2#IERR1
2728 010360 077206      SOB      R2,I5
2729 010362 000401      BR      I106
2730 010364 077216      SOB      R2,I7
2731 010366 000137 010700      JMP      2#IERR2
2732
2733 010372 077227      I105: SOB      R2,I6
2734
2735      ;NOW TEST THE LOAD INSTRUCTION WITH FSRC MODE ZERO AND FD CLEAR.
2736
2737 010374      I11:
2738 010374 104413      LPERR      ;SET UP THE LOOP ON ERROR ADDRESS.
2739
2740 010376 012700 010776      I12: MOV      #IPATIO,RO
2741 010402 012701 011026      MOV      #IDATIO,R1
2742 010406 012702 000004      MOV      #4,R2
2743 010412 012021      I13: MOV      (RO)+,(R1)+
2744 010414 077202      SOB      R2,I13
2745
2746 010416 012700 011026      MOV      #IDATIO,RO      ;SET UP AC1
2747 010422 172510      LDD      (RO),AC1
2748
2749 010424 012700 011006      MOV      #IPAT20,RO      ;SET UP ACO
2750 010430 172410      LDD      (RO),ACO
2751
2752 010432 012701 000001      MOV      #1,R1
2753 010436 012737 010454 001236      MOV      #I14,2#STMP2
  
```

MO4

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2754 010444 012737 042643 001240      MOV      #MS36,2#STMP3
2755 010452 170001                      SETF                      ;CLEAR FD.
2756
2757 010454 172401      I14:    LDF      AC1,ACD      ;TEST INSTRUCTION.
2758 010456 000240      I15:    NOP
2759 010460 000240      I16:    NOP
2760
2761 010462 170200      STFPS   R0                ;SEE IF FPS IS STILL CLEAR.
2762 010464 022700 000004      CMP      #4,R0
2763 010470 001402      BEQ     I17
2764 010472 000137 010752      JMP     2#IERR3
2765
2766 010476      I17:    SETD                      ;RESET TO DOUBLE MODE.
2767 010476 170011
2768
2769 010500 012700 011016      MOV      #IDAT00,R0
2770 010504 174010      STD     ACD,(R0)          ;GET ACD
2771
2772 010506 012737 177777 011032      MOV      #-1,2#IDATI2
2773 010514 012737 177777 011034      MOV      #-1,2#IDATI3
2774 010522 012700 011016      MOV      #IDAT00,R0
2775 010526 012701 011026      MOV      #IDATI0,R1
2776 010532 012702 000004      MOV      #4,R2
2777 010536 022021      I20:    CMP      (R0)+,(R1)+  ;SEE IF ACD WAS CORRECT.
2778 010540 001414      BEQ     I23
2779
2780 010542 023737 011022 011002      CMP      2#IDAT02,2#IPAT12  ;DID (BUT FD) FAIL?
2781 010550 001402      BEQ     I22
2782 010552 000137 010660      I21:    JMP     2#IERR1
2783 010556 023737 011024 011004      I22:    CMP      2#IDAT03,2#IPAT13
2784 010564 001372      BNE     I21
2785 010566 000137 010726      JMP     2#IERR4
2786
2787 010572 077217      I23:    SOB     R2,I20
2788
2789 010574 000520      BR      IDONE              ;NO ERRORS.
2790
2791      ;IF AN ODD ADDRESS TRAP OCCURS COME HERE TO ANALYZE THE FSRC FAILURE.
2792 010576 022716 010270      IERR0:  CMP      #I4,(SP)        ;MAKE SURE THE TRAP OCCURRED
2793 010602 001413      BEQ     15                 ;ON THE INSTRUCTION BEING TESTED.
2794 010604 022716 010272      CMP      #I5,(SP)
2795 010610 001410      BEQ     15
2796 010612 022716 010456      CMP      #I15,(SP)
2797 010616 001405      BEQ     15
2798 010620 022716 010460      CMP      #I16,(SP)
2799 010624 001402      BEQ     15
2800 010626 000137 040232      JMP     2#CPSPUR
2801
2802 010632 011637 001236      15:    MOV      (SP),2#STMP2      ;REPORT FAILURE.
2803 010636 012737 000627 001240      MOV      #627,2#STMP3
2804 010644 012737 000320 001242      MOV      #320,2#STMP4
2805 010652 022626      CMP      (SP)+,(SP)+
2806 010654 104047      25:    ERROR   47
2807 010656 000467      BR      IDONE
2808
2809      ;REPORT DATA ERROR.

```

2800 010660  
2801 010660 012737 011026 001242  
2802 010660 012737 011016 001244  
2803 010674 104051  
2804 010676 000457  
2805  
2806  
2807 010700 012737 000153 001244  
2808 010706 012737 000434 001246  
2809 010714 012737 000435 001250  
2810 010722  
2811 010724 104050  
2812 010726 000444  
2813 010726 012737 000153 001244  
2814 010734 012737 000435 001246  
2815 010742 012737 000434 001250  
2816 010750 000764  
2817  
2818  
2819 010752  
2820 010752 012737 010454 001236  
2821 010760 010037 001240  
2822 010764 012737 000004 001242  
2823 010772 104041  
2824 010774 000420  
2825  
2826  
2827 010776 000000  
2828 011000 170360  
2829 011002 016161  
2830 011004 052525  
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2836  
2837 011006 177777  
2838 011010 177777  
2839 011012 177777  
2840 011014 177777  
2841  
2842  
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2845  
2846  
2847 011016 000000  
2848 011020 000000  
2849 011022 000000  
2850 011024 000000  
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2852 011026 000000  
2853 011030 000000  
2854 011032 000000  
2855 011034 000000  
2856  
2857 011036  
2858 011036 104412  
2859  
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IERR1: MOV #IDATIO,2#STMP4  
MOV #IDATIO,2#STMP5  
IS: ERROR 51  
BR IDONE  
:REPORT FAILURE OF (BUT FD)  
IERR2: MOV #153,2#STMP5  
MOV #434,2#STMP6  
MOV #435,2#STMP7  
IERR25:  
IS: ERROR 50  
BR IDONE  
IERR4: MOV #153,2#STMP5  
MOV #435,2#STMP6  
MOV #434,2#STMP7  
BR IERR25  
:REPORT INCORRECT FPS AFTER LOAD INSTRUCTION.  
IERR3: MOV #114,2#STMP2  
MOV #RO,2#STMP3  
MOV #4,2#STMP4  
IS: ERROR 41  
BR IDONE  
IPAT10: 0  
IPAT11: 170360  
IPAT12: 016161  
IPAT13: 052525  
IPAT20: -1  
IPAT21: -1  
IPAT22: -1  
IPAT23: -1  
IDATIO: 0  
IDATIO1: 0  
IDATIO2: 0  
IDATIO3: 0  
IDATIO: 0  
IDATIO1: 0  
IDATIO2: 0  
IDATIO3: 0  
IDONE: RSETUP

:GO INITIALIZE THE FPS AND STACK; AND  
:SEE IF THE USER HAS EXPRESSED  
:THE DESIRE TO CHANGE THE SOFTWARE  
:VIRTUAL CONSOLE SWITCH REGISTER (HAS  
:THE USER TYPED CONTROL G?).

::\*\*\*\*\*

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012700 011614  
012701 000001  
012703 011412 000004  
012705 011634  
000004  
012703 011630  
012705 000002  
005723  
001402  
000137 011474  
077505  
000137 011514  
077516  
104413  
012700 011604  
012701 011634  
012702 000004

\*TEST 10 FDST MODE 0 TEST  
\*THIS IS A TEST OF THE STORE INSTRUCTIONS, STD AND STF, WITH FDST MODE C.  
\*\*\*\*\*  
T10: SCOPE  
LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
SETD ;SET FD  
MOV #TPAT10,R3  
MOV #TDAT10,R1  
MOV #4,R2  
T2: MOV (R0)+,(R1)+ ;SET UP THE INPUT DATA BUFFER.  
SOB R2,T2  
MOV #TDAT10,R0 ;LOAD AC0  
LDO (R0),AC0  
MOV #TPAT20,R0 ;LOAD AC1  
LDO (R0),AC1  
MOV #1,R1 ;IF THE (BUT FDST) FORK FAILS  
MOV #TERR0,#ERRVECT ;AN 000 ADDRESS TRAP COULD RESULT.  
MOV #T3,#STMP2  
MOV #MS35,#STMP3  
T3: STD AC0,AC1  
T4: NOP  
T5: NOP  
MOV #TDAT00,R0 ;GET THE DATA.  
STD AC1,(R0)  
MOV #TDAT00,R3 ;SEE IF THE DATA IS CORRECT.  
MOV #TDAT10,R4  
MOV #4,R5  
T6: CMP (R3)+,(R4)+  
BEQ T105  
MOV #TDAT02,R3 ;DID (BUT FD) FAIL?  
MOV #2,R5  
T7: TST (R3)+  
BEQ T10  
JMP #TERR1  
T10: SOB R5,T7  
JMP #TERR2  
T105: SOB R5,T6  
;NOW TEST THE STF AC0,AC1 INSTRUCTION.  
T11: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
T12: MOV #TPAT10,R0 ;SET UP THE INPUT DATA BUFFER.  
MOV #TDAT10,R1  
MOV #4,R2

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011232 012021 T13: MOV (R0)+,(R1)+
011234 012022 SOB R2,T13
011236 012700 011634 MOV @DAT0,R0 ;SET UP ACC
011242 172410 LDD (R0),ACC
011244 012700 011614 MOV @PAT20,R0 ;SET UP AC1
011252 172510 LDD (R0),AC1
011254 012701 000001 MOV @R1
011256 012737 011274 001236 MOV @+14,@STMP2
011264 012737 042643 001240 MOV @MS36,@STMP3
011272 170001 SETF ;CLEAR FD
011274 174001 T14: STF ACC,AC1
011276 000240 T15: NOP
011300 000240 T16: NOP
011302 005000 CLR R0
011304 170200 STEPS R0 ;SEE IF FPS IS CLEAR.
011306 022700 000010 CMP @10,R0
011312 001401 BEQ T17
011314 000521 BR TERR3
011316 T17: SETD ;SET FD.
011316 170011
011320 012700 011624 MOV @DAT00,R0
011324 174110 STD AC1,(R0) ;PICK UP AC1.
011326 012737 177777 011640 MOV @-1,@DAT12
011334 012737 177777 011642 MOV @-1,@DAT13
011342 012703 011624 MOV @CAT00,R3
011346 012704 011634 MOV @DAT10,R4
011352 012705 000004 MOV @4,R5
011356 022324 T20: CMP (R3)+,(R4)+ ;WAS THE DATA TRANSFERRED CORRECTLY?
011360 001412 BEQ T23
011362 023737 011630 011610 CMP @DAT02,@PAT12 ;DID (BUT FD) FAIL.
011370 001401 BEQ T22
011372 000440 BR TERR1
011374 023737 011632 011612 T21: CMP @DAT03,@PAT13
011402 001373 T22: BNE T21
011404 000456 BR TERR4
011406 077515 T23: SOB R5,T20
011410 000515 BR TDONE

;TRAP HERE THROUGH VECTOR 4 IF AN ODD ADDRESS OCCURS.
*ERR0: CMP @T4,(SP) ;MAKE SURE THE TRAP WAS ON
;AN INSTRUCTION BEING TESTED.
011412 022716 011132 BEQ IS
011416 001413 CMP @T5,(SP)
011420 022716 011134 BEQ IS
011424 001410 CMP @T15,(SP)
011426 022716 011276 BEQ IS
011432 001405 BEQ IS
011434 022716 011300 CMP @T16,(SP)

```

011440	001402				BEG	18
011442	000137	040232			JMP	28CPSPUR
011446	011637	001236			15:	MOV (SP),28STMP2
011450	022636					CMF (SP)+ (SP)+
011454	012737	000527	001240			MOV 8527,28STMP3
011456	012737	000640	001242			MOV 8640,28STMP4
011472	104121			25:	ERROR	121
011472	000464				BR	TDONE
:REPORT DATA FAILURE.						
011474				TERR1:		
011474	012737	011634	001242			MOV 87DAT10,28STMP4
011502	012737	011624	001244			MOV 87DAT00,28STMP5
011510	104123			15:	ERROR	123
011512	000454				BR	TDONE
:REPORT FAILURE OF (BUT FD).						
011514	012737	000160	001246	TERR2:		
011522	012737	000161	001250			MOV 8160,28STMP6
011530	012737	000640	001244	TERR25:		MOV 8640,28STMP5
011536	104122			15:	ERROR	122
011540	000441				BR	TDONE
011542	012737	000161	001246	TERR4:		MOV 8161,28STMP6
011550	012737	000160	001250			MOV 8160,28STMP7
011556	000764				BR	TERR25
:REPORT INCORRECT FPS AFTER STORE INSTRUCTION.						
011560				TERR3:		
011560	012737	011276	001236			MOV 8T15,28STMP2
011566	010037	001240				MOV RD,28STMP3
011572	012737	000010	001242			MOV 810,28STMP4
011600	104041			15:	ERROR	41
011602	000420				BR	TDONE
011604	000000			TPAT10:		0
011606	170360			TPAT11:		170360
011610	016161			TPAT12:		016161
011612	052525			TPAT13:		052525
011614	177777			TPAT20:		-1
011616	177777			TPAT21:		-1
011620	177777			TPAT22:		-1
011622	177777			TPAT23:		-1
011624	000000			TDAT00:		0
011626	000000			TDAT01:		0
011630	000000			TDAT02:		0
011632	000000			TDAT03:		0
011634	000000			TDAT10:		0
011636	000000			TDAT11:		0
011640	000000			TDAT12:		0
011642	000000			TDAT13:		0
011644				TDONE:		



011644 104412

RSETUP

:GO INITIALIZE THE FPS AND STACK; AND  
:SEE IF THE USER HAS EXPRESSED  
:THE DESIRE TO CHANGE THE SOFTWARE  
:VIRTUAL CONSOLE SWITCH REGISTER .HAS  
:THE USER TYPED CONTROL G?).

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\*\*\*\*\*  
:TEST 11 ACCUMULATORS DATA PATTERNS TEST

\*THIS IS A TEST OF THE FLOATING POINT PROCESSOR ACCUMULATORS.  
\*EACH ACCUMULATOR IS TESTED IN TWO WAYS:  
\* 1 TEST PATTERN GENERATED BY FLOATING A ONE ACROSS  
\* A FIELD OF ZEROES.  
\* 2 TEST PATTERN GENERATED BY FLOATING A ZERO ACROSS  
\* A FIELD OF ONES.  
\*EACH OF ACCUMULATORS ACC THROUGH ACS IS TESTED.  
\*NOTE THAT THIS TEST KEEPS A DYNAMIC RECORD OF THE LOGICAL 'AND' AND 'OR'  
\*OF THE FAILING DATA PATTERNS. THESE CAN BE VERY USEFUL IN DETERMINING  
\*STUCK BITS. IF THE USER HAS THE INHIBIT ERROR TYPE OUT SWITCH (SWR13)  
\*OFF, THEN THE USER WILL RECIEVE EACH INDIVIDUAL ERROR MESSAGE PLUS  
\*AN ERROR SUMMARY AT THE END OF THE TEST. INHIBITING ERROR PRINT OUT  
\*WILL INHIBIT ERROR SUMMARY PRINT OUT, EXCEPT IN THE CASE DESCRIBED BELOW.  
\*TO GET JUST THE ERROR SUMMARY WITH NO INDIVIDUAL ERROR REPORTS,  
\*SET SWITCH REGISTER BIT13 AND SWITCH REGISTER BIT7 BOTH ON.

\*THE FOLLOWING PROCEDURE IS PRESENTED TO AID THE TROUBLE  
\*SHOOTER IN SITUATIONS WHERE AM2901 CHIP ISOLATION IS ATTEMPTED.

\*WARNING: THIS PROCEDURE ASSUMES THAT THE FAULT IS IN ONE OF THE  
\*AM2901 CHIPS. THIS ASSUMPTION IS NOT NECESSARILY VALID IN ALL  
\*SITUATIONS. IT REMAINS TO BE SEEN WHAT NUMBER OF FAILURES CAN  
\*PROBABILISTICALLY ASSOCIATED WITH THEM. NOTE ALSO THAT THIS  
\*INFORMATION SHOULD NOT BE TAKEN AS ABSOLUTE, THAT IS  
\*THIS INFORMATION IS THE AUTHOR'S SUGGESTION FOR ACHIEVING ISOLATION  
\*WHEN CHIP LEVEL REPAIR IS NECESSARY.

\*WHEN THIS TEST HAS FINISHED RUNNING, IF ERRORS HAVE OCCURRED,  
\*AN ERROR SUMMARY WILL BE TYPED. THIS SUMMARY WILL CONSIST OF TWO  
\*IMPORTANT QUANTITIES:  
\* A. FOUR SIXTEEN BIT NUMBERS LABELED THE LOGICAL 'AND' ('\*')  
\* OF THE FAILING DATA PATTERNS.  
\* B. FOUR SIXTEEN BIT NUMBERS LABELED THE LOGICAL 'OR' ('+')  
\* OF THE FAILING DATA PATTERNS.

\*A BIT STUCK HIGH IN THE HARDWARE WILL SHOW UP AS A 0 IN THAT  
\*BIT POSITION OF THE 'OR' OF THE FAILING DATA PATTERNS.

\*A BIT STUCK LOW IN THE HARDWARE WILL SHOW UP AS A 1 IN THAT BIT  
\*PCSITION OF THE 'AND' OF THE FAILING DATA PATTERNS.

\*THUS IF A FAILJRE OCCURS:  
\* A. STUCK HIGHS WILL SHOW AS 0'S IN THE 'OR' PATTERN.

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\* B. STUCK LOW WILL SHOW AS 1'S IN THE 'AND' PATTERN.  
 \* IF THE FAILURE IS INTERMITTANT THEN THIS PROCEDURE WILL STILL  
 \* APPLY!!  
 \* IF THE FAILURE MOVES FROM ONE BIT TO ANOTHER, OR FROM ONE  
 \* GROUP OF BITS TO ANOTHER GROUP OF BITS THEN THE FAULT WILL  
 \* PROBABLY NOT SHOW UP IN THE 'AND' OR THE 'OR' PATTERNS. IN THIS  
 \* CASE THE 'AND' PATTERN WILL BE ALL 0'S AND THE 'OR' PATTERN WILL  
 \* BE ALL 1'S. WHEN THIS OCCURS SOME OTHER METHOD OF REPAIR MUST  
 \* BE FOUND (SUCH AS INSPECTION OF EACH INDIVIDUAL ERROR REPORT  
 \* RATHER THAN USING THE SUMMARY).

\* MAP THE FOLLOWING NOTATION ONTO EACH BIT POSITION IN THE 'AND'  
 \* AND THE 'OR' PATTERNS WHICH ARE TYPED IN THE ERROR SUMMARY.

\*A15,A14,...A1,A0 B15,B14,...B1,B0 C15,C14,...C1,C0 D15,D14,...D1,D0

\* IN THIS NOTATION A15 THROUGH A0 IS THE FIRST OF THE FOUR 16 BIT  
 \* OCTAL NUMBERS TYPED. B15 THROUGH B0 IS THE SECOND, ETC.

\* THIS TABLE SHOWS THE CORRESPONDING AM2901 CHIP ('E' NUMBER)  
 \* WHICH IS RESPONSIBLE FOR EACH BIT POSITION USING THE ABOVE  
 \* NOTATION. NOTE THAT ECC'S TO THE HARDWARE MIGHT MAKE THIS  
 \* TABLE OBSOLETE IF IT IS NOT UP DATED. NOTE ALSO THAT THERE ARE  
 \* FOUR BITS FOR EACH AM2901 CHIP:

BITS ----	AM2901 CHIP NUMBER -----
A15,A14,A13,A12	E61
A11,A10,A9,A8	E62
A7,A6,A5,A4	E90
A3,A2,A1,A0	E81
B15,B14,B13,B12	E86
B11,B10,B9,B8	E85
B7,B6,B5,B4	E83
B3,B2,B1,B0	E88
C15,C14,C13,C12	E79
C11,C10,C9,C8	E94
C7,C6,C5,C4	E89
C3,C2,C1,C0	E87
D15,D14,D13,D12	E78
D11,D10,D9,D8	E77
D7,D6,D5,D4	E82
D3,D2,D1,D0	E80

\* NOW FIVE IMPORTANT CASES WHICH WILL ARRISE WHEN A FAULTY  
 \* AM2901 IS PRESENT CAN BE DESCRIBED:

- \* 1.) IF ONLY ONE BIT OF THE 64 BITS IS INCORRECT THE CHIP INDICATED  
 IN THE ABOVE TABLE IS MOST PROBABLY AT FAULT. BUT IF THAT  
 CHIP IS REPLACED AND THE ERROR PERSISTS THEN SUPPOSE THAT  
 BIT IS, LN WHERE 'L' IS A, B, C OR D

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*****
* THEN IN GENERAL ANY OF THE FOUR CHIPS RESPONSIBLE FOR
* AN, BN, CN OR DN COULD BE AT FAULT, WITH N BEING MOST PROBABLE.
* FOR EXAMPLE IF BIT C12 IS FAULTY, THEN CHIP E79
* IS THE MOST PROBABLE SOURCE OF THE ERROR. IF REPAIRING
* THAT CHIP DOES NOT REMOVE THE FAULT THEN TRY EACH OF THE
* CHIPS ASSOCIATED WITH BITS A12, B12 AND D12 SHOULD BE TRIED
* WITH EQUAL PROBABILITY OF THE FAULT BEING
* IN ANY ONE OF THESE OTHER THREE CHIPS. TRY CHIPS E61, E66 AND E78.
*2.) IF THERE ARE FOUR CONSECUTIVE BITS IN ERROR, FOLLOWING THE
* PATTERN:
* LN, LN+1, LN+2 AND LN+3 WHERE 'L' IS A, B, C
* OR D.
* AND N=0,4,8 OR 12
* THEN THE ABOVE TABLE SHOULD DIRECTLY IDENTIFY THE
* FAILING CHIP.
*3.) IF FOUR BITS ARE DROPPED WHICH FIT THE PATTERN:
* AN, BN, CN AND DN WHERE N=15,14,... OR 3
* THEN ANY ONE OF THE FOUR CHIPS ASSOCIATED WITH EACH OF
* THE BITS AN, BN, CN AND DN COULD BE AT FAULT WITH
* EQUAL PROBABILITY.
*4.) IF 16 BITS ARE IN ERROR, FITTING THE PATTERN:
* AN, AN+1, AN+2, AN+3 WHERE N=0,4,8 OR 12
* BN, BN+1, BN+2, BN+3
* CN, CN+1, CN+2, CN+3
* AND
* DN, DN+1, DN+2, DN+3
* THEN ANY ONE OF THE FOUR CHIPS ASSOCIATED
* WITH THESE BITS COULD BE AT FAULT WITH EQUAL PROBABILITY.
*5.) IF THE FAILING BIT PATTERNS DISPLAYED IN THE 'AND' AND THE 'OR'
* DATA TYPED IN THE SUMMARY DOES NOT CONFORM EXPLICITELY TO
* ANY OF THE ABOVE PATTERNS, THEN THE TROUBLE SHOOTER MUST
* INTUITIVELY TRY TO FIND WHICH OF THE ABOVE CASES (1 THROUGH 4)
* IS A 'BEST FIT' OF THE SYMPTOMS.
*****

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011646 000004
011650 170011
011652 012737 042210 001244
011660 012737 011710 001236
011666 012700 014130
011672 012701 014170
011676 104413
011700 004737 013606
011704 012703 000102
011710 172410
011712 174000
011714 172400
011716 174011
011720 004737 013704

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*****
TST11: SCOPE
        SETD                                ;SET FD.
;TEST ACCUMULATOR D WITH FLOATING ONE
        MOV      #MNUMD,2#STMP5
        MOV      #G1,2#STMP2
        MOV      #GPAT00,R0
        MOV      #GDAT00,R1
        LPERR                                ;SET JP THE LOOP ON ERROR ADDRESS.
        JSR      PC,2#GSETUP                 ;LOAD TEST PATTERN.
        MOV      #102,R3
G1:     LDD      (R0),ACD
        STD      ACD,ACD
        LDD      ACD,ACD
        STD      ACD,(R1)
        JSR      PC,2#GCMP                    ;COMPARE THE DATA READ WITH
;THAT WHICH WAS WRITTEN.

```

# H05

32	011724	005737	014124		TST	2#GFLAG1	
33	011730	001004			BNE	G2	
34	011732	005137	014124		COM	2#GFLAG1	
35	011736	000261			SEC		
36	011740	000401			BR	G3	
37	011742	000241		G2:	CLC		
38	011744	006160	000006	G3:	ROL	6(R0)	;GENERATE THE NEXT TEST PATTERN.
39	011750	006160	000004		ROL	4(R0)	
40	011752	006160	000002		ROL	2(R0)	
41	011756	006110			ROL	(R0)	
42	011762	004737	013664		JSR	PC, 2#GRESET	;RESET DEFAULT PATTERN IN OUTPUT ;BUFFER.
43	011766	077330			SOB	R3, G1	
44	011770	004737	014022		JSR	PC, 2#GSUM	;TYPE ERROR SUMMARY.
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;TEST ACCUMULATOR 0 WITH FLOATING ZERO

;SET UP THE LOOP ON ERROR ADDRESS.  
;LOAD TEST PATTERN.

;STORE THE TEST PATTERN.

;COMPARE THE DATA READ WITH  
;THAT WHICH WAS WRITTEN.

;GENERATE THE NEXT TEST PATTERN.

;RESET DEFAULT PATTERN IN OUTPUT  
;BUFFER.

;TYPE ERROR SUMMARY.

;TEST ACCUMULATOR 1 WITH FLOATING ONE

;SET UP THE LOOP ON ERROR ADDRESS.  
;LOAD TEST PATTERN.

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3258 012160 172401 LDD AC1,ACD ;STORE THE TEST PATTERN.
3259 012162 174011 STD ACD,(R1)
3260 012164 004737 013704 JSR PC,#GCMPC ;COMPARE THE DATA READ WITH
;THAT WHICH WAS WRITTEN.
3261 012170 005737 014124 TST #GFLAG1
3262 012174 001004 BNE G10
3263 012176 005137 014124 COM #GFLAG1
3264 012202 000261 SEC
3265 012204 000401 BR G11
3266 012206 000241 G10: CLC
3267 012210 006160 000006 G11: ROL 6(R0) ;GENERATE THE NEXT TEST PATTERN.
3268 012214 006160 000004 ROL 4(R0)
3269 012220 006160 000002 ROL 2(R0)
3270 012224 006110 ROL (R0)
3271 012226 004737 013664 JSR PC,#GRESET ;RESET DEFAULT PATTERN IN OUTPUT
;BUFFER.
3272 012232 077330 SOB R3,G7
3273 012234 004737 014022 JSR PC,#GSUM ;TYPE ERROR SUMMARY.
3274 012240 012737 042216 001244 ;TEST ACCUMULATOR 1 WITH FLOATING ZERO
3275 012246 012737 012276 001236 MOV #MNUM1,#STMP5
3276 012254 012700 014140 MOV #G12,#STMP2
3277 012260 012701 014170 MOV #GPAT00,R0
3278 012264 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
3279 012266 004737 013606 JSR PC,#GSETUP ;LOAD TEST PATTERN.
3280 012272 012703 000102 MOV #102,R3
3281 012276 172410 G12: LDD (R0),ACD
3282 012276 174001 STD ACD,AC1
3283 012300 172401 LDD AC1,ACD ;STORE THE TEST PATTERN.
3284 012302 174011 STD ACD,(R1)
3285 012306 004737 013704 JSR PC,#GCMPC ;COMPARE THE DATA READ WITH
;THAT WHICH WAS WRITTEN.
3286 012312 005737 014124 TST #GFLAG1
3287 012316 001004 BNE G13
3288 012320 005137 014124 COM #GFLAG1
3289 012324 000241 CLC
3290 012326 000401 BR G14
3291 012330 000261 G13: SEC
3292 012332 006160 000006 G14: ROL 6(R0) ;GENERATE THE NEXT TEST PATTERN.
3293 012336 006160 000004 ROL 4(R0)
3294 012342 006160 000002 ROL 2(R0)
3295 012346 006110 ROL (R0)
3296 012350 004737 013664 JSR PC,#GRESET ;RESET DEFAULT PATTERN IN OUTPUT
;BUFFER.
3297 012354 077330 SOB R3,G12
3298 012356 004737 014022 JSR PC,#GSUM ;TYPE ERROR SUMMARY.
3299 012362 012737 042223 001244 ;TEST ACCUMULATOR 2 WITH FLOATING ONE
3300 012370 012737 012420 001236 MOV #MNUM2,#STMP5
3301 012376 012700 014130 MOV #G15,#STMP2
3302 012402 012701 014170 MOV #GPAT00,R0
3303 012406 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
3304 012410 004737 013606 JSR PC,#GSETUP ;LOAD TEST PATTERN.
    
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# J05

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3314 012703 000102      MOV      #102,R3
3315 012704 172410      G15:    LDD      (R0),AC0
3316 012705 174002      STD      AC0,AC2
3317 012706 172402      LDD      AC2,AC0      ;STORE THE TEST PATTERN.
3318 012707 174011      STD      AC0,(R1)
3319 012708 004737 013704      JSR      PC,@#GCMP      ;COMPARE THE DATA READ WITH
3320 012709 005737 014124      TST      @#GFLAG1      ;THAT WHICH WAS WRITTEN.
3321 012710 001004      BNE      G16
3322 012711 005137 014124      COM      @#GFLAG1
3323 012712 000261      SEC
3324 012713 000401      BR       G17
3325 012714 000241      G16:    CLC
3326 012715 006160 000006      G17:    ROL      6(R0)      ;GENERATE THE NEXT TEST PATTERN.
3327 012716 006160 000004      ROL      4(R0)
3328 012717 006160 000002      ROL      2(R0)
3329 012718 006110      ROL      (R0)
3330 012719 004737 013664      JSR      PC,@#GRESET      ;RESET DEFAULT PATTERN IN OUTPUT
3331 012720 077330      SOB      R3,G15          ;BUFFER.
3332 012500 004737 014022      JSR      PC,@#GSUM      ;TYPE ERROR SUMMARY.
3333 012504 012737 042223 001244      ;TEST ACCUMULATOR 2 WITH FLOATING ZERO
3334 012512 012737 012542 001236      MOV      #MNUM2,@#STMP5
3335 012520 012700 014140      MOV      #G20,@#STMP2
3336 012524 012701 014170      MOV      #GPAT10,R0
3337 012530 104413      MOV      #GDAT00,R1
3338 012532 004737 013606      LPERR    JSR      PC,@#GSETUP      ;SET UP THE LOOP ON ERROR ADDRESS.
3339 012536 012703 000102      MOV      #102,R3          ;LOAD TEST PATTERN.
3340 012542 172410      G20:    LDD      (R0),AC0
3341 012544 174002      STD      AC0,AC2
3342 012546 172402      LDD      AC2,AC0      ;STORE THE TEST PATTERN.
3343 012550 174011      STD      AC0,(R1)
3344 012552 004737 013704      JSR      PC,@#GCMP      ;COMPARE THE DATA READ WITH
3345 012556 005737 014124      TST      @#GFLAG1      ;THAT WHICH WAS WRITTEN.
3346 012562 001004      BNE      G21
3347 012564 005137 014124      COM      @#GFLAG1
3348 012570 000241      CLC
3349 012572 000401      BR       G22
3350 012574 000261      G21:    SEC
3351 012576 006160 000006      G22:    ROL      6(R0)      ;GENERATE THE NEXT TEST PATTERN.
3352 012602 006160 000004      ROL      4(R0)
3353 012606 006160 000002      ROL      2(R0)
3354 012612 006110      ROL      (R0)
3355 012614 004737 013664      JSR      PC,@#GRESET      ;RESET DEFAULT PATTERN IN OUTPUT
3356 012620 077330      SOB      R3,G20          ;BUFFER.
3357 012622 004737 014022      JSR      PC,@#GSUM      ;TYPE ERROR SUMMARY.
3358 012626 012737 042230 001244      ;TEST ACCUMULATOR 3 WITH FLOATING ONE
3359 012634 012737 012664 001236      MOV      #MNUM3,@#STMP5
3360 012634 012737 012664 001236      MOV      #G23,@#STMP2
  
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# K05

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3370	012642	012700	014130		MOV	#GPAT00,R0	
3371	012646	012701	014170		MOV	#GDAT00,R1	
3372	012652	104413			LPERR		;SET UP THE LOOP ON ERROR ADDRESS.
3373	012654	004737	013606		JSR	PC,#GSETUP	;LOAD TEST PATTERN.
3374	012660	012703	000102		MOV	#102,R3	
3375	012664			G23:			
3376	012664	172410			LDD	(R0),AC0	
3377	012666	174003			STD	AC0,AC3	
3378	012670	172403			LDD	AC3,AC0	;STORE THE TEST PATTERN.
3379	012672	174011			STD	AC0,(R1)	
3380	012674	004737	013704		JSR	PC,#GCMP	;COMPARE THE DATA READ WITH ;THAT WHICH WAS WRITTEN.
3381							
3382	012700	005737	014124		TST	#GFLAG1	
3383	012704	001004			BNE	G24	
3384	012706	005137	014124		COM	#GFLAG1	
3385	012712	000261			SEC		
3386	012714	000401			BR	G25	
3387	012716	000241		G24:	CLC		
3388	012720	006160	000006	G25:	ROL	6(R0)	;GENERATE THE NEXT TEST PATTERN.
3389	012724	006160	000004		ROL	4(R0)	
3390	012730	006160	000002		ROL	2(R0)	
3391	012734	006110			ROL	(R0)	
3392	012736	004737	013664		JSR	PC,#GRESET	;RESET DEFAULT PATTERN IN OUTPUT ;BUFFER.
3393							
3394	012742	077330			SOB	R3,G23	
3395	012744	004737	014022		JSR	PC,#GSUM	;TYPE ERROR SUMMARY.
3396							
3397							
3398	012750	012737	042230	001244			;TEST ACCUMULATOR 3 WITH FLOATING ZERO
3399	012756	012737	013006	001236	MOV	#MNUM3,#STMP5	
3400	012764	012700	014140		MOV	#G26,#STMP2	
3401	012770	012701	014170		MOV	#SPAT10,R0	
3402	012774	104413			MOV	#GDAT00,R1	
3403	012776	004737	013606		LPERR		;SET UP THE LOOP ON ERROR ADDRESS.
3404	013002	012703	000102		JSR	PC,#GSETUP	;LOAD TEST PATTERN.
3405	013006			G26:	MOV	#102,R3	
3406	013006	172410			LDD	(R0),AC0	
3407	013010	174003			STD	AC0,AC3	
3408	013012	172403			LDD	AC3,AC0	;STORE THE TEST PATTERN.
3409	013014	174011			STD	AC0,(R1)	
3410	013016	004737	013704		JSR	PC,#GCMP	;COMPARE THE DATA READ WITH ;THAT WHICH WAS WRITTEN.
3411							
3412	013022	005737	014124		TST	#GFLAG1	
3413	013026	001004			BNE	G27	
3414	013030	005137	014124		COM	#GFLAG1	
3415	013034	000241			CLC		
3416	013036	000401			BR	G30	
3417	013040	000261		G27:	SEC		
3418	013042	006160	000006	G30:	ROL	6(R0)	;GENERATE THE NEXT TEST PATTERN.
3419	013046	006160	000004		ROL	4(R0)	
3420	013052	006160	000002		ROL	2(R0)	
3421	013056	006110			ROL	(R0)	
3422	013060	004737	013664		JSR	PC,#GRESET	;RESET DEFAULT PATTERN IN OUTPUT ;BUFFER.
3423							
3424	013064	077330			SOB	R3,G26	
3425	013066	004737	014022		JSR	PC,#GSUM	;TYPE ERROR SUMMARY.

# L05

```

;TEST ACCUMULATOR 4 WITH FLOATING ONE
013072 012737 042237 001244      MOV      #NUM4,2#STMP5
013100 012737 013130 001236      MOV      #G31,2#STMP2
013106 012700 014130      MOV      #GPAT00,R0
013112 012701 014170      MOV      #GDAT00,R1
013116 104413      LPERR
013120 004737 013606      JSR      PC,2#GSETUP      ;SET UP THE LOOP ON ERROR ADDRESS.
013124 012703 000102      MOV      #102,R3      ;LOAD TEST PATTERN.
013130      G31:
013130 172410      LDD      (R0),AC0
013132 174004      STD      AC0,AC4
013134 172404      LDD      AC4,AC0      ;STORE THE TEST PATTERN.
013136 174011      STD      AC0,(R1)
013140 004737 013704      JSR      PC,2#GCMP      ;COMPARE THE DATA READ WITH
                                ;THAT WHICH WAS WRITTEN.
013144 005737 014124      TST      2#GFLAG1
013150 001004      BNE      G32
013152 005137 014124      COM      2#GFLAG1
013156 000261      SEC
013160 000401      BR       G33
013162 000241      G32: CLC
013164 006160 000006      G33: ROL      6(R0)      ;GENERATE THE NEXT TEST PATTERN.
013170 006160 000004      ROL      4(R0)
013174 006160 000002      ROL      2(R0)
013200 006110      ROL      (R0)
013202 004737 013664      JSR      PC,2#GRESET      ;RESET DEFAULT PATTERN IN OUTPUT
                                ;BUFFER.
013206 077330      SOB      R3,G31
013210 004737 014022      JSR      PC,2#GSUM      ;TYPE ERROR SUMMARY.

;TEST ACCUMULATOR 4 WITH FLOATING ZERO
013214 012737 042237 001244      MOV      #NUM4,2#STMP5
013222 012737 013252 001236      MOV      #G34,2#STMP2
013230 012700 014140      MOV      #GPAT10,R0
013234 012701 014170      MOV      #GDAT00,R1
013240 104413      LPERR
013242 004737 013606      JSR      PC,2#GSETUP      ;SET UP THE LOOP ON ERROR ADDRESS.
013246 012703 000102      MOV      #102,R3      ;LOAD TEST PATTERN.
013252      G34:
013252 172410      LDD      (R0),AC0
013254 174004      STD      AC0,AC4
013256 172404      LDD      AC4,AC0      ;STORE THE TEST PATTERN.
013260 174011      STD      AC0,(R1)
013262 004737 013704      JSR      PC,2#GCMP      ;COMPARE THE DATA READ WITH
                                ;THAT WHICH WAS WRITTEN.
013266 005737 014124      TST      2#GFLAG1
013272 001004      BNE      G35
013274 005137 014124      COM      2#GFLAG1
013300 000241      CLC
013302 000401      BR       G36
013304 000261      G35: SEC
013306 006160 000006      G36: ROL      6(R0)      ;GENERATE THE NEXT TEST PATTERN.
013312 006160 000004      ROL      4(R0)
013316 006160 000002      ROL      2(R0)
013322 006110      ROL      (R0)
  
```



# M05

MAINDEC-11-DFFPA-A PDP 11 34 FPP DIAGNOSTIC PART 1 MACY11.27(1006) 01-NOV-76 21:09 PAGE 64  
 DFFPA.P11 01-NOV-76 21:03 T11 ACCUMULATORS DATA PATTERNS TEST

```

013324 004737 013664 JSR PC, @GRESET ;RESET DEFAULT PATTERN IN OUTPUT
;BUFFER.
013330 077330 SOB R3, G34
013332 004737 014022 JSR PC, @GSUM ;TYPE ERROR SUMMARY.

;TEST ACCUMULATOR 5 WITH FLOATING ONE
013336 012737 042245 001244 MOV #MNUM5, @STMP5
013344 012737 013374 001236 MOV #G37, @STMP2
013352 012700 014130 MOV #GPAT00, R0
013356 012701 014170 MOV #GDAT00, R1
013362 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
013364 004737 013606 JSR PC, @GSETUP ;LOAD TEST PATTERN.
013370 012703 000102 MOV #102, R3
G37: LDD (R0), ACO
STD ACO, AC5
LDD AC5, ACO ;STORE THE TEST PATTERN.
STD ACO, (R1)
JSR PC, @GCMP ;COMPARE THE DATA READ WITH
;THAT WHICH WAS WRITTEN.

013410 005737 014124 TST @GFLAG1
013414 001004 BNE G40
013416 005137 014124 COM @GFLAG1
013422 000261 SEC
013424 000401 BR G41
013426 000241 G40: CLC
G41: ROL 6(R0) ;GENERATE THE NEXT TEST PATTERN.
ROL 4(R0)
ROL 2(R0)
ROL (R0)
JSR PC, @GRESET ;RESET DEFAULT PATTERN IN OUTPUT
;BUFFER.
013452 077330 SOB R3, G37
013454 004737 014022 JSR PC, @GSUM ;TYPE ERROR SUMMARY.

;TEST ACCUMULATOR 5 WITH FLOATING ZERO
013460 012737 042245 001244 MOV #MNUM5, @STMP5
013466 012737 013516 001236 MOV #G42, @STMP2
013474 012700 014140 MOV #GPAT10, R0
013500 012701 014170 MOV #GDAT00, R1
013504 104413 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
013506 004737 013606 JSR PC, @GSETUP ;LOAD TEST PATTERN.
013512 012703 000102 MOV #102, R3
G42: LDD (R0), ACO
STD ACO, AC5
LDD AC5, ACO ;STORE THE TEST PATTERN.
STD ACO, (R1)
JSR PC, @GCMP ;COMPARE THE DATA READ WITH
;THAT WHICH WAS WRITTEN.

013532 005737 014124 TST @GFLAG1
013536 001004 BNE G43
013540 005137 014124 COM @GFLAG1
013544 000241 CLC
013546 000401 BR G44
013550 000261 G43: SEC
  
```

# N05

MACY11-11-DFFPR-A PDP 11 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 65  
 DFFPR.F11 01-NOV-76 21:03 T11 ACCUMULATORS DATA PATTERNS TEST

3538	013552	006160	000006	G44:	ROL 6(R0)	:GENERATE THE NEXT TEST PATTERN.
3539	013556	006160	000004		ROL 4(R0)	
3540	013562	006160	000002		ROL 2(R0)	
3541	013566	006110			ROL (R0)	
3542	013570	004737	013664		JSR PC, 2#GRESET	:PESET DEFAULT PATTERN IN OUTPUT :BUFFER.
3543	013574	007330			SOB R3, G42	
3544	013576	004737	014022		JSR PC, 2#GSLM	:TYPE ERROR SUMMARY.
3545	013602	000137	014202		JMP 2#GDONE	
3546	013606	012705	014124		:USE THIS ROUTINE TO INITIALIZE ALL THE DATA BUFFERS.	
3547	013612	012704	000026	GSETUP:	MOV #GFLAG1, R5	
3548	013616	005025		1\$:	MOV #26, R4	
3549	013620	077402			CLR (R5)+	
3550	013622	012705	014140		SOB R4, 1\$	
3551	013626	012704	000010		MOV #GPAT10, R5	
3552	013632	005125		2\$:	MOV #10, R4	
3553	013634	077402			COM (R5)+	
3554	013636	020067	000266		SOB R4, 2\$	
3555	013642	001401		G\$1:	CMP R0, GPAT00	
3556	013644	000207			BEQ 3\$	
3557	013646	012705	014170		RTS PC	
3558	013652	012704	000004	3\$:	MOV #GDAT00, R5	
3559	013656	005125		4\$:	MOV #4, R4	
3560	013660	077402			COM (R5)+	
3561	013662	000207			SOB R4, 4\$	
3562	013664	012705	014170		RTS PC	
3563	013670	012704	000004	GRESET:	MOV #GDAT02, R5	
3564	013674	005025		1\$:	MOV #4, R4	
3565	013676	077402			CLR (R5)+	
3566	013700	000137	013636		SOB R4, 1\$	
3567	013704	012705	014170		JMP 2#G\$1	
3568	013710	012704	000004		:SEE IF THE DATA WRITTEN MATCHES THE DATA READ.	
3569	013714	010002		GCOMP:	MOV #GDAT00, R5	
3570	013716	022225		1\$:	MOV #4, R4	
3571	013720	001402			MOV R0, R2	
3572	013722	000137	013732		CMP (R2)+, (R5)+	
3573	013726	077405			BEQ 2\$	
3574	013730	000207		2\$:	JMP 2#GERR1	
3575	013732	012637	014200		SOB R4, 1\$	
3576	013736	010003			RTS PC	
3577	013740	012705	014160		:COME HERE TO REPORT AND RECORD ERRORS.	
3578	013744	012704	000004	GERR1:	MOV (SP)+, 2#GADR	:SAVE THE RETURN ADDRESS.
3579	013748	052325			MOV R0, R3	:COMPUTE 'OR' OF BAD DATA.
3580	013750	077402		1\$:	MOV #GORD, R5	
3581	013752	077402			MOV #4, R4	
3582	013754	077402			BIS (R3)+, (R5)+	
3583	013756	077402			SOB R4, 1\$	



```

3653 014164 000000
3654 014166 000000
3655 014170 000000
3656 014172 000000
3657 014174 000000
3658 014176 000000
3659 014200 000000
3660 014202
3661 014202 104412
3662
3663
3664
3665
3666
3667
3668
3669
3670
3671
3672
3673
3674
3675 014204 000004
3676 014206 104413
3677
3678 014210 005037 014734
3679 014214 012700 014736
3680 014220 012701 015056
3681 014224 012703 000024
3682 014230 012120
3683 014232 077302
3684
3685 014234 004767 000422
3686
3687 014240 170011
3688
3689 014242 012700 014736
3690 014246 172410
3691 014250 174001
3692
3693 014252 012700 014746
3694 014256 172410
3695 014260 174002
3696
3697 014262 012700 014756
3698 014266 172410
3699 014270 174003
3700
3701 014272 012700 014766
3702 014276 172410
3703 014300 174004
3704
3705 014302 012700 014776

```

```

GOR2: 0
GOR3: 0
GDAT00: 0
GDAT01: 0
GDAT02: 0
GDAT03: 0
GADR: 0
GOCNE:
RSET JF

```

```

:GO INITIALIZE THE FPS AND STACK; AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER (HAS
:THE USER TYPED CONTROL G?).

```

```

:*****
:TEST 12 FPP ACCUMULATORS DUAL ADDRESS TEST
:

```

```

:THIS TEST PERFORMS A DUAL ADDRESSING TEST ON THE FLOATING ACCUMULATORS.
:NOTE THAT ACCUMULATOR ZERO IS USED TO ACCESS ALL THE OTHERS.
:

```

```

:*****
:ST12: SCOPE
LPERR

```

```

:SET UP THE LOOP ON ERROR ADDRESS.

```

```

H1: CLR ZHFFLAG
MOV #HA1W,RO
MOV #HDAT1,R1
MOV #24,R3
H2: MOV (R1)+,(RO)+
SOB R3,H2

```

```

:INITIALIZE THE LOAD BUFFER DATA.

```

```

JSR PC,HCLR

```

```

:CLEAR THE OUTPUT DATA BUFFER.

```

```

H3: SETD
:LOAD ACCUMULATOR 1
MOV #HA1W,RO
LDD (RO),AC0
STD AC0,AC1
:LOAD ACCUMULATOR 2
MOV #HA2W,RO
LDD (RO),AC0
STD AC0,AC2
:LOAD ACCUMULATOR 3
MOV #HA3W,RO
LDD (RO),AC0
STD AC0,AC3
:LOAD ACCUMULATOR 4
MOV #HA4W,RO
LDD (RO),AC0
STD AC0,AC4
:LOAD ACCUMULATOR 5
MOV #HA5W,RO

```

3706	014306	172410		LDD	(R0),AC0	
3707	014310	174005		STD	AC0,AC5	
3708						
3709	014312	004737	014546	H4:	JSR	PC,2#HSTD ;GO READ ALL ACCUMULATORS BACK.
3710						
3711	014316	004737	014624		JSR	PC,2#HCMP ;SEE IF DATA IS CORRECT.
3712						
3713						
3714						
3715						
3716	014322	012700	014736		MOV	#HA1W,R0
3717	014326	012702	000004		MOV	#4,R2
3718	014332	010001			MOV	R0,R1
3719	014334	005121		H5:	COM	(R1)+
3720	014336	172410			LDD	(R0),AC0
3721	014340	174001			STD	AC0,AC1
3722	014342	004737	014546		JSR	PC,2#HSTD ;READ ALL THE ACCUMULATORS BACK.
3723	014346	004737	014624		JSR	PC,2#HCMP ;CHECK THE DATA.
3724	014352	077210			SOB	R2,H5
3725						
3726						
3727						
3728						
3729						
3730	014354	012700	014746		MOV	#HA2W,R0
3731	014360	012702	000004		MOV	#4,R2
3732	014364	010001			MOV	R0,R1
3733	014366	005121		H6:	COM	(R1)+
3734	014370	172410			LDD	(R0),AC0
3735	014372	174002			STD	AC0,AC2
3736	014374	004737	014546		JSR	PC,2#HSTD ;READ ALL THE ACCUMULATORS BACK.
3737	014400	004737	014624		JSR	PC,2#HCMP ;CHECK THE DATA.
3738	014404	077210			SOB	R2,H6
3739						
3740						
3741						
3742	014406	012700	014756		MOV	#HA3W,R0
3743	014412	012702	000004		MOV	#4,R2
3744	014416	010001			MOV	R0,R1
3745	014420	005121		H7:	COM	(R1)+
3746	014422	172410			LDD	(R0),AC0
3747	014424	174003			STD	AC0,AC3
3748	014426	004737	014546		JSR	PC,2#HSTD ;READ ALL THE ACCUMULATORS BACK.
3749	014432	004737	014624		JSR	PC,2#HCMP ;CHECK THE DATA.
3750	014436	077210			SOB	R2,H7
3751						
3752						
3753						
3754						
3755	014440	012700	014766		MOV	#HA4W,R0
3756	014444	012702	000004		MOV	#4,R2
3757	014450	010001			MOV	R0,R1
3758	014452	005121		H10:	COM	(R1)+
3759	014454	172410			LDD	(R0),AC0
3760	014456	174004			STD	AC0,AC4
3761	014460	004737	014546		JSR	PC,2#HSTD ;READ ALL THE ACCUMULATORS BACK.

# E06

3762	014464	004737	014624	JSR	PC, @#HCMP	;CHECK THE DATA.
3763	014470	077210		SQB	R2, H10	
3764						
3765						
3766						
3767						
3768	014472	012700	014776			
3769	014476	012702	000004			
3770	014502	010001				
3771	014504	005121		H11:	COM	
3772	014506	172410			(R1)+	
3773	014510	174005			LDD	(R0), ACO
3774	014512	004737	014546		STD	ACO, ACS
3775	014516	004737	014624		JSR	PC, @#HSTD
3776	014522	077210			JSR	PC, @#HCMP
3777					SQB	R2, H11
3778						
3779	014524	005737	014734		TST	@#HFLAG
3780	014530	001402			BEQ	H12
3781	014532	000137	015126		JMP	@#HDONE
3782						
3783	014536	005137	014734	H12:	COM	@#HFLAG
3784	014542	000137	014240		JMP	@#H3
3785						
3786						
3787	014546	004737	014662			
3788						
3789	014552	012704	015006			
3790	014556	172401				
3791	014560	174014				
3792						
3793	014562	012704	015016			
3794	014566	172402				
3795	014570	174014				
3796						
3797	014572	012704	015026			
3798	014576	172403				
3799	014600	174014				
3800						
3801	014602	012704	015036			
3802	014606	172404				
3803	014610	174014				
3804						
3805	014612	012704	015046			
3806	014616	172405				
3807	014620	174014				
3808	014622	000207				
3809						
3810						
3811	014624	012637	014732			
3812	014630	012703	014736			
3813	014634	012704	015006			
3814	014640	012705	000024			
3815	014644	022324				
3816	014646	001402				
3817	014650	000137	014700			

;COMPLIMENT EACH WORD OF THE DATA STORED IN ACCUMULATOR 5  
 ;RELOAD THAT ACCUMULATOR, READ ALL THE ACCUMULATORS BACK AND CHECK  
 ;THE DATA.

MOV @#ASW, R0  
 MOV #4, R2  
 MOV R0, R1  
 H11: COM (R1)+  
 LDD (R0), ACO  
 STD ACO, ACS  
 JSR PC, @#HSTD ;READ ALL THE ACCUMULATORS BACK.  
 JSR PC, @#HCMP ;CHECK THE DATA.  
 SQB R2, H11

;STORE ALL ACCUMULATORS IN THE OUTPUT BUFFERS.  
 HSTD: JSR PC, @#HCLR ;CLEAR ALL OUTPUT BUFFERS.  
 ;STORE ACCUMULATOR 1

MOV @#A1R, R4  
 LDD AC1, ACO  
 STD ACO, (R4)

;STORE ACCUMULATOR 2  
 MOV @#A2R, R4  
 LDD AC2, ACO  
 STD ACO, (R4)

;STORE ACCUMULATOR 3  
 MOV @#A3R, R4  
 LDD AC3, ACO  
 STD ACO, (R4)

;STORE ACCUMULATOR 4  
 MOV @#A4R, R4  
 LDD AC4, ACO  
 STD ACO, (R4)

;STORE ACCUMULATOR 5  
 MOV @#A5R, R4  
 LDD AC5, ACO  
 STD ACO, (R4)

RTS PC

;COMPARE DATA LOADED WITH DATA READ.  
 HCMP: MOV (SP)+, @#HADR ;SAVE RETURN ADDRESS.  
 MOV @#A1W, R3  
 MOV @#A1R, R4

MOV #24, R5  
 HCMP1: CMP (R3)+, (R4)+  
 BEQ HCMP2  
 JMP @#HERROR

F06

3818	014654	077505			MCMP2: SOB	R5, MCMP1
3819	014656	000177	000050		JMP	2HADR
3820						
3821					:CLEAR THE DATA	OUTPUT BUFFER.
3822	014662	012704	015006		HCLR: MOV	#HA1R, R4
3823	014666	012705	000024			#24, R5
3824	014672	005024			HCLR1: CLR	(R4)+
3825	014674	077502				R5, HCLR1
3826	014676	000207			RTS	PC
3827						
3828					:REPORT ERROR.	
3829	014700				HERROR:	
3830	014700	012703	014736		MOV	#HA1W, R3
3831	014704	012704	001236		MOV	#STMP2, R4
3832	014710	012705	000012		MOV	#12, R5
3833	014714	010324			15: MOV	R3, (R4)+
3834	014716	062703	000010		ADD	#10, R3
3835	014722	077504			SOB	R5, 15
3836	014724	104046			25: ERROR	46
3837	014726	000137	015126		JMP	2#HOONE
3838						
3839						
3840	014732	000000			HADR: 0	
3841	014734	000000			HFLAG: 0	
3842						
3843	014736	000000	000000	000000	HA1W: .WORD	0,0,0,0
3844	014744	000000				
3845	014746	000000	000000	000000	HA2W: .WORD	0,0,0,0
3846	014754	000000				
3847	014756	000000	000000	000000	HA3W: .WORD	0,0,0,0
3848	014764	000000				
3849	014766	000000	000000	000000	HA4W: .WORD	0,0,0,0
3850	014774	000000				
3851	014776	000000	000000	000000	HA5W: .WORD	0,0,0,0
3852	015004	000000				
3853						
3854						
3855	015006	000000	000000	000000	HA1R: .WORD	0,0,0,0
3856	015014	000000				
3857	015016	000000	000000	000000	HA2R: .WORD	0,0,0,0
3858	015024	000000				
3859	015026	000000	000000	000000	HA3R: .WORD	0,0,0,0
3860	015034	000000				
3861	015036	000000	000000	000000	HA4R: .WORD	0,0,0,0
3862	015044	000000				
3863	015046	000000	000000	000000	HA5R: .WORD	0,0,0,0
3864	015054	000000				
3865						
3866	015056	073567	073567	073567	HDATA1: .WORD	73567,73567,73567,73567
3867	015064	073567				
3868	015066	063146	063146	063146	HDATA2: .WORD	63146,63146,63146,63146
3869	015074	063146				
3870	015076	010421	010421	010421	HDATA3: .WORD	10421,10421,10421,10421
3871	015104	010421				
3872	015106	031463	031463	031463	HDATA4: .WORD	31463,31463,31463,31463
3873	015114	031463				
3874	015116	042104	042104	042104	HDATA5: .WORD	42104,42104,42104,42104

015124 042104  
015126 104412  
015130 000004  
015132 104413  
015134 170011  
015136 012700 015642  
015142 172410  
015144 012737 015342 000244  
015152 012700 000001  
015156 012737 015552 000004  
015164 005003  
015166 172407  
015170 170000  
015172 005203  
015174 005203  
015176 012701 015652  
015202 174011  
015204 012701 015652  
015210 012702 015642  
015214 012703 000004  
015220 022122  
015222 001402  
015224 000137 015502  
015230 077305  
015232 000137 015526  
015236  
015238 104413  
015240 170011

MDONE:

RSETUP

:GO INITIALIZE THE FPS AND STACK; AND  
:SEE IF THE USER HAS EXPRESSED  
:THE DESIRE TO CHANGE THE SOFTWARE  
:VIRTUAL CONSOLE SWITCH REGISTER HAS  
:THE USER TYPED CONTROL G?).

\*\*\*\*\*  
TEST 13 FSRC MODE 0 WITH ILLEGAL ACCUMULATOR TEST  
\*\*\*\*\*

\*THIS IS A TEST OF FSRC MODE 0 WITH ACCUMULATORS 6 AND 7. USE OF  
\*EITHER OF THESE NON-EXISTENT ACCUMULATORS SHOULD RESULT IN A TRAP TO 244  
\*WITH FEC=2 (ILLEGAL FPP INSTRUCTION).  
\*

\*\*\*\*\*  
TEST 13: SCOPE  
S1:

LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
SETD ;SET FD  
MOV #SPAT10,R0 ;LOAD ACO  
LDD (R0),ACO

MOV #SERR0,2#FPVECT ;USE OF THE NON-EXISTENT AC-  
;CUMULATOR SHOULD RESULT IN  
;A TRAP TO 244.  
MOV #1,R0 ;A FAILURE IN THE FSRC FLOWS  
;WILL RESULT IN AN ODD ADDRESS  
CLR R3 ;TRAP TO 4.

S2: LDD AC7,ACO  
S3: CFCC  
INC R3  
S4: INC R3

MOV #SDAT00,R1 ;NO TRAP OCCURRED!!  
STD ACO,(R1) ;SEE IF ACO WAS MODIFIED.

MOV #SDAT00,R1  
MOV #SPAT10,R2  
MOV #4,R3  
S5: CMP (R1)+,(R2)+  
BEQ S6  
JMP 2#SERR2  
S6: SOB R3,S5

JMP 2#SERR3

:NOW TEST AC6.

S7: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
SETC



H06

3933	015242	012700	015642		MOV	#SPAT10,R0		:LOAD ACO
3934	01524E	172410			LDD	(R0),ACO		
3935	015250	012737	015420	000244	MOV	#SERR4,#FPVECT		
3936	015256	012700	000001		MOV	#1,R0		
3937	015262	012737	015634	000004	MOV	#SERR5,#ERRVECT		
3938	015270	005003			CLR	R3		
3939	015272	172406			S8: LDD	ACC,ACO		
3940	015274	170000			S9: CFCC			
3941	015276	005203			INC	R3		
3942	015300	005203			S10: INC	R3		
3943	015302	012701	015652		MOV	#SDATOC,R1		
3944	015306	174011			STD	ACO,(R1)		:NO TRAP! GET ACO.
3945	015310	012701	015652		MOV	#SDATOC,R1		:WAS ACO MODIFIED.
3946	015314	012702	015642		MOV	#SPAT10,R2		
3947	015320	012703	000004		MOV	#4,R3		
3948	015324	022122			S11: CMP	(R1)+,(R2)+		
3949	015326	001402			BEQ	S12		
3950	015330	000137	015514		JMP	#SERR6		
3951	015334	077305			S12: SOB	R3,S11		
3952	015336	000137	015540		JMP	#SERR7		
3953								
3954								
3955								
3956								
3957	015342	021627	015170		:TRAPPED TO 244. SERR0: CMP	(SP),#S3		:PC OF TRAP CORRECT?
3958	015346	001402			BEQ	15		
3959	015350	000137	040200		JMP	#FPSPUR		
3960	015354	012737	015236	015636	15: MOV	#S7,#SADR		
3961	015362	011637	001236		SERR10: MOV	(SP),#STMP2		
3962	015366	022626			CMP	(SP)+,(SP)+		
3963	015370	005004			CLR	R4		
3964	015372	170204			STFPS	R4		:IS FPS CORRECT?
3965	015374	022704	100200		CMP	#100200,R4		
3966	015400	001020			BNE	SERR15		
3967								
3968								
3969	015402	005004			CLR	R4		
3970	015404	170304			STST	R4		:IS FEC CORRECT?
3971	015406	022704	000002		CMP	#2,R4		
3972	015412	001023			BNE	SERR20		
3973	015414	000177	000216		JMP	#SADR		
3974								
3975	015420	021627	015274		SERR4: CMP	(SP),#S9		
3976	015424	001402			BEQ	15		
3977	015426	000137	040200		JMP	#FPSPUR		
3978	015432	012737	015662	015636	15: MOV	#SDONE,#SADR		
3979	015440	000750			BR	SERR10		
3980								
3981								
3982	015442	012737	100200	001242	:REPORT FPS FAILURE: SERR15: MOV	#100200,#STMP4		
3983	015450	010437	001240		MOV	R4,#STMP3		
3984	015454	104117			15: ERROR	117		
3985	015456	000177	000154		JMP	#SADR		

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021042  
031463  
042104  
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:REPORT FEC BAD:  
SERR20: MOV #2,2#STMP4  
MOV R4,2#STMP3  
IS: ERROR 120  
JMP @SADR  
  
:ACC WAS MODIFIED. (BUT FSRC) FORK FAILED.  
SERR2: MOV #52,2#STMP2  
IS: ERROR 112  
BR SDONE  
SERP6: MOV #58,2#STMP2  
IS: ERROR 114  
BR SDONE  
SERR3: MOV #52,2#STMP2  
IS: ERROR 111  
BR SDONE  
SERR7: MOV #58,2#STMP2  
IS: ERROR 113  
BR SDONE  
  
:FAILURE OF (BUT FSRC) CAUSED AN ODD ADDRESS TRAP TO 4.  
SERR1: CMP (SP),#53  
BEQ 15  
CMP (SP),#54  
BEQ 15  
JMP @CPSPUR  
  
IS: MOV (SP),2#STMP2  
CMP (SP),+(SP)+  
2S: ERROR 115  
BR SDONE  
  
SERP5: CMP (SP),#58  
BEQ 15  
CMP (SP),#59  
BEQ 15  
JMP @CPSPUR  
  
IS: MOV (SP),2#STMP2  
CMP (SP),+(SP)+  
2S: ERROR 116  
BR SDONE  
  
SADR: 0  
-1  
SPAT10: 10421  
SPAT11: 21042  
SPAT12: 31463  
SPAT13: 42104  
  
SDAT00: 0  
SDAT01: 0  
SDAT02: 0

:DID TRAP OCCUR ON TESTED INSTRUCTION?  
:DID TRAP OCCUR ON TEST INSTRUCTION?

# JOB

411NOV-11-05FPA-A PDP 11 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 74  
 05FPA.P11 01-NOV-76 21:03 T13 FSRC MODE 0 WITH ILLEGAL ACCUMULATOR TEST

```

4070 015660 000000          SDAT03: 0
4071 015662          SDONE:
4072 015662 104412          RSETUP
                                ;GO INITIALIZE THE FPS AND STACK; AND
                                ;SEE IF THE USER HAS EXPRESSED
                                ;THE DESIRE TO CHANGE THE SOFTWARE
                                ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
                                ;THE USER TYPED CONTROL G?).

                                ;*****
                                ;*TEST 14          FSRC MODE 2 TEST
                                ;*
                                ;* THIS IS A TEST OF FSRC MODE 2, AUTO
                                ;* INCREMENT MODE.
                                ;*
                                ;*****
4073 015664 000004          TEST14: SCOPE
4074 015666 104413          LPERR          ;SET UP THE LOOP ON ERROR ADDRESS.

4075 015670          J1:
4076 015670 170011          SETD          ;SET DOUBLE MODE

4077 015672 012700 016146    MOV      #JDAT0,RO
4078 015676 172410          LDD      (RO),AC0          ;LOAD AC0

4079 015700 012700 016126    MOV      #JDAT10,RO
4080 015704 005003          CLR      R3
4081 015706 012737 015776 000004  MOV      #JERR0,#ERRVECT

4082 015714 172420          J2:      LDD      (RO)+,AC0          ;TEST INSTRUCTION
4083 015716 005203          J3:      INC      R3
4084 015720 005203          J4:      INC      R3

4085 015722 012701 016136    MOV      #JDAT00,R1
4086 015726 174011          STD      AC0,(R1)          ;PICK UP RESULTS

4087 015730 020027 016116    CMP      RO,#JBUFO          ;WAS AN AUTO
4088 015734 001001          BNE     1$                ;DECREMENT EXECUTED?
4089 015736 000442          BR      JERR1

4090 015740 012702 016126    1$:      MOV      #JDAT10,R2          ;IS DATA CORRECT?
4091 015744 012703 016136    MOV      #JDAT00,R3
4092 015750 012704 000004    MOV      #4,R4
4093 015754 022223          J5:      CMP      (R2)+,(R3)+
4094 015756 001401          BEQ     J6
4095 015760 000443          BR      JERR2
4096 015762 077404          J6:      SOB     R4,J5

4097 015764 022700 016136    CMP      #JDAT10+10,RO      ;WAS RD INCREM.
4098 015770 001401          BEQ     J7                ;BY 10 (OCTAL)
4099 015772 000424          BR      JERR1

4099 015774 000470          J7:      BR      JDONE

                                ;IF A TRAP THROUGH 4 OCCURS COME HERE
  
```

K06

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4098 015776 021627 015716 JERR0: CMP (SP),#J3 :SEE IF THE TRAP
4099 016002 0C1405 BEQ J10 :OCCURRED ON THE
4100 016004 021627 015720 CMP (SP),#J4 :TESTED INSTRUCTION
4101 016010 001402 BEQ J10
4102 016012 000137 040232 JMP #CPSUR
4103
4104 016016 012737 000762 001240 J10: MOV #762,#STMP3 :REPORT FSRC FLOW
4105 016024 012737 000322 001242 MOV #322,#STMP4 :FAILURE
4106 016032 011637 001236 MOV (SP),#STMP2
4107 016036 022626 CMP (SP)+,(SP)+
4108 016040 104052 15: ERROR 52
4109 016042 000445 BR JDONE
4110
4111 016044 JERR1: :REPORT, RD NOT
4112 016044 012737 015714 001236 MOV #J2,#STMP2 :CORRECTLY AFFECTED
4113 016052 010037 001240 MOV RC,#STMP3
4114 016056 012737 016136 001242 MOV #JDATIO+10,#STMP4
4115 016064 104053 15: ERROR 53
4116 016066 00C433 BR JDONE
4117
4118 ;REPORT DATA FAILURE
4119
4120 016070 JERR2:
4121 016070 012737 015714 001236 MOV #J2,#STMP2
4122 016076 012737 016126 001240 MOV #JDATIO,#STMP3
4123 016104 012737 016136 001242 MOV #JDATIO,#STMP4
4124 016112 104054 15: ERROR 54
4125 016114 000420 BR JDONE
4126
4127 JBUF0: .WORD 010421
4128 016120 021042 JBUF1: 021042
4129 016122 042104 JBUF2: 042104
4130 016124 031463 JBUF3: 031463
4131
4132 016126 052525 JDATIO: 052525
4133 016130 114631 JDATIO1: 114631
4134 016132 063146 JDATIO2: 063146
4135 016134 073567 JDATIO3: 073567
4136
4137 016136 000000 JDATIO0: 0
4138 016140 000000 JDATIO1: 0
4139 016142 000000 JDATIO2: 0
4140 016144 000000 JDATIO3: 0
4141
4142 016146 177777 JDATIO: -1
4143 016150 177777 JDATIO1: -1
4144 016152 177777 JDATIO2: -1
4145 016154 177777 JDATIO3: -1
4146
4147
4148 016156 JDONE:
4149 016156 104412 RSETUP :GO INITIALIZE THE FPS AND STACK; AND
4150 :SEE IF THE USER HAS EXPRESSED
4151 :THE DESIRE TO CHANGE THE SOFTWARE
4152 :VIRTUAL CONSOLE SWITCH REGISTER (HAS
4153 :THE USER TYPED CONTROL G?).

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016160 000004  
016162 104413  
  
016164 170011  
016166 012701 172440  
016172 172410  
  
016174 012700 016420  
016200 005003  
016202 012737 016272 000004  
  
016210 172440  
016212 005203  
016214 005203  
  
016216 012701 016430  
016222 174011  
  
016224 020027 016430  
016230 001001  
016232 000441  
  
016234 012702 016410  
016240 012703 016430  
016244 012704 000004  
016250 022223  
016252 001401  
016254 000442  
016256 077404  
  
016260 022700 016410  
016264 001401  
016266 000423  
  
016270 000457  
  
016272 021627 016212  
016276 001405  
016300 021627 016214  
016304 001402  
016306 000137 040232  
  
016312 012737 000762 001240

```
*****
*TEST 15      FSRC MODE 4 TEST
*
* THIS IS A TEST OF FSRC MODE 4, AUTO
* DECREMENT MODE.
*
*****
TEST15:  SCOPE
          LPERR                      ;SET UP THE LOOP ON ERROR ADDRESS.

K1:      SETD                        ;SET DOUBLE MODE

          MOV      #KPATO,R0
          LDD      (R0),AC0          ;LOAD A DEFAULT
                                          ;PATTERN INTO AC0
          MOV      #KBUFO,R0
          CLR      R3
          MOV      #KERR0,#ERRVECT

K2:      LDD      -(R0),AC0          ;TEST INSTRUCTION
K3:      INC      R3
K4:      INC      R3

          MOV      #KDAT00,R1
          STD      AC0,(R1)          ;PICK UP THE RESULT

          CMP      R0,#KBUFO+10      ;WAS AN AUTO
          BNE      1$                ;INCREMENT EXECUTED
          BR       KERR1

1$:      MOV      #KDAT10,R2          ;IS DATA CORRECT?
          MOV      #KDAT00,R3
          MOV      #4,R4
          CMP      (R2)+,(R3)+
          BEQ      K6
          BR       KERR2
K6:      SOB      R4,K5

          CMP      #KBUFO-10,R0      ;WAS R0 DECREMENTED
          BEQ      K7                ;PROPERLY?
          BR       KERR1

K7:      BR       KDONE

;TRAP TO HERE ON AN ODD ADDRESS ERROR

KERR0:   CMP      (SP),#K3          ;SEE IF THE ERROR
          BEQ      K10              ;OCCURRED AT THE
          CMP      (SP),#K4          ;INSTRUCTION TESTED.
          BEQ      K10
          JMP      @#CPSPUR

K10:     MOV      #762,@#STMP3      ;REPORT FAILLRE IN
```

M06

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4210 016320 012737 000324 001242      MOV      #324,2#STMP4      :FSRC FLOWS
4211 016326 011637 001236      MOV      (SP),2#STMP2
4212 016332 104055      IS:      ERROR          55
4213 016334 000445      BR      KDONE
4214
4215 016336      KERR1:      :REPORT, RC
4216 016336 012737 016210 001236      MOV      #K2,2#STMP2      :INCORRECTLY AFFECTED.
4217 016344 010037 001242      MOV      RD,2#STMP3
4218 016350 012737 016410 001242      MOV      #KDAT10,2#STMP4
4219 016356 104056      IS:      ERROR          56
4220 016360 000433      BR      KDONE
4221
4222      :REPORT DATA FAILURE
4223
4224 016362      KERR2:
4225 016362 012737 016210 001236      MOV      #K2,2#STMP2
4226 016370 012737 016410 001242      MOV      #KDAT10,2#STMP3
4227 016376 012737 016430 001242      MOV      #KDAT00,2#STMP4
4228 016404 104057      IS:      ERROR          57
4229 016406 000420      BR      KDONE
4230
4231 016410 052525      KDAT10: .WORD      052525
4232 016412 114631      KDAT11:      114631
4233 016414 063140      KDAT12:      063140
4234 016416 073567      KDAT13:      073567
4235
4236 016420 010421      KBUF0:      010421
4237 016422 031463      KBUF1:      031463
4238 016424 042104      KBUF2:      042104
4239 016426 021042      KBUF3:      021042
4240
4241 016430 000000      KDAT00:      0
4242 016432 000000      KDAT01:      0
4243 016434 000000      KDAT02:      0
4244 016436 000000      KDAT03:      0
4245
4246 016440 177777      KPAT0:      -1
4247 016442 177777      KPAT1:      -1
4248 016444 177777      KPAT2:      -1
4249 016446 177777      DPAT3:      -1
4250
4251 016450      KDONE:
4252 016450 104412      RSETUP      :GO INITIALIZE THE FPS AND STACK: AND
4253      :SEE IF THE USER HAS EXPRESSED
4254      :THE DESIRE TO CHANGE THE SOFTWARE
4255      :VIRTUAL CONSOLE SWITCH REGISTER (HAS
4256      :THE USER TYPED CONTROL G?).
4257
4258
4259      :*****
4260      :*TEST 16      FSRC MODE 2, WITH FD=0, TEST
4261      :*
4262      :* THIS IS A TEST OF FSRC MODE 2 WITH
4263      :* FD=0. (AUTO INCREMENT)
4264      :*
4265      :*****

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# NO6

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4266 016452 000004          TST16: SCOPE
4267 016454 104413          LPEER          ;SET UP THE LOOP ON ERROR ADDRESS.
4268
4269 016456
4270 016456 170011          L1:          SETD          ;SET DOUBLE MODE
4271
4272 016460 012700 016726          MOV          #LPAT10,R0
4273 016464 17241C          LDD          (R0),AC0          ;LOAD AC0
4274
4275 016466 012700 016750          MOV          #LDAT10,R0          ;SET UP THE INPUT
4276 016470 012701 016736          MOV          #LPAT20,R1          ;DATA
4277 016476 012702 000004          MOV          #4,R2
4278
4279 016502 012120          L5:          MOV          (R1)+,(R0)+
4280 016504 077202          SOB          R2,L5
4281
4282 016506 012700 016750          MOV          #LDAT10,R0
4283 016512 005003          CLR          R3
4284 016514 170001          SETF          ;CLEAR FD.
4285
4286 016516 172420          L2:          LDF          (R0)+,AC0
4287 016520 005203          L3:          INC          R3
4288
4289 016522
4290 016522 170011          L4:          SETD          ;SET FD
4291
4292 016524 012701 016762          MOV          #LDAT00,R1
4293 016530 174011          STD          AC0,(R1)          ;PICK UP RESULTS
4294
4295 016532 020027 016754          CMP          R0,#LDAT12          ;WAS R0 INCREMENTED
4296 016536 001401          BEQ          L5          ;CORRECTLY BY 4
4297 016540 000421          BR          LERR1
4298
4299 016542 012737 177777 016754          L5:          MOV          #-1,#LDAT12
4300 016550 012737 177777 016756          MOV          #-1,#LDAT13
4301 016556 012702 016750          MOV          #LDAT10,R2          ;IS DATA CORRECT
4302 016562 012703 016762          MOV          #LDAT00,R3
4303 016566 012704 000004          MOV          #4,R4
4304
4305 016572 022223          L5:          CMP          (R2)+,(R3)+
4306 016574 001401          BEQ          L6
4307 016576 000427          BR          LERR2
4308 016600 077404          L6:          SOB          R4,L5
4309
4310 016602 000473          BR          LDONE
4311
4312 016604          LERR1:          ;REPORT FAILURE
4313 016604 012737 016516 001236          MOV          #L2,#STMP2          ;RO NOT INCREMENTED
4314 016612 010037 001240          MOV          R0,#STMP3          ;BY 4
4315 016616 012737 016754 001242          MOV          #LDAT12,#STMP4
4316 016624 104060          L5:          ERROR          6C
4317 016626 000461          BR          LDONE
4318
4319 016630          LERR3:          ;REPORT DATA FAILURE.
4320 016630 012737 016516 001236          MOV          #L2,#STMP2
4321 016636 012737 016750 001240          MOV          #LDAT10,#STMP3
  
```

```

4322 016644 012737 016762 001242      MOV      @LDAT00,@STMP4
4323 016652 104061      15:     ERROR
4324 016654 000476      BR      LDONE
4325
4326 016656 012732 016736      LERR2:  MOV      @LPAT20,R2      ;DID .BUT FC.
4327 016662 012733 016762      MOV      @LDAT00,R3      ;FAIL.
4328 016666 012704 000004      MOV      R4,R4
4329 016672 022223      15:     CMP      (R2)+,(R3)+
4330 016674 001355      BNE     LERR3
4331 016676 077403      SOB     R4,15
4332 016700 012737 016516 001236      MOV      @L2,@STMP2
4333 016706 012737 016750 001240      MOV      @LDAT10,@STMP3
4334 016714 012737 016764 001242      MOV      @LDAT01,@STMP4
4335 016722 104062      25:     ERROR
4336 016724 000422      BR      LDONE

```

```

4338 016726 177777      LPAT10: .WORD  -1
4339 016730 177777      LPAT11:      -1
4340 016732 177777      LPAT12:      -1
4341 016734 177777      LPAT13:      -1

```

```

4342 016736 052525      LPAT20:      052525
4343 016740 114631      LPAT21:      114631
4344 016742 063142      LPAT22:      063142
4345 016744 073567      LPAT23:      073567
4346 016746 000001      .WORD  000001
4347 016748 000000      LDAT10:      0
4348 016750 000000      LDAT11:      0
4349 016752 000000      LDAT12:      0
4350 016754 000000      LDAT13:      0
4351 016756 000000      .WORD  000001
4352 016760 000001      LDAT00:      0
4353 016762 000000      LDAT01:      0
4354 016764 000000      LDAT02:      0
4355 016766 000000      LDAT03:      0
4356 016770 000000

```

```

4358 016772
4359 016772 104412      LDONE:      RSETUP

```

```

:GO INITIALIZE THE FPS AND STACK; AND
:SEE IF THE USER HAS EXPRESSED
:THE DESIRE TO CHANGE THE SOFTWARE
:VIRTUAL CONSOLE SWITCH REGISTER .HAS
:THE USER TYPED CONTROL G?).

```

```

:*****
:TEST 17      FSRC MODE 2 WITH GR7, IMMEDIATE MODE, TEST
:
:* THIS IS A TEST OF FSRC MODE 2
:* USING GR7 (THE PC). THIS IS IMMEDIATE
:* MODE.
:*****
:TEST17: SCOPE
M1:          SETC

```

```

4374 016774 000004
4375 016776
4376 016776 170011
4377 016776

```



017000	012700	017272		MOV	#MPAT10, R0	
017004	172410			LDD	(R0), ACC	:LOAD BACKGROUND :PATTERN INTO ACC.
017006	005004			CLR	R4	
017010	012737	017232	000004	MOV	#MERR3, #MERRVECT	
017016	172427	000000	M15:	LDD	#0, ACC	:TEST INSTRUCTION
017020	017020			MOV	#5204, R4	
017022	005204		M2:	INC	R4	:NOTE THAT
017024	005204		M3:	INC	R4	:005204=INC R4
017026	005204		M4:	INC	R4	
017030	020427	000003		CMP	R4, #3	:SEE IF THE PC
017034	001401			BEG	#3	:WAS INCREMENTED
017036	000443			BR	MERRC	:BY 2 DURING THE :INSTRUCTION. IF :NOT THEN A BAD :CONSTANT WAS GENERATED
017040	012700	017312	M5:	MOV	#MDAT00, R0	
017044	174010			STD	ACC, (R0)	:GET THE DATA
017046	012700	017312		MOV	#MDAT00, R0	
017050	022720	005204		CMP	#5204, (R0)+	:IS THE DATA CORRECT?
017056	001401			BEG	M5	
017060	000451			BR	MERR1	
017062	012701	000003	M6:	MOV	#3, R1	
017066	005720			TST	(R0)+	
017070	001002			BNE	M7	
017072	077103			SQB	R1, M6	
017074	000512			BR	MOONE	
017076	012700	017312	M7:	MOV	#MDAT00, R0	:DID (BUT GR7) FAIL?
017080	012701	000004		MOV	#4, R1	
017106	022720	005204	M8:	CMP	#5204, (R0)+	
017112	001401			BEG	M9	
017114	000433			BR	MERR1	
017116	077105		M9:	SQB	R1, M8	
017120			MERR2:			:REPORT FAILURE :OF (BUT GR7)
017120	012737	017016	001236	MOV	#M15, #STMP2	
017126	012737	017302	001240	MOV	#MPAT20, #STMP3	
017134	012737	017312	001242	MOV	#MDAT00, #STMP4	
017142	104063		M10:	ERROR	#63	
017144	000466			BR	MOONE	
017146	012705	017022		MOV	#M2, R5	:REPORT FAILURE
017152	010537	001242		MOV	R5, #STMP4	:PC INCREMENTED
017156	162704	000003		SUB	#3, R4	
017162	006304			ASL	R4	
017164	160405			SUB	R4, R5	
017166	010537	001240		MOV	R5, #STMP3	
017172	012737	017016	001236	MOV	#M15, #STMP2	
017200	104064		M11:	ERROR	#64	
017202	000447			BR	MOONE	

017300  
017301  
017302  
017303  
017304  
017305  
017306  
017307  
017308  
017309  
017310  
017311  
017312  
017313  
017314  
017315  
017316  
017317  
017318  
017319  
017320  
017321  
017322  
017323  
017324  
017325  
017326  
017327  
017328  
017329  
017330  
017331  
017332  
017333  
017334

012737 017016 001236  
012737 017312 001240  
012737 017302 001242  
104066  
000434  
032716 000001  
001002  
000137 040232  
011637 001240  
012737 017322 001242  
012737 017316 001236  
022626  
104065  
000414  
177777  
177777  
177777  
177777  
005204  
005204  
005204  
005204  
000000  
000000  
000000  
000000  
104412

MERR1: MOV #M15,2#STMP2  
MOV #MDAT00,2#STMP3  
MOV #MPAT20,2#STMP4  
18: ERROR 66  
BR MOONE  
:TRAP TO HERE THROUGH 4.  
MERR3: BIT #1,(SP)  
BNE 18  
JMP 2#CPSPUR  
18: MOV (SP),2#STMP3  
MOV #M2,2#STMP4  
MOV #M15,2#STMP2  
CMP (SP)+,(SP)+  
28: ERROR 65  
BR MOONE  
MPAT10: -1  
MPAT11: -1  
MPAT12: -1  
MPAT13: -1  
MPAT20: 5204  
MPAT21: 5204  
MPAT22: 5204  
MPAT23: 5204  
MDAT00: 0  
MDAT01: 0000  
MDAT02: 0000  
MDAT03: 0  
MOONE: RSETUP

:REPORT DATA  
:FAILURE  
:SEE IF THE  
:TRAP TO 4 OCCURRED  
:BECAUSE OF AN  
:ODD ADDRESS  
:IF YES REPORT  
:BAD CONSTANT  
:GENERATED  
:GO INITIALIZE THE FPS AND STACK; AND  
:SEE IF THE USER HAS EXPRESSED  
:THE DESIRE TO CHANGE THE SOFTWARE  
:VIRTUAL CONSOLE SWITCH REGISTER (HAS  
:THE USER TYPED CONTROL G?).

\*\*\*\*\*  
:TEST 20 FSRC MODE 3 TEST  
: THIS IS A TEST OF FSRC MODE 3, AUTO INCREMENT  
: DEFERRED  
:\*\*\*\*\*  
TST20: SCOPE

N1: SETD ;SET FD MODE  
MOV #MPAT10,RO  
LDD (RO),AC0 ;LOAD AC0 WITH A DEFAULT

# E07

```

: PATTERN
017336 012700 017776 MOV #NPAT20,R0
017342 005003 CLR R3
017344 012737 017520 000004 MOV #NERR0,#ERRVECT ; IF A FAILURE OCCURS
; IN THE FSRC FLOWS AN
; ODD TRAP TO 4 COULD OCCUR
; TEST INSTRUCTION.
017352 172430 N2: LDD 2(R0)+,AC0
017354 005203 N3: INC R3
017356 005203 N4: INC R3
017360 012701 017756 MOV #NDAT00,R1
017364 174011 STD AC0,(R1) ; GET THE DATA
017366 020027 020000 CMP RC,#NPAT20+2 ; WAS RC INCREMENTED
017372 001437 BEQ N12 ; BY 2?
017374 020027 020006 N5: CMP RC,#NPAT20+10 ; FSRC MODE 2?
017400 001001 BNE N6
017402 000506 BR NERR1
017404 020027 017756 N6: CMP RC,#NPAT20-10 ; FSRC MODE 4?
017410 001001 BNE N7
017412 000520 BR NERR2
017414 020027 017776 N7: CMP RC,#NPAT20
017420 001023 BNE N11
017422 012702 017756 MOV #NDAT00,R2 ; FSRC MODE 0?
017426 012703 000004 MOV #4,R3
017432 022227 177777 N8: CMP (R2)+,#-1
017436 001002 BNE N9
017440 077304 SOB R3,N8
017442 000510 BR NERR3
017444 012702 017756 N9: MOV #NDAT00,R2 ; FSRC MODE 1
017450 012703 017776 MOV #NPAT20,R3
017454 012704 000004 MOV #4,R4
017460 022223 N10: CMP (R2)+,(R3)+
017462 001002 BNE N11
017464 077403 SOB R4,N10
017466 000502 BR NERR4
017470 000505 N11: BR NERR5
017472 012702 017756 N12: MOV #NDAT00,R2 ; DATA CORRECT?
017476 012703 020020 MOV #NDAT10,R3
017502 012704 000004 MOV #4,R4
017506 022223 N13: CMP (R2)+,(R3)+
017510 001002 BNE N14
017512 077403 SOB R4,N13
017514 000545 BR NDONE
017516 000504 N14: BR NERR6

```

; IF AN ODD ADDRESS TRAP OCCURS COME HERE  
 ; TO SEE IF THE FAILURE WAS IN THE FSRC

: FLOWS

4545									
4546	017520	022716	017356						
4547	017524	001412							
4548	017526	022716	017354						
4549	017530	001402							
4550	017534	000137	040232						
4551	017540	020027	017774						
4552	017544	001407							
4553	017546	000137	040232						
4554									
4555	017552								
4556	017552	011637	001236						
4557	017556	022626							
4558	017560	104067							
4559	017562	000522							
4560									
4561	017564	011637	001236						
4562	017570	022626							
4563	017572	012737	000627	001244					
4564	017600	012737	000323	001250					
4565	017606	012737	000325	001246					
4566	017614	104070							
4567	017616	000504							
4568	017620	012737	000322	001246					
4569	017626	012737	000627	001246					
4570	017634	012737	000323	001246					
4571	017642	012737	017352	001236					
4572	017650	104071							
4573	017652	000466							
4574	017654	012737	000324	001246					
4575	017662	000761							
4576	017664	012737	000320	001246					
4577	017672	000755							
4578	017674	012737	000321	001246					
4579	017702	000751							
4580									
4581	017704	010037	001240						
4582	017710	012737	020000	001242					
4583	017716	012737	017352	001236					
4584	017724	104072							
4585	017726	000440							
4586									
4587									
4588									
4589	017730								
4590	017730	012737	017352	001236					
4591	017736	012737	017756	001240					
4592	017744	012737	020020	001242					
4593	017752	104073							
4594	017754	000425							
4595									
4596	017756	000000							
4597	017760	000000							
4598	017762	000000							
4599	017764	000000							
4600									
4601	017766	052525	052525	052525					

```

;FSRC MODE 6 OR 7?
;FSRC MODE 5?
;WENT TO FSRC
;MODE 6 OR 7.
;WENT TO FSRC
;MODE 5.
;FSRC MODE 2.
;FSRC MODE 4
;FSRC MODE 0
;FSRC MODE 1
;RD NOT
;INCREMENTED
;PROPERLY.
;DATA FAILURE.
.NWORD 0
.NWORD 0
.NWORD 0
.NWORD 0
.NWORD 52525,52525,52525,52525

```

4602 017774 052525  
4603 017776 020020  
4604 020000 070707  
4605 020002 070707  
4606 020004 070707  
4607 020006 000001  
4608 020010 177777  
4609 020012 177777  
4610 020014 177777  
4611 020016 177777  
4612  
4613 020020 010421  
4614 020022 021042  
4615 020024 031463  
4616 020026 042104  
4617  
4618 020030  
4619 020032 104412  
4620  
4621  
4622  
4623  
4624  
4625  
4626  
4627  
4628  
4629  
4630  
4631  
4632  
4633 020032 000004  
4634  
4635 020034  
4636 020034 :70011  
4637  
4638 020036 012700 020514  
4639 020042 172410  
4640  
4641 020044 012700 020502  
4642 020050 005003  
4643 020052 012737 020224 000004  
4644  
4645  
4646  
4647 020060 172450  
4648 020062 005203  
4649 020064 005203  
4650  
4651 020066 012701 020462  
4652 020072 174011  
4653  
4654 020074 020027 020500  
4655 020100 001436  
4656  
4657 020102 020027 020512

NPAT20: .WORD NDAT10  
NPAT21: 070707  
NPAT22: 070707  
NPAT23: 070707  
NPAT10: .WORD 1  
NPAT11: .WORD -1  
NPAT12: -1  
NPAT13: -1  
NDAT10: .WORD 010421  
NDAT11: 021042  
NDAT12: 031463  
NDAT13: 042104

NOONE: RSETJP

:GO INITIALIZE THE FPS AND STACK; AND  
:SEE IF THE USER HAS EXPRESSED  
:THE DESIRE TO CHANGE THE SOFTWARE  
:VIRTUAL CONSOLE SWITCH REGISTER HAS  
:THE USER TYPED CONTROL G).

\*\*\*\*\*  
:TEST 21 FSRC MODE 5 TEST  
:  
: THIS IS A TEST OF FSRC MODE 5, AUTO DECREMENT  
: DEFERRED.  
:\*\*\*\*\*

†ST21: SCOPE  
01: SETD ;SET FD MODE  
MOV #OPAT10,R0  
LDD (R0),AC0 ;LOAD AC0 WITH A  
;DEFAULT PATTERN.  
MOV #OPAT21,R0  
CLR R3  
MOV #OERR0,2#ERRVEC ;IF A FAILURE  
;OCCURS IN THE FSRC  
;FLOWS AN ODD ADDR.  
;TRAP TO 4 MAY OCCUR.  
;TEST INSTRUCTION  
02: LDD 2-(R0),AC0  
03: INC R3  
04: INC R3  
MOV #ODAT00,R1  
STD AC0,(R1) ;GET THE DATA  
CMP R0,#OPAT20 ;WAS R0 DECREMENTED  
BEQ 012 ;BY 2?  
05: CMP R0,#OPAT21+10 ;FSRC MODE 2

# H07

4658	020106	001001		BNE	06	
4659	020110	000505		BR	OERR1	
4660						
4661	020112	020027	020472	06:	CMP	RD, #OPAT21-10 ;FSRC MODE 4?
4662	020116	001001		BNE	07	
4663	020120	000517		BR	OERR2	
4664						
4665	020122	020027	020502	07:	CMP	RD, #OPAT21
4666						
4667	020126	012702	020464		MOV	#ODAT01, R2 ;FSRC MODE 0?
4668	020132	012703	000004		MOV	#4, R3
4669	020136	022227	177777	08:	CMP	(R2)+, #-1
4670	020142	001002		BNE	09	
4671	020144	077304		SOB	R3, 08	
4672	020146	000510		BR	OERR3	
4673						
4674	020150	012702	020462	09:	MOV	#ODAT00, R2 ;FSRC MODE 1?
4675	020154	012703	020502		MOV	#OPAT21, R3
4676	020160	012704	000004		MOV	#4, R4
4677	020164	022223		10:	CMP	(R2)+, (R3)+
4678	020166	001002		BNE	011	
4679	020170	077403		SOB	R4, 010	
4680	020172	000502		BR	OERR4	
4681						
4682	020174	000505		011:	BR	OERR5
4683						
4684	020176	012702	020462	012:	MOV	#ODAT00, R2 ;DATA CORRECT?
4685	020202	012703	020524		MOV	#ODAT10, R3
4686	020206	012704	000004		MOV	#4, R4
4687	020212	022223		013:	CMP	(R2)+, (R3)+
4688	020214	001002		BNE	014	
4689	020216	077403		SOB	R4, 013	
4690	020220	000545		BR	ODONE	
4691						
4692	020222	000504		014:	BR	OERR6
4693						
4694						
4695						
4696						
4697						
4698	020224	022716	020064	OERR0:	CMP	#04, (SP) ;FSRC MODE 6 OR 7?
4699	020230	001412			BEQ	OERR10
4700	020232	022716	020062		CMP	#03, (SP)
4701	020236	001402			BEQ	15
4702	020240	000137	040232		JMP	2#CPSPUR
4703	020244	020027	020504	15:	CMP	RD, #OPAT21+2 ;FSRC MODE 3?
4704	020250	001425			BEQ	OERR1
4705	020252	000137	040232		JMP	2#CPSPUR
4706						
4707	020256			OERR10:		;WENT TO FSRC
4708	020256	011637	001236		MOV	(SP), 2#STMP2 ;MODE 6 OR 7
4709	020262	022626			CMP	(SP)+, (SP)+
4710	020264	104074		15:	ERROR	74
4711	020266	000522			BR	ODONE
4712						
4713	020270	011637	001240	OERR11:	MOV	(SP), 2#STMP3 ;WENT TO FSRC MODE

;IF AN ODD ADDRESS TRAP OCCURS COME  
 ;HERE TO SEE IF THE FAILURE WAS IN THE  
 ;FSRC FLOWS:

```

4744 (SP)+ (SP)+      :3
4745      MOV      #627,2#STMP5
4746      MOV      #325,2#STMP7
4747      MOV      #323,2#STMP6
4748      ERROR    75
4749      BR      ODONE
4750
4751 OERR1: MOV      #322,2#STMP6          :FSRC MODE2
4752 OERR20: MOV     #627,2#STMP4
4753      MOV      #325,2#STMP7
4754      MOV      #02,2#STMP2
4755      ERROR    76
4756      BR      ODONE
4757 OERR2: MOV      #324,2#STMP6          :FSRC MODE 4
4758      BR      OERR20
4759 OERR3: MOV      #320,2#STMP6          :FSRC MODE 0
4760      BR      OERR20
4761 OERR4: MOV      #321,2#STMP6          :FSRC MODE 1
4762      BR      OERR20
4763
4764 OERR5: MOV      RC,2#STMP3           :RC NOT DECREMENTED
4765      MOV      #OPAT20,2#STMP4       :PFCOPERLY
4766      MOV      #04,2#STMP2
4767      ERROR    77
4768      BR      ODONE
4769
4770 OERR6:                                :DATA FAILURE
4771      MOV      #02,2#STMP2
4772      MOV      #ODAT00,2#STMP3
4773      MOV      #ODATIO,2#STMP4
4774      ERROR    100
4775      BR      ODCNE
4776
4777 ODAT00: .WORD   0
4778 ODAT01:        0
4779 ODAT02:        0
4780 ODAT03:        0
4781
4782 O52525: .WORD   52525,52525,52525
4783 OPAT20: .WORD   ODATIO
4784 OPAT21: 070707
4785 OPAT22: 070707
4786 OPAT23: 070707
4787 OPAT24: 070707
4788 OWORD   1
4789 OPAT10: .WORD   -1
4790 OPAT11:        -1
4791 OPAT12:        -1
4792 OPAT13:        -1
4793
4794 ODATIO: .WORD   73567
4795 ODATI1:        004210
4796 ODATI2:        114631
4797 ODATI3:        125252
4798
4799 OODONE:
    
```

4770 020534 104412 RSETUP ;GO INITIALIZE THE FPS AND STACK; AND  
4771 ;SEE IF THE USER HAS EXPRESSED  
4772 ;THE DESIRE TO CHANGE THE SOFTWARE  
4773 ;VIRTUAL CONSOLE SWITCH REGISTER (HAS  
4774 ;THE USER TYPED CONTROL G?).

\*\*\*\*\*  
:TEST 22 FSRC MODE 6 TEST  
: THIS IS A TEST OF FSRC MODE 6, INDEX MODE  
:\*\*\*\*\*  
↑ST22: SCOPE

4783 020536 000004  
4784  
4785 020540 P1: SETD ;SET FD MODE  
4786 020540 170011  
4787  
4788 020542 012700 021160 MOV #PPAT10,R0  
4789 020546 172410 LDD (R0),AC0 ;LOAD A DEFAULT PATTERN  
4790 ;INTO AC0  
4791 020550 012737 020656 000004 MOV #PERR0,#ERRVECT ;IF THE (BUT FSRC) FORG  
4792 ;FAILS AN ODD ADDRESS TRAP  
4793 020556 012700 020727 MOV #PDAT10-241,R0 ;COULD OCCUR.  
4794  
4795 020562 172460 000241 P2: LDD 241(R0),AC0  
4796 020564 P3=P2+2  
4797  
4798 020566 012701 021200 P4: MOV #PDAT00,R1  
4799 020572 174011 STD AC0,(R1) ;GET THE DATA  
4800 020574 012703 000004 MOV #4,R3  
4801 020600 012702 021170 MOV #PDAT10,R2  
4802 020604 012701 021200 MOV #PDAT00,R1  
4803 020610 022221 P5: CMP (R2)+,(R1)+ ;CHECK THE DATA  
4804 020612 001007 BNE P6  
4805 020614 077303 SOB R3,P5  
4806 020616 022700 020727 CMP #PDAT10-241,R0 ;RO CORRECT?  
4807 020622 001401 BEQ 15  
4808 020624 000512 BR PERR21  
4809 020626 000137 021210 15: JMP 2#PDCNE  
4810  
4811 020632 012701 021200 P6: MOV #PDAT00,R1  
4812 020636 012703 000004 MOV #4,R3  
4813 020642 022721 177777 P7: CMP #-1,(R1)+ ;WAS IT FSRC MODE 0?  
4814 020646 001401 BEQ P8  
4815 020650 000512 BR PERR1  
4816 020652 077305 P8: SOB R3,P7  
4817 020654 000523 BR PERR2  
4818 ;TRAP TO HERE ON AN ODD ADDRESS  
4819 020656 021627 020564 PERR0: CMP (SP),#P3  
4820 020662 001411 BEQ PERR11  
4821 020664 021627 020566 CMP (SP),#P4 ;WAS IT FSRC MODE 7?  
4822 020670 001402 BEQ PERR10  
4823 020672 000137 040232 JMP 2#CPSPUR  
4824  
4825 020676 012737 000327 001246 PERR10: MOV #327,2#STMP6



# K07

4826	020704	000443			BR	PERR17	
4827	020706	022700	020727		PERR11: CMP	#PDATIO-241,RO	;WAS IT FSRC MODE 1
4828	020712	001004			BNE	PERR12	
4829	020714	012737	000321	001246	MOV	#321,0#STMP6	
4830	020722	000434			BR	PERR17	
4831	020724	022700	020737		PERR12: CMP	#PDATIO-241+10,RO	;WAS IT FSRC MODE 2
4832	020730	001004			BNE	PERR13	
4833	020732	012737	000322	001246	MOV	#322,0#STMP6	
4834	020740	000425			BR	PERR17	
4835	020742	022700	020731		PERR13: CMP	#PDATIO-241+2,RO	;WAS IT FSRC MODE 3
4836	020746	001004			BNE	PERR14	
4837	020750	012737	000323	001246	MOV	#323,0#STMP6	
4838	020756	000416			BR	PERR17	
4839	020760	022700	020717		PERR14: CMP	#PDATIO-241-10,RO	;WAS IT FSRC MODE 4
4840	020764	001004			BNE	PERR15	
4841	020766	012737	000324	001246	MOV	#324,0#STMP6	
4842	020774	000407			BR	PERR17	
4843	020776	022700	020725		PERR15: CMP	#PDATIO-241-2,RO	;WAS IT FSRC MODE 5
4844	021002	001401			BEQ	PERR16	
4845	021004	000416			BR	PERR20	
4846	021006	012737	000325	001246	PERR16: MOV	#325,0#STMP6	
4847							
4848	021014	012737	000627	001244	PERR17: MOV	#627,0#STMP5	;REPORT FSRC
4849	021022	012737	000326	001250	MOV	#326,0#STMP7	;FLOWS FAILURE.
4850	021030	011637	001236		MOV	(SP),0#STMP2	
4851	021034	022626			CMP	(SP)+,(SP)+	
4852	021036	104101			IS: ERROR	101	
4853	021040	000463			BR	PDONE	
4854							
4855	021042	011637	001236		PERR20: MOV	(SP),0#STMP2	;REPORT RD AFFECTED
4856	021046	022626			CMP	(SP)+,(SP)+	
4857	021050	000403			BR	PERR22	
4858	021052	012737	020562	001236	PERR21: MOV	#P2,0#STMP2	
4859	021060				PERR22:		
4860	021060	010037	001240		MOV	RO,0#STMP3	
4861	021064	012737	020727	001242	MOV	#PDATIO-241,0#STMP4	
4862	021072	104102			IS: ERROR	102	
4863	021074	000445			BR	PDONE	
4864							
4865	021076				PERR1:		;DATA FAILURE.
4866	021076	012737	020562	001236	MOV	#P2,0#STMP2	
4867	021104	012737	021170	001240	MOV	#PDATIO,0#STMP3	
4868	021112	012737	021200	001242	MOV	#PDATIO,0#STMP4	
4869	021120	104104			IS: ERROR	104	
4870	021122	000432			BR	PDONE	
4871							
4872	021124				PERR2:		;FSRC FAILURE TO
4873	021124	012737	020562	001236	MOV	#P2,0#STMP2	;MODE 0
4874	021132	012737	000627	001244	MOV	#627,0#STMP5	
4875	021140	012737	000326	001250	MOV	#326,0#STMP7	
4876	021146	012737	000320	001246	MOV	#320,0#STMP6	
4877	021154	104103			IS: ERROR	103	
4878	021156	000414			BR	PDONE	
4879							
4880	021160	177777			PPATIO: .WORD	-1	
4881	021162	177777			PPATI1:	-1	

```

4882 021164 177777 PPAT12: -1
4883 021166 177777 PPAT13: -1
4884
4885 021170 010421 PDAT10: .WORD 010421
4886 021172 031463 PDAT11: 031463
4887 021174 052525 PDAT12: 052525
4888 021176 073567 PDAT13: 073567
4889
4890 021200 000000 PDAT00: .WORD 0
4891 021202 000000 PDAT01: 0
4892 021204 000000 PDAT02: 0
4893 021206 000000 PDAT03: 0
4894
4895 021210 PDONE:
4896 021210 104412 RSETUP ;GO INITIALIZE THE FFS AND STACK; AND
;SEE IF THE USER HAS EXPRESSED
;THE DESIRE TO CHANGE THE SOFTWARE
;VIRTUAL CONSOLE SWITCH REGISTER (HAS
;THE USER TYPED CONTROL G?).
4897
4898
4899
4900
4901
4902
4903
4904
4905
4906
4907
4908
4909
4910 021212 000004
4911
4912 021214
4913 021214 170011
4914
4915 021216 012700 021650
4916 021222 172410 ;LOAD A DEFAULT
;PATTERN INTO ACO
4917
4918 021224 012737 021356 000004 MOV #QERR0, Q#ERRVECT ;IF THE (BUT FSRC)
;FORK FAILS AN
;ODD ADR TRAP COULD
;OCCUR
4919
4920
4921 021232 012700 021417 MOV #QPAT20-241, R0
4922
4923 021236 172470 000241 Q2: LDD Q241(R0), ACO
4924 021240 Q3=Q2+2
4925
4926
4927 021242 012701 021670 Q4: MOV #QDAT00, R1
4928 021246 174011 STD ACO, (R1) ;GET THE DATA
4929
4930
4931 021250 012703 000004 MOV #4, R3
4932 021254 012704 021670 MOV #QDAT00, R4
4933 021260 012705 021700 MOV #QDAT10, R5
4934 021264 022425 Q5: CMP (R4)+, (R5)+ ;CHECK THE DATA
4935 021266 001007 BNE Q6
4936 021270 077303 SOB R3, Q5
4937 021272 022700 021417 CMP #QPAT20-241, R0 ;CHECK R0.

```

```

*****
*TEST 23 FSRC MODE 7 TEST
*
* THIS IS A TEST OF FSRC MODE 7, INDEX
* DEFERRED MODE.
*
*****

```

M07

```

4938 021276 001401          BEQ      15
4939 021300 000514          BR       QERR21
4940 021302 000137 021710    15:     JMP      2#QDONE
4941
4942 021306 012701 021670    06:     MOV     #QDAT00,R1
4943 021312 012703 000004          MOV     #4,R3
4944 021316 022721 177777    07:     CMP     #-1,(R1)+      ;WAS IT FSRC MODE 0?
4945 021322 001002          BNE     Q8
4946 021324 077304          SOB     R3,Q7
4947 021326 000513          BR      QERR2
4948
4949 021330 012701 021660    08:     MOV     #QPAT20,R1
4950 021334 012702 021670          MOV     #QDAT00,R2
4951 021340 012703 000004          MOV     #4,R3
4952 021344 022122    09:     CMP     (R1)+,(R2)+      ;WAS IT FSRC 6
4953 021346 001401          BEQ     Q10              ;OR DATA FAILURE
4954 021350 000524          BR      QERR1
4955 021352 077304    10:     SOB     R3,Q9
4956 021354 000504          BR      QERR3
4957
4958          ;TRAP TO HERE ON AN ODD ADR FAILURE
4959
4960 021356 021627 020564    QERR0:  CMP     (SP),#P3
4961 021362 000137 040232          JMP     2#CPSPUR
4962
4963 021366 022700 021417    QERR11: CMP     #QPAT20-241,RO ;WAS IT FSRC
4964 021372 001004          BNE     QERR12          ;MODE 1?
4965 021374 012737 000321 001246          MOV     #321,2#STMP6
4966 021402 000434          BR      QERR17
4967 021404 022700 021427    QERR12: CMP     #QPAT20-241+10,RO ;WAS IT FSRC
4968 021410 001004          BNE     QERR13          ;MODE 2?
4969 021412 012737 000322 001246          MOV     #322,2#STMP6
4970 021420 000425          BR      QERR17
4971 021422 022700 021421    QERR13: CMP     #QPAT20-241+2,RO ;WAS IT FSRC
4972 021426 001004          BNE     QERR14          ;MODE 3?
4973 021430 012737 000323 001246          MOV     #323,2#STMP6
4974 021436 000416          BR      QERR17
4975 021440 022700 021407    QERR14: CMP     #QPAT20-241-10,RO ;WAS IT FSRC
4976 021444 001004          BNE     QERR15          ;MODE 4
4977 021446 012737 000324 001246          MOV     #324,2#STMP6
4978 021454 000407          BR      QERR17
4979
4980 021456 022700 021415    QERR15: CMP     #QPAT20-241-2,RO ;WAS IT FSRC
4981 021462 001401          BEQ     QERR16          ;MODE 5
4982 021464 000416          BR      QERR20
4983
4984 021466 012737 000325 001246    QERR16: MOV     #325,2#STMP6
4985
4986 021474 012737 000627 001244    QERR17: MOV     #627,2#STMP5 ;REPORT FSRC FAILURE
4987 021502 012737 000327 001250          MOV     #327,2#STMP7
4988 021510 011637 001236          MOV     (SP),2#STMP2
4989 021514 022826          CMP     (SP)+,(SP)+
4990 021516 104105    15:     ERROR  105
4991 021520 000473          BR      QDONE
4992
4993 021522 011637 001236    QERR20: MOV     -SP,2#STMP2 ;REPORT RO AFFECTED.

```

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4992 021526 022626      CMP      (SP)+,(SP)+
4993 021530 003403      BR      QERR2
4994 021532 012737 021236 001236  QERR21: MOV     #02,2#STMP2
4995 021540 012737 021417 001242  QERR22: MOV     R0,2#STMP3
4996 021544 012737 021417 001242  IS:     MOV     #QPAT20-241,2#STMP4
4997 021552 104106      IS:     ERROR  106
4998 021554 000455      BR      QDONE

5000 021556 012737 000320 001246  QERR2:  MOV     #320,2#STMP6      :WENT TO FSRC
5001 021564 003403      BR      QERR4                :MODE 0
5002 021566 012737 000326 001246  QERR3:  MOV     #326,2#STMP6      :WENT TO FSRC
5003 021574 012737 000627 001244  QERR4:  MOV     #627,2#STMP5
5004 021602 012737 000327 001250      MOV     #327,2#STMP7
5005 021610 012737 021236 001236      MOV     #02,2#STMP2
5006 021616 104107      IS:     ERROR  107
5007 021620 000433      BR      QDONE

5009 021622      QERR1:                :DATA FAILURE
5010 021622 012737 021236 001236      MOV     #02,2#STMP2
5011 021630 012737 021700 001240      MOV     #QDAT10,2#STMP3
5012 021636 012737 021670 001242      MOV     #QDAT00,2#STMP4
5013 021644 104110      IS:     ERROR  110
5014 021646 000420      BR      QDONE

5016 021650 177777      QPAT10: .WORD  -1
5017 021652 177777      QPAT11:                -1
5018 021654 177777      QPAT12:                -1
5019 021656 177777      QPAT13:                -1

5021 021660 021700      QPAT20: .WORD  QDAT10
5022 021662 052525      QPAT21:                52525
5023 021664 052525      QPAT22:                52525
5024 021666 052525      QPAT23:                52525

5026 021670 000000      QDAT00: .WORD   0
5027 021672 000000      QDAT01:                0
5028 021674 000000      QDAT02:                0
5029 021676 000000      QDAT03:                0

5031 021700 073567      QDAT10: .WORD  073567
5032 021702 052525      QDAT11: .WORD  052525
5033 021704 031463      QDAT12: .WORD  031463
5034 021706 010421      QDAT13: .WORD  010421

5036 021710      QDONE:
5037 021710 104412      RSETUP      :GO INITIALIZE THE FPS AND STACK; AND
                    :SEE IF THE USER HAS EXPRESSED
                    :THE DESIRE TO CHANGE THE SOFTWARE
                    :VIRTUAL CONSOLE SWITCH REGISTER (HAS
                    :THE USER TYPED CONTROL G?).

```

```

*****
*TEST 24 (BUT EZBT 19 (BUT ENBT) AND (BUT FIUV) TEST
*
```

\* THIS IS A TEST OF THE (BUT EZBT Y8) FORK, THE  
\* (BUT ENBT) FORK AND (BUT FIUV) FORK IN THE  
\* LOAD INSTRUCTION FLOWS.  
\* EACH OF THE PATTERNS:

0  
+ NUM  
- NUM  
- 0  
\* IS LOADED TWICE, ONCE WITH AC=0 THEN  
\* WITH AC=C. AFTER EACH LOAD THE FPS IS  
\* CHECK TO INSURE THAT CONTROL WAS PASSED  
\* THROUGH WITH THE FORKS PROPERLY.

\*\*\*\*\*

012737 023012 023012  
012737 022742 022742  
012737 000004 000004  
012737 177777 177777  
  
022737 023033 023014  
012737 000023 023016  
012737 022472 000244  
  
104413  
012700 000200  
170100  
012700 022742  
172410  
013737 023014 023020  
012737 000001 023022  
012737 000254 023024  
  
012700 022752  
172410  
010037 001252  
012737 022024 001236  
  
012704 000204  
170205  
  
022405  
001402  
000137 022516  
  
104413  
012700 000200  
170100  
  
012700 022742  
172410  
013737 023016 023020  
012737 000003 023022  
012737 000054 023024

U24: SCOPE  
CLR #BUFLAG  
MOV #UPAT00,RO ;SET UP ACC DATA.  
MOV #4,R1  
U0: MOV #-1,RO)+  
SOB R1,U0  
  
U1: MOV #033,#STMP1  
MOV #023,#STMP2  
MOV #UERR0,#FPVECT ;IN CASE (BUT FIUV FAILS)  
  
LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
MOV #200,RO  
LDFPS RO  
MOV #UPAT00,RO ;LOAD ACC  
LDD (RO),AC0  
MOV #STMP1,#UROM1  
MOV #001,#UROM2  
MOV #254,#UROM3  
  
U2: MOV #UPAT10,RO ;LOAD C INTO ACC  
LDD (RO),AC0  
MOV RO,#STMP10  
MOV #U2,#STMP2  
  
MOV #204,R4 ;SEE IF FPS IS CORRECT  
STFPS R5  
  
CMP R4,R5  
BEO U3  
JMP #UERR1  
  
U3: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
MOV #200,RO  
LDFPS RO  
  
MOV #UPAT00,RO ;LOAD ACC  
LDD (RO),AC0  
MOV #STMP2,#UROM1  
MOV #003,#UROM2  
MOV #054,#UROM3

```

022116 012700 022762      MOV      #UPAT20,R0      :LOAD A POSITIVE NUMBER
                                :INTO ACC
022122 172410      U4:      LDD      (R0),ACC
022124 010037      MOV      R0,#STMP10
022126 012737 001252      MOV      #U4,#STMP2
022130 012704 000200      MOV      #200,R4      :FPS CORRECT?
022142 170205      STFPS   R5
022144 020405      CMP      R4,R5
022146 001402      BEQ     U5
022148 000137 022602      JMP     #UERR2
                                U5:
022150 104413      LPERR   :SET UP THE LOOP ON ERROR ADDRESS.
022152 012700 000200      MOV      #200,R0
022154 170100      LDFPS   R0
022156 012700 022742      MOV      #UPAT00,R0   :LOAD ACC
022158 172410      LDD      (R0),ACC
022160 012737 023016 023020      MOV      #UTMP2,#UROM1
022162 012737 000403 023022      MOV      #403,#UROM2
022164 012737 000256 023024      MOV      #056,#UROM3
022166 012700 022772      MOV      #UPAT30,R0   :LOAD A NEGATIVE
                                :NUMBER INTO ACC
022222 172410      U6:      LDD      (R0),ACC
022224 010037 001252      MOV      R0,#STMP10
022226 012737 022220 001236      MOV      #U6,#STMP2
022228 012704 000210      MOV      #210,R4      :FPS CORRECT
022230 170205      STFPS   R5
022232 020405      CMP      R4,R5
022234 001402      BEQ     U7
022236 000137 022602      JMP     #UERR2
                                U7:
022238 104413      LPERR   :SET UP THE LOOP ON ERROR ADDRESS.
022240 012700 000200      MOV      #200,R0
022242 170100      LDFPS   R0
022244 012700 022742      MOV      #UPAT00,R0   :LOAD ACC
022246 172410      LDD      (R0),ACC
022248 012737 023014 023020      MOV      #UTMP1,#UROM1
022250 012737 000401 023022      MOV      #401,#UROM2
022252 012737 000256 023024      MOV      #256,#UROM3
022254 012700 023002      MOV      #UPAT40,R0   :LOAD -0 INTO ACC
022256 172410      U10:     LDD      (R0),ACC
022258 000240      U11:     NOP
022260 010037 001252      MOV      R0,#STMP10
022262 012737 022316 001236      MOV      #U10,#STMP2
022264 012704 000214      MOV      #214,R4      : (BUT FIUV) FAULT!
022266 170205      STFPS   R5      :SEE IF FPS IS CORRECT.
022268 020405      CMP      R4,R5
022270 001402      BEQ     U12
022272 000137 022516      JMP     #UERR1
022274 005737 023012      U12:     TST     #UFLAG :SEE IF ALL THE PATTERNS
022276 001021      BNE    U14      :HAVE BEEN TEST WITH
                                :BOTH AC NOT EQUAL TO 0 AND AC=0
022360 012700 022742      MOV      #UPAT00,R0   :IF NOT GO BACK AND
022364 012701 000004      MOV      #4,R1      :CHECK THEM WITH AC=0
022370 005020      U13:     CLR     (R0)+

```

```

023012    023012    SOB    R1,U13
023014    023014    MOV    #1, @BUFLAG
023016    023016    MOV    @223, @OUTMP1
023018    023018    MOV    @223, @OUTMP2
023020    023020    JMP    @BU1

U14:
LPERR :SET UP THE LOOP ON ERROR ADDRESS.
: NOW SEE IF A TRAP CAN BE FORCED BY SETTING FIUV AND LOADING -0
MOV    @UERR3, @FPVECT :SET FD AND FIUV
MOV    @4200, R0 :SET JP ACC
LDFPS  R0 :SET JP ACC
MOV    @UPAT00, R0 :_OAC -0
LDD    (R0), ACC :SHOULD TRAP TO 244
MOV    @UPAT40, R0
LDD    (R0), ACC
U15:
U16: CFCC
NOP
MOV    @U15, @STMP2 :REPORT ERROR.
: DIDN'T TRAP
: (BUT FIUV) FAILED.

: $:
ERROR 127
BR     UDONE

: TRAPPED TO 244. DID (BUT FIUV) FAIL?
UERR0: CMP    (SP), @U11
BEQ    IS
JMP    @FPSPUR
IS:    MOV    (SP), @STMP2
CMP    (SP)+, (SP)+
ZS:    ERROR 126
BR     UDONE

: COME HERE TO ANALYZE FPS ERRORS

UERR1: BIT    #4, R5
BEQ    UERR20
UERR10: MOV    @443, @STMP5
MOV    @UROM3, R3
MOV    R3, @STMP7
BIT    #200, R3
BEQ    IS
BIC    #200, R3
BR     ZS
IS:    BIS    #200, R3
ZS:    MOV    R3, @STMP6
JERR11: MOV    R5, @STMP3
MOV    R4, @STMP4
IS:    ERROR 124
BR     UDONE
UERR2: BIT    #4, R5
BEQ    UERR10
UERR20: MOV    @UROM1, @STMP5
MOV    @UROM2, R3
MOV    R3, @STMP7
BIT    #400, R3
BEQ    IS
BIC    #400, R3

```

# E08

```

022640 000402
022642 052703 002400
022644 010337 001246
022646 010527 001240
022648 010427 001242
022650 104125
022652 000460
022654 021627 022454
022656 051402
022658 030137 040200
022660 022626
022662 005000
022664 170300
022666 022700 000014
022668 001001
022670 000444
022672 012737 022452 001236
022674 012737 000012 001242
022676 010037 001240
022678 104130
022680 000432
022682 000000
022684 000000
022686 000000
022688 000000
022690 022752 000000
022692 022754 000000
022694 022756 000000
022696 022760 000000
022698 022762 010421
022700 022764 114631
022702 022766 125252
022704 022770 177777
022706 022772 114631
022708 022774 135673
022710 022776 146314
022712 023000 167356
022714 023002 100000
022716 023004 000000
022718 023006 000000
022720 023010 000000
022722 023012 000000
022724 023014 000000
022726 023016 000000
022728 023020 000000
022730 023022 000000
022732 023024 000000
022734 023026
  
```

```

BR 25
BIS #400,R3
MOV R3,#STMP6
LERR21: MOV R5,#STMP3
MOV R4,#STMP4
IS: ERROR 125
BR UDONE

; INTERRUPT HERE WHEN FIUV SET AND ATTEMPTED TO LOAD-0
LERR3: JMP (SP),#UI6
BEQ IS
JMP #FPSPJR
IS: CMP (SP)+,(SP)+
CLR R0
STST R0 ;GET FEC.
CMP #14,R0 ;CORRECT
BNE LERR4
BR UDONE
LERR4: MOV #UI5,#STMP2
MOV #12,#STMP4
MOV R0,#STMP3
IS: ERROR 130
BR UDONE

UPAT00: .WORD 0
UPAT01: 0
UPAT02: 0
UPAT03: 0

UPAT10: .WORD 0 ;0
UPAT11: 0
UPAT12: 0
UPAT13: 0

UPAT20: .WORD 010421 ;POS NUM
UPAT21: 114631
UPAT22: 125252
UPAT23: 177777

UPAT30: 114631 ;NEG NUM
UPAT31: 135673
UPAT32: 146314
UPAT33: 167356

UPAT40: 100000 ;NEG ZERO
UPAT41: 0
UPAT42: 0
UPAT43: 0

UFLAG: .WORD 0
UTMP1: 0
UTMP2: 0
UROM1: 0
UROM2: 0
UROM3: 0
UDONE:
  
```



# F08

7  
53200  
53201  
53202  
53203  
53204  
53205  
53206  
53207  
53208  
53209  
53210  
53211  
53212  
53213  
53214  
53215  
53216  
53217  
53218  
53219  
53220  
53221  
53222  
53223  
53224  
53225  
53226  
53227  
53228  
53229

```
023026 000004  
023030  
023030 104413  
023032 012700 000200  
023036 170100  
023040 012700 023562  
023044 172410  
023046 012737 023060 001236  
023054 012700 023562  
023060 172010  
023062 170205  
023064 170011  
023066 012700 023562  
023072 174010  
023074 012701 023562  
023100 012702 000004  
023104 022021  
023106 001405  
  
023110 004737 023530  
023114 104133  
023116 000137 023602  
023122 077210  
023124 022705 000204  
023130 001410  
  
023132 012737 000204 001242  
023140 010537 001240  
023144 104137  
023146 000137 023602  
023152  
023152 104413  
023154 012700 000200  
023160 170100  
023162 012700 023562  
023166 172410  
023170 012737 023206 001236  
023176 005000  
023200 170100  
023202 012700 023562  
023206 172010  
023210 170205  
023212 170011  
023214 012700 023562  
023220 174010  
023222 012701 023562  
023226 012702 000004
```

```
*****  
*TEST 25 ADDF,ADD,SUBF AND SUBD WITH FSRC=AC=0 TEST  
*****  
* THIS IS A TEST OF ADD AND SUB WITH FSRC=AC=0  
*****  
*ST25: SCOPE  
W1: LFERR ;SET UP THE LOOP ON ERROR ADDRESS.  
MOV #200,RO ;SET DOUBLE MODE  
LDFPS RO ;LOAD ACC=0  
MOV #WPAT00,RO  
LDD (RO),ACC  
MOV #W2,#STMP2  
MOV #WPAT00,RO  
W2: ADDD (RO),ACC ;TEST INSTRUCTION.  
STFPS R5 ;GET FPS  
SETD ;SET DOUBLE MODE  
MOV #WPAT00,RO ;GET THE RESULT  
STD ACC,(RO)  
MOV #WPAT00,R1  
W3: MOV #4,R2 ;IS RESULT CORRECT  
CMP (RO)+,(R1)+  
BEQ W4 ;NO  
  
W4: JSR PC,#WSETUP  
ERROR 133  
JMP #WDONE  
W4: SOB R2,W3 ;IS FPS CORRECT  
CMP #204,R5  
BEQ W5 ;NO  
  
W5: MOV #204,#STMP4  
MOV R5,#STMP3  
W5: ERROR 137  
JMP #WDONE  
  
W6: LFERR ;SET UP THE LOOP ON ERROR ADDRESS.  
MOV #200,RO ;SET DOUBLE MODE  
LDFPS RO ;LOAD ACC=0  
MOV #WPAT00,RO  
LDD (RO),ACC  
MOV #W6,#STMP2  
CLR RO ;GO TO FLOATING MODE  
LDFPS RO  
W6: MOV #WPAT00,RO ;TEST INSTRUCTION  
ADDF (RO),ACC ;GET FPS  
STFPS R5 ;RESET TO DOUBLE MODE  
SETD ;GET THE RESULT  
MOV #WPAT00,RO  
STD ACC,(RO)  
MOV #WPAT00,R1  
W6: MOV #4,R2
```

5330	023332	022021		W7:	CMP	(R0)+, (R1)+	: WAS THE RESULT
5331	023334	001407			BEG	W10	: NO. REPORT FAILURE.
5332	023336	104134		15:	ERROR	134	
5333	023340	000560			BR	W00NE	
5334	023344	022705	000304	W10:	SOB	R2, W7	: WAS FPS CORRECT
5335	023344	022705	000304		CMP	R4, R5	
5336	023350	001407			BEG	W11	: INCORRECT FPS.
5337	023352	012737	000004		MOV	R4, 2#STMP4	
5338	023352	010537	001242		MOV	R5, 2#STMP3	
5339	023364	104140		15:	ERROR	140	
5340	023366	000545			BR	W00NE	
5341	023370	104113		W11:	LPERR		: SET UP THE LOOP ON ERROR ADDRESS.
5342	023372	012700	000200		MOV	#200, R0	
5343	023376	170100			LDFPS	R0	: SET DOUBLE MODE
5344	023380	012700	023562		MOV	#WPAT00, R0	: LOAD AC0=0
5345	023384	172410			LDD	(R0), AC0	
5346	023386	012737	023320		MOV	#W12, 2#STMP2	
5347	023390	012700	023552	001236	MOV	#WPAT00, R0	
5348	023394	173010		W12:	SUBD	(R0), AC0	: TEST INSTRUCTION
5349	023398	170205			STFPS	R5	: GET FPS
5350	023402	170011			SETD		: SET DOUBLE MODE
5351	023406	012700	023562		MOV	#WPAT00, R0	
5352	023410	174010			STD	AC0, (R0)	: GET THE RESULT
5353	023414	012701	023562		MOV	#WPAT00, R1	
5354	023418	012702	000304		MOV	R4, R2	
5355	023422	022021		W13:	CMP	(R0)+, (R1)+	: IS RESULT CORRECT?
5356	023426	001404			BEG	W14	: NO.
5357	023430	004737	023530		JSR	PC, 2#WSETUP	
5358	023434	104135		15:	ERROR	135	
5359	023438	000511			BR	W00NE	
5360	023442	077207		W14:	SOB	R2, W13	: IS FPS CORRECT?
5361	023446	022705	000204		CMP	#204, R5	
5362	023450	001407			BEG	W15	: NO.
5363	023454	012737	000204		MOV	#204, 2#STMP4	
5364	023458	010537	001242		MOV	R5, 2#STMP3	
5365	023462	104141		15:	ERROR	141	
5366	023466	000476			BR	W00NE	
5367	023470	104413		W15:	LPERR		: SET UP THE LOOP ON ERROR ADDRESS.
5368	023474	012700	000200		MOV	#200, R0	
5369	023478	170100			LDFPS	R0	: SET DOUBLE MODE
5370	023482	012700	023562		MOV	#WPAT00, R0	: LOAD AC0=0
5371	023486	172410			LDD	(R0), AC0	
5372	023490	012737	023442	001236	MOV	#W16, 2#STMP2	
5373	023494	005000			CLR	R0	
5374	023498	170100			LDFPS	R0	: ENTER FLOATING MODE.
5375	023502	012700	023562		MOV	#WPAT00, R0	
5376	023506	173010		W16:	SUBF	(R0), AC0	: TEST INSTRUCTION.
5377	023510	170205			STFPS	R5	: GET FPS
5378	023514	170011			SETD		: RESET TO DOUBLE MODE
5379	023518	012700	023562		MOV	#WPAT00, R0	: GET THE RESULT.
5380	023522	174010			STD	AC0, (R0)	

H08

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023456 012701 023562      MOV      #WPAT00,R1
023460 012702 000004      MOV      #4,R2
023466 022021      W17:     CMP      (R0)+,(R1)+      ;IS RESULT CORRECT?
023470 001404      BEQ      W20              ;NO.
023472 004737 023530      JSR      PC,@WSETUP
023476 104136      IS:     ERROR 136
023500 000440      BR      WDONE
023502 077207      W20:     SOB      R2,W17
023504 022705 000004      CMP      #4,R5            ;IS FPS CORRECT?
023510 001434      BEQ      WDONE           ;NO
023512 012737 000004 001242      MOV      #4,@STMP4
023520 010537 001240      MOV      R5,@STMP3
023524 104142      IS:     ERROR 142
023526 000425      BR      WDONE
;SET UP FOR ERROR CALL
023530 012737 023562 001240      WSETUP: MOV      #WPAT00,@STMP3
023536 012737 023562 001242      MOV      #WPAT00,@STMP4
023544 012737 023562 001246      MOV      #WPAT00,@STMP6
023552 012737 023562 001244      MOV      #WPAT00,@STMP5
023560 000207      RTS      PC
023562 000000      WPAT00: .WORD 0
023564 000000      WPAT01:      0
023566 000000      WPAT02:      0
023570 000000      WPAT03:      0
023572 000000      WDATA00: .WORD 0
023574 000000      WDATA01:      0
023576 000000      WDATA02:      0
023600 000000      WDATA03:      0
023602      WDONE:
023602 104412      RSETUP      ;GO INITIALIZE THE FPS AND STACK; AND
;SEE IF THE USER HAS EXPRESSED
;THE DESIRE TO CHANGE THE SOFTWARE
;VIRTUAL CONSOLE SWITCH REGISTER (HAS
;THE USER TYPED CONTROL G?).

;*****
;*TEST 26      ADDO AND SUB WITH FSRC=0
;*
;* THIS IS A TEST OF ADD AND SUB WITH FSRC=0.
;*
;*****
023604 000004      *ST26:     SCOPE
023606      X1:
023606 104413      LPERR      ;SET UP THE LOOP ON ERROR ADDRESS.
023610 012700 000200      MOV      #200,R0
023614 170100      LDFPS     R0            ;SET DOUBLE MODE
023616 012700 024346      MOV      #XPAT00,R0     ;SET ACD TO POSITIVE
023622 010037 024334      MOV      R0,@XTMP      ;NUMBER #0

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437	024062	174010			STD	AC0,(R0)				
436	024056	012700	024336		MOV	#XDAT00,R0				;GET RESULT
435	024054	170011			SETD					
434	024052	170205			STFPS	R5				
433	024050	173010		X12:	SUBD	(R0),AC0				;TEST INSTRUCTION
432	024044	012700	024356		MOV	#XPAT10,R0				;FSRC=0
431	024036	012737	024050	001236	MOV	#X12,#STMP2				
430	024034	172410			LDD	(R0),AC0				
429	024030	010037	024334		MOV	R0,#XTMP				;POSITIVE NUMBER
428	024024	012700	024346		MOV	#XPAT00,R0				;SET ACO TO NON-ZERO
427	024022	170100			LDFPS	R0				;SET DOUBLE MODE
426	024016	012700	000200		MOV	#200,R0				;SET UP THE LOOP ON ERROR ADDRESS.
425	024014	104413		X11:	LPERR					
424	024014	024014			BR	XERR2				
423	024010	000534			BEQ	X11				
422	024006	001401			CMP	R4,R5				;IS FPS CORRECT?
421	024002	012704	000210		MOV	#210,R4				
420	024000	077204		X10:	SOB	R2,X7				
419	023776	000511			BR	XERR1				
418	023774	001401			BEQ	X10				
417	023772	022021		X7:	CMP	(R0)+,(R1)+				;IS RESULT CORRECT?
416	023766	012702	000004		MOV	#4,R2				
415	023762	012701	024366		MOV	#XPAT20,R1				
414	023760	174010			STD	AC0,(R0)				
413	023754	012700	024336		MOV	#XDAT00,R0				;GET RESULT
412	023752	170011			SETD					
411	023750	170205		X6:	STFPS	R5				;TEST INSTRUCTION
410	023746	172010			ADD	(R0),AC0				;FSRC=0
409	023742	012700	024356		MOV	#XPAT10,R0				
408	023734	012737	023746	001236	MOV	#X6,#STMP2				
407	023732	172410			LDD	(R0),AC0				
406	023726	010037	024334		MOV	R0,#XTMP				;NEGATIVE NUMBER
405	023722	012700	024366		MOV	#XPAT20,R0				;SET ACO TO
404	023720	170100			LDFPS	R0				;SET DOUBLE MODE
403	023714	012700	000200		MOV	#200,R0				;SET UP THE LOOP ON ERROR ADDRESS.
402	023712	104413		X5:	LPERR					
401	023706	000137	024304		JMP	#XERR2				
400	023704	001402			BEQ	X5				
399	023702	020405			CMP	R4,R5				;IS FPS CORRECT?
398	023700	020405			MOV	#200,R4				
397	023676	012704	000200		SOB	R2,X3				
396	023674	077204		X4:	SOB	R2,X3				
395	023672	000553			BR	XERR1				
394	023670	001401			BEQ	X4				
393	023666	022021		X3:	CMP	(R0)+,(R1)+				;IS RESULT CORRECT?
392	023662	012702	000004		MOV	#4,R2				
391	023656	012701	024346		MOV	#XPAT00,R1				
390	023654	174010			STD	AC0,(R0)				
389	023650	012700	024336		MOV	#XDAT00,R0				;GET RESULT.
388	023646	170011			SETD					
387	023644	170205			STFPS	R5				
386	023642	172010		X2:	ADD	(R0),AC0				;TEST INSTRUCTION
385	023636	012700	024356		MOV	#XPAT10,R0				;FSRC=0
384	023630	012737	001236		MOV	#X2,#STMP2				
383	023626	172410			LDD	(R0),AC0				

J08

024064	012701	024346			MOV	#XPAT00,R1	
024070	012702	000004			MOV	#4,R2	
024074	022021			X13:	CMP	(R0)+,(R1)+	;IS RESULT CORRECT?
024076	001401				BEQ	X14	
024100	000463				BR	XERR3	
024102	077204			X14:	SOB	R2,X13	
024104	012704	000200			MOV	#200,R4	;IS FPS CORRECT?
024110	020405				CMP	R4,R5	
024112	001401				BEQ	X15	
024114	000501			X15:	BR	XERR4	
024116							;SET UP THE LOOP ON ERROR ADDRESS.
024118	104413				LPERR		
024120	012700	000200			MOV	#200,R0	
024124	170100				LDFPS	R0	;SET DOUBLE MODE
024126	012700	024366			MOV	#XPAT20,R0	;SET ACD=A NEGATIVE
024132	010037	024334			MOV	R0,@#XTMP	;NUMBER
024136	172410				LDD	(R0),ACD	
024140	012737	024152	001236		MOV	#X16,@#STMP2	
024146	012700	024356			MOV	#XPAT10,R0	;FSRC=0
024152	173010			X16:	SUBD	(R0),ACD	;TEST INSTRUCTION.
024154	170205				STFPS	R5	
024156	170011				SETD		
024160	012700	024336			MOV	#XDAT00,R0	;GET RESULT
024164	174010				STD	ACD,(R0)	
024166	012701	024366			MOV	#XPAT20,R1	
024172	012702	000004			MOV	#4,R2	
024176	022021			X17:	CMP	(R0)+,(R1)+	;IS RESULT CORRECT?
024200	001401				BEQ	X20	
024202	000422				BR	XERR3	
024204	077204			X20:	SOB	R2,X17	
024206	012704	000210			MOV	#210,R4	;IS FPS CORRECT?
024212	020405				CMP	R4,R5	
024214	001401				BEQ	X21	
024216	000440			X21:	BR	XERR4	
024220	000466				BR	XDONE	
							;REPORT DATA ERRORS
024222	012737	024356	001240	XERR1:	MOV	#XPAT10,@#STMP3	
024230	013737	024334	001242		MOV	@#XTMP,@#STMP4	
024236	012737	024336	001244		MOV	#XDAT00,@#STMP5	
024244	104143			15:	ERROR	143	
024246	000453				BR	XDONE	
024250	012737	024356	001240	XERR3:	MOV	#XPAT10,@#STMP3	
024256	013737	024334	001242		MOV	@#XTMP,@#STMP4	
024264	012737	024336	001244		MOV	#XDAT00,@#STMP5	
024272	013737	024334	001246		MOV	@#XTMP,@#STMP6	
024300	104144			15:	ERROR	144	
024302	000435				BR	XDONE	
							;REPORT FPS ERRORS
024304				XERR2:			
024304	010537	001240			MOV	R5,@#STMP3	
024310	010437	001242			MOV	R4,@#STMP4	
024314	104145			15:	ERROR	145	

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5554 024316 000427
5555 024320
5556 024320 010537 001240
5557 024324 010437 001242
5558 024330 104146
5559 024332 000421
5560 024334 000000
5561 024336 000000
5562 024340 000000
5563 024342 000000
5564 024344 000000
5565
5566 024346 010421
5567 024350 021042
5568 024352 031463
5569 024354 042104
5570
5571 024356 000000
5572 024360 000000
5573 024362 000000
5574 024364 000000
5575 024366 104210
5576 024370 114631
5577 024372 125252
5578 024374 135673
5579
5580 024376
5581 024376 104412
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5595 024400 000004
5596 024402 005037 024732
5597 024406 012737 024752 024734
5598 024414 012737 024762 024736
5599 024422 012737 000210 024740
5600 024430
5601 024430 104413
5602 024432 012700 000200
5603 024436 170100
5604 024440 012700 024772
5605 024444 172410
5606 024446 013700 024734
5607 024452 173010
5608 024454 170205
5609 024456 170011

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```

BR XDONE
XERR4:
MOV R5, @#STMP3
MOV R4, @#STMP4
IS:
ERROR 146
BR XDONE
XTMP: .WORD 0
XDAT00: .WORD 0
XDAT01: 0
XDAT02: 0
XDAT03: 0
XPAT00: .WORD 010421
XPAT01: 021042
XPAT02: 031463
XPAT03: 042104
XPAT10: .WORD 0
XPAT11: 0
XPAT12: 0
XPAT13: 0
XPAT20: .WORD 104210
XPAT21: 114631
XPAT22: 125252
XPAT23: 135673

```

```

XDONE:
RSETUP
;GO INITIALIZE THE FPS AND STACK; AND
;SEE IF THE USER HAS EXPRESSED
;THE DESIRE TO CHANGE THE SOFTWARE
;VIRTUAL CONSOLE SWITCH REGISTER (HAS
;THE USER TYPED CONTROL G?).

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```

;*****
;TEST 27 SUBD WITH AC=0 TEST
;
; THIS IS A TEST OF SUBD WITH AC=0. BOTH POSITIVE
; AND NEGATIVE FSRC'S ARE TRIED.
;
;*****

```

```

;*****
†ST27: SCOPE
CLR @#YFLAG
MOV #YPAT00, @#YTMP1 ;P
MOV #YPAT10, @#YTMP2 ;N
MOV #210, @#YTMP3
Y1:
LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV #200, R0
LDFPS R0 ;SET DOUBLE MODE
MOV #YPAT20, R0 ;SET ACO=0
LDD (R0), ACO
MOV @#YTMP1, R0
Y2:
SUBD (R0), ACO ;TEST INSTRUCTION
STFPS R5
SETD

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L08

5610	024460	012700	024742		MOV	#YDAT00,R0	;GET RESULT
5611	024464	012700			STD	AC0,(R0)	
5612	024466	012702	000004		MOV	#4,R2	
5613	024472	013701	024736		MOV	@YTMP2,R1	;CHECK RESULT.
5614	024476	022021		Y3:	CMP	(R0)+,(R1)+	
5615	024500	001026			BNE	Y6	
5616	024502	077203			SOB	R2,Y3	
5617	024504	023705	024740		CMP	@YTMP3,R5	;FPS CORRECT?
5618	024510	001401			BEQ	Y4	
5619	024512	000475			BR	YERR3	
5620	024514	005737	024732	Y4:	TST	@YFLAG	;FINISHED TEST?
5621	024520	001015			BNE	Y5	
5622	024522	012737	177777	024732	MOV	#-1,@YFLAG	
5623	024530	012737	024762	024734	MOV	#YPAT10,@YTMP1	
5624	024536	012737	024752	024736	MOV	#YPAT00,@YTMP2	
5625	024544	012737	000200	024740	MOV	#200,@YTMP3	
5626	024552	000726			BR	Y1	
5627	024554	000512		Y5:	BR	YDONE	
5628	024556	012702	000004	Y6:	MOV	#4,R2	
5629	024562	012700	024734		MOV	@YTMP1,R0	;DID XOR OF SIGN BIT
5630	024566	012701	024742		MOV	#YDAT00,R1	;FAIL?
5631	024572	022021		Y7:	CMP	(R0)+,(R1)+	
5632	024574	001002			BNE	YERR1	
5633	024576	077203			SOB	R2,Y7	
5634	024600	000421			BR	YERR2	
5635	024602			YERR1:			;DATA FAILURE
5636	024602	012737	024452	001236	MOV	#Y2,@STMP2	
5637	024610	013737	024734	001240	MOV	@YTMP1,@STMP3	
5638	024616	012737	024772	001242	MOV	#YPAT20,@STMP4	
5639	024624	012737	024742	001244	MOV	#YDAT00,@STMP5	
5640	024632	013737	024736	001246	MOV	@YTMP2,@STMP6	
5641	024640	104147		15:	ERROR	147	
5642	024642	000457			BR	YDONE	
5643	024644			YERR2:			;XOR OF SIGN BIT
5644	024644	012737	024452	001236	MOV	#Y2,@STMP2	;FAILED
5645	024652	013737	024734	001240	MOV	@YTMP1,@STMP3	
5646	024660	012737	024772	001242	MOV	#YPAT20,@STMP4	
5647	024666	012737	024742	001244	MOV	#YDAT00,@STMP5	
5648	024674	013737	024736	001246	MOV	@YTMP2,@STMP6	
5649	024702	104150		15:	ERROR	150	
5650	024704	00043E			BR	YDONE	
5651	024706			YERR3:			;FPS WRONG.
5652	024706	012737	024452	001236	MOV	#Y2,@STMP2	
5653	024714	010537	001240		MOV	R5,@STMP3	
5654	024720	013737	024740	001242	MOV	@YTMP3,@STMP4	
5655	024726	104151		15:	ERROR	151	
5656	024730	000424			BR	YDONE	
5657							
5658	024732	000000		YFLAG:	.WORD	0	
5659	024734	000000		YTMP1:		0	
5660	024736	000000		YTMP2:		0	
5661	024740	000000		YTMP3:		0	
5662							
5663	024742	000000		YDAT00:	.WORD	0	
5664	024744	000000		YCAT01:		0	
5665	024746	000000		YDAT02:		0	

M08

5666	024750	000000	YDAT03:	0
5667				
5668	024752	063146	YPAT00:	063146
5669	024754	052525	YPAT01:	052525
5670	024756	042104	YPAT02:	042104
5671	024760	167356	YPAT03:	167356
5672				
5673	024762	163146	YPAT10:	163146
5674	024764	052525	YPAT11:	052525
5675	024766	042104	YPAT12:	042104
5676	024770	167356	YPAT13:	167356
5677				
5678	024772	000000	YPAT20:	0
5679	024774	000000	YPAT21:	0
5680	024776	000000	YPAT22:	0
5681	025000	000000	YPAT23:	0
5682				
5683	025002		YDONE:	
5684	025002	104412	RSETJP	

;GO INITIALIZE THE FPS AND STACK; AND  
;SEE IF THE USER HAS EXPRESSED  
;THE DESIRE TO CHANGE THE SOFTWARE  
;VIRTUAL CONSOLE SWITCH REGISTER (HAS  
;THE USER TYPED CONTROL G?).

\*\*\*\*\*  
;\*TEST 30 ADD WITH AC=0 TEST

;@  
;@ THIS IS A TEST OF ADD WITH AC=0. BOTH  
;\* POSITIVE AND NEGATIVE FSRC'S ARE TRIED.  
;\*

\*\*\*\*\*

5698	025004	000004		
5699	025006	035067	000224	
5700	025012	012737	025254	025240
5701	025020	012737	000200	025242
5702	025026			
5703	025026	104413		
5704	025030	012700	000200	
5705	025034	170100		
5706	025036	012700	025274	
5707	025042	172410		
5708	025044	013700	025240	
5709	025050	172010		
5710	025052	170205		
5711	025054	170011		
5712	025056	012700	025244	
5713	025062	174010		
5714	025064	012702	000004	
5715	025070	013701	025240	
5716	025074	022021		
5717	025076	001401		
5718	025100	000423		
5719	025102	077204		
5720	025104	023705	025242	
5721	025110	001401		

```

TST30: SCOPE
        CLR      ZFLAG
        MOV      #ZPAT00,@ZTMP1 ;P
        MOV      #200,@ZTMP2
Z1:     LPERR                    ;SET UP THE LOOP ON ERROR ADDRESS.
        MOV      #200,R0
        LDFPS   R0                ;SET DOUBLE MODE
        MOV      #ZPAT20,R0       ;SET ACO=0
        LDD     (R0),AC0
        MOV      @ZTMP1,R0
Z2:     ADDD    (R0),AC0          ;TEST INSTRUCTION
        STFPS   R5
        SETD
        MOV      #ZDAT00,R0       ;GET RESULT
        STD     ACO,(R0)
        MOV      #4,R2
        MOV      @ZTMP1,R1       ;RESULT CORRECT?
Z3:     CMP     (R0)+,(R1)+
        BEQ     Z4
        BR     ZERR1
Z4:     SOB    R2,Z3
        CMP     @ZTMP2,R5        ;FPS CORRECT?
        BEQ     Z5

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104412  
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025244  
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001240  
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Z5:  
Z6:  
ZERR1:  
ZERR2:  
ZERR2:  
ZFLAG:  
ZTMP1:  
ZTMP2:  
ZDAT00:  
ZDAT01:  
ZDAT02:  
ZDAT03:  
ZPAT00:  
ZPAT01:  
ZPAT02:  
ZPAT03:  
ZPAT10:  
ZPAT11:  
ZPAT12:  
ZPAT13:  
ZPAT20:  
ZPAT21:  
ZPAT22:  
ZPAT23:  
ZDONE:  
RSETUP

BR ZERR2  
TST ZERR2  
BNE Z6  
MOV #1,ZERR2  
MOV ZPAT10,ZTMP1  
MOV Z20,ZTMP2  
BR Z1  
BR ZDONE  
MOV Z2,ZSTMP2  
MOV ZTMP1,ZSTMP3  
MOV ZPAT20,ZSTMP4  
MOV ZDAT00,ZSTMP5  
MOV ZTMP1,ZSTMP6  
ERRCR 152  
BR ZDONE  
MOV Z2,ZSTMP2  
MOV R5,ZSTMP3  
MOV ZTMP2,ZSTMP4  
ERROR 153  
BR ZDONE  
.WORD 0  
0  
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.WORD 0  
0  
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031463  
010421  
146314  
156735  
156735  
167356  
135673  
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:FINISHED TEST?  
:DATA FAILURE  
:GO INITIALIZE THE FPS AND STACK; AND  
:SEE IF THE USER HAS EXPRESSED  
:THE DESIRE TO CHANGE THE SOFTWARE  
:VIRTUAL CONSOLE SWITCH REGISTER (HAS  
:THE USER TYPED CONTROL G?).

::\*\*\*\*\*

TEST 31 ADDF AND ADDC WITH E(AC)=E(FSRC) TEST AND BUT FT TEST  
THIS IS A TEST OF THE ADD INSTRUCTION WITH THE  
OPERANDS HAVING EQUAL EXPONENTS. THE (BUT FT)  
FORK IN THE ROUND/TRUNK FLOWS IS ALSO TESTED.

\*\*\*\*\*

```

TST31: SCOPE
AA1: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
      MOV #3240,R0
      LDFPS R0 ;SET FIV FIV FD AND FT
      MOV #AAERR0,2#FPVECT ;IN CASE THE OVER/UNDER
      MOV #AAPATO,R0 ;FLOWS IN TRAP WILL
      ;OCCUR
      LDD (R0),AC0 ;SET UP AC0
      MOV #AA2,2#STMP2 ;OPERAND
      MOV #AAPAT1,R0
      AA2: ADDC (R0),AC0 ;TEST INSTRUCTION
      ;SHOULD TRUNCATE
      AA3: MOV #AADATO,R0
      STD AC0,(R0) ;GET THE RESULT
      MOV #AAPAT2,R1
      MOV #4,R2
      AA4: CMP (R0)+,(R1)+ ;CORRECT?
      BEQ AA7
      MOV #AAPAT3,R0 ;DID (BUT FT) FAIL
      MOV #AADATO,R1
      MOV #4,R2
      AA5: CMP (R0)+,(R1)+
      BEQ AA6
      BR AAERR1 ;DATA ERROR
      AA6: SOB R2,AA5
      CMP #AAERR2 ;(BUT FT) ERROR
      SOB R2,AA4

```

:NOW TEST DOUBLE FLOATING ROUND MODE.

```

AA10: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
      MOV #3200,R0 ;SET FD FIV FIV. FT=0
      LDFPS R0
      MOV #AAPATO,R0
      LDD (R0),AC0 ;SET UP AC0 OPERAND
      MOV #AA11,2#STMP2
      MOV #AAPAT1,R0
      AA11: ADDC (R0),AC0 ;TEST INSTRUCTION
      ;SHOULD ROUND
      AA12: MOV #AADATO,R0
      STD AC0,(R0) ;GET THE RESULT
      MOV #AAPAT3,R1
      MOV #4,R2
      AA13: CMP (R0)+,(R1)+ ;CORRECT?
      BEQ AA20
      MOV #AAPAT2,R0 ;DID (BUT FT) FAIL?
      MOV #AADATO,R1

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000001	000000		
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000011	000000		
000012	000000		
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000014	000000		
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026316  
025610  
025610 170C11  
026236  
026326  
000002  
026266  
026236  
000002  
026336  
001236  
04020C

AA14: MOV #4,R2  
CMP (R0)+,(R1)+  
BEQ AA17  
MOV #AAPAT4,R0 ;WAS THE FLOATING  
MOV #AADATO,R1 ;CONSTANT USED  
MOV #4,R2 ;INSTEAD OF THE  
AA15: CMP (R0)+,(R1)+ ;DOUBLE CONSTANT  
BEQ AA16 ;IN THE ROUND  
BR AAERR3 ;FLOWS?  
AA16: SOB R2,AA15 ;DATA ERROR  
BR AAERR4 ;CONSTANT ERROR  
AA17: SOB R2,AA14  
BR AAERR5 ;(BUT FT) ERROR  
AA20: SOB R2,AA13  
;NOW TEST ADDF WITH FT=0, ROUND MODE  
AA21: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
MOV #3200,R0 ;FIV=1, FIV=1, FT=0  
LDFPS RC  
MOV #AAPATO,R0 ;LOAD ACD OPERAND  
LDD (R0),ACD  
SETF ;ENTER FLOATING MODE  
MOV #AA22,#STMP2  
MOV #AAPA+5,R0  
AA22: ADDF (R0),ACD ;TEST INSTRUCTION  
;SHOULD ROUND  
AA23: SETD ;RESET TO DOUBLE  
;MODE  
MOV #AADATO,R0 ;GET THE RESULT  
STD ACC,(R0)  
MOV #AAPAT6,R1 ;CORRECT?  
MOV #2,R2  
AA24: CMP (R0)+,(R1)+  
BEQ AA27  
MOV #AAPAT2,R0 ;WAS THE DOUBLE  
MOV #AADATO,R1 ;CONSTANT USED INSTEAD  
MOV #2,R2 ;OF THE FLOATING  
AA25: CMP (R0)+,(R1) ;CONSTANT IN THE  
BEQ AA26 ;ROUND FLOWS?  
BR AAERR6 ;DATA ERROR  
AA26: SOB R2,AA25  
BR AAERR7 ;CONSTANT ERROR  
AA27: SOB R2,AA24  
JMP #AADONE  
;COME HERE IF A TRAP OCCURS TO 244.  
AAERR0: MOV #STMP2,R0 ;SEE IF THE TRAP WAS  
TST (R0)+ ;AT A TEST INSTRUCTION  
CMP R0,(SP)  
BEQ IS  
IS: JMP #FPSPUR

025706	170300			STST	RC		:GET FEC
025710	020027	000010		CMP	RC, #10		
025714	001405			BEG	20\$		:OVERFLOW
025716	020027	000012		CMP	RC, #12		
025722	001410			BEG	30\$		:UNDERFLOW
025724	000756			BR	10\$		
025726	025730						
025730	011637	001236		20\$:	MOV (SP), 2*STMP2		:REPORT OVERFLOW ERROR
025734	022626			CMP	(SP)+, (SP)+		
025736	104154			21\$:	ERROR 154		
025740	000137	026336		25\$:	JMP 2*ADONE		
025744	011637	001236		30\$:	MOV (SP), 2*STMP2		:REPORT UNDERFLOW
025750	022626			CMP	(SP)+, (SP)+		:ERROR
025752	104155			31\$:	ERROR 155		
025754	000771			BR	25\$		

:ADD RESULT INCORRECT

026756	012737	026266	001246	AAERR1:	MOV	AAPAT2, 2*STMP6	
026764	012737	026246	001242	AAERR10:	MOV	AAPAT0, 2*STMP4	
026772	012737	026256	001240		MOV	AAPAT1, 2*STMP3	
026780	012737	026236	001244		MOV	AADAT0, 2*STMP5	
026786	104152			1\$:	ERROR 162		
026790	000552			BR	ADONE		
026792	012737	026266	001246	AAERR2:	MOV	AAPAT2, 2*STMP6	: (BUT FT) FAILED.
026796	012737	026246	001242		MOV	AAPAT0, 2*STMP4	
026800	012737	026256	001240		MOV	AAPAT1, 2*STMP3	
026804	012737	026236	001244		MOV	AADAT0, 2*STMP5	
026808	104156			1\$:	ERROR 156		
026812	000534			BR	ADONE		
026816	012737	026276	001246	AAERR3:	MOV	AAPAT3, 2*STMP6	: DATA ERROR.
026820	000743			BR	AAERR10		
026824	012737	026276	001246	AAERR4:	MOV	AAPAT3, 2*STMP6	: BAD CONSTANT
026828	012737	026246	001242		MOV	AAPAT0, 2*STMP4	
026832	012737	026256	001240		MOV	AAPAT1, 2*STMP3	
026836	012737	026236	001244		MOV	AADAT0, 2*STMP5	
026840	104160			1\$:	ERROR 160		
026844	000512			BR	ADONE		
026848	012737	026276	001246	AAERR5:	MOV	AAPAT3, 2*STMP6	: (BUT FT) FAILED.
026852	012737	026246	001242		MOV	AAPAT0, 2*STMP4	
026856	012737	026256	001240		MOV	AAPAT1, 2*STMP3	
026860	012737	026236	001244		MOV	AADAT0, 2*STMP5	
026864	104157			1\$:	ERROR 157		
026868	000474			BR	ADONE		
026872	012737	026316	001240	AAERR6:	MOV	AAPAT5, 2*STMP3	: FD=0 AND
026876	012737	026246	001242		MOV	AAPAT0, 2*STMP4	: DATA ERROR
026880	012737	026236	001244		MOV	AADAT0, 2*STMP5	
026884	012737	026326	001246		MOV	AAPAT6, 2*STMP6	
026888	104160			1\$:	ERROR 160		
026892	000456			BR	ADONE		
026896	012737	026316	001240	AAERR7:	MOV	AAPAT5, 2*STMP3	: CONSTANT ERROR
026900	012737	026246	001242		MOV	AAPAT0, 2*STMP4	
026904	012737	026236	001244		MOV	AADAT0, 2*STMP5	
026908	012737	026326	001246		MOV	AAPAT6, 2*STMP6	
026912	104161			1\$:	ERROR 161		
026916	000440			BR	ADONE		
026920	000000			AADAT0:	0		

026240 000000  
026242 000000  
026244 000000  
026246 000200  
026248 000000  
026250 000000  
026252 000000  
026254 000200  
026256 000000  
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026260 000000  
026262 000000  
026264 000001  
026266 000400  
026268 000000  
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026306 000400  
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026318 000001  
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026322 000000  
026324 000000  
026326 000400  
026328 000001  
026330 000000  
026332 000000  
026334 000000  
026336 104412

APPAT0: 200  
APPAT1: 200  
APPAT2: 400  
APPAT3: 400  
APPAT4: 400  
APPAT5: 200  
APPAT6: 400  
ADDONE: RSETUP

;GO INITIALIZE THE FPS AND STACK; AND  
;SEE IF THE USER HAS EXPRESSED  
;THE DESIRE TO CHANGE THE SOFTWARE  
;VIRTUAL CONSOLE SWITCH REGISTER (HAS  
;THE USER TYPED CONTROL G?).

\*\*\*\*\*  
\*TEST 32 ADDF AND ADD WITH E(AC) LESS THAN E(FSRC) TEST  
\*\*\*\*\*

\*THIS IS A TEST OF THE ADD AND ADDF  
\*INSTRUCTIONS AND THE ALIGN AC ALGORITHM  
\*FLOWS. THE CONSTANT (25 FOR FLOATING, 57 FOR  
\*DOUBLE) USED IS CHECKED. THEN SIMPLE  
\*AND WORST CASE ALIGNMENT SITUATIONS ARE  
\*TRIED. NOTE E(AC) IS LESS THEN E(FSRC)  
\*\*\*\*\*

ST32: SCOPE  
EXPONENT DIFFERENCE=57=71 (OCT) FD=1  
CCI:

LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
MOV #3200,R4 ;SET FIV,FIV, AND FD  
LDFPS R4  
MOV #CC2,2\*STMP2  
MOV #CCP0,R0 ;SET ACD OPERAND

# F09

6002	026464	172410				LDD	(R0),ACD		:ACC
6003	026464	172410	030014			MOV	#CCP2,R0		
6004	026464	172010			CC2:	ADD	(R0),ACD		:TEST INSTRUCTION
6005	026464	170205				STFPS	R5		:GET FPS
6006	026464	012700	027764			MOV	#CCDAT0,R0		:GET THE RESULT
6007	026464	174010				STD	ACD,(R0)		
6008	026464	012701	030014			MOV	#CCP2,R1		:IS IT CORRECT
6009	026464	012702	000004			MOV	#4,R2		
6010	026464	022021			CC3:	CMP	(R0)+,(R1)+		
6011	026464	001415				BEG	CC6		
6012	026464	012700	027764			MOV	#CCDAT0,R0		:DID A BAD
6013	026464	012701	027774			MOV	#CCP0,R1		:CONSTANT (NOT 57)
6014	026464	012702	000004			MOV	#4,R2		:GET GENERATED
6015	026464	022021			CC4:	CMP	(R0)+,(R1)+		:FOR THE ALIGNMENT
6016	026464	001402				BEG	CC5		:FLOWS?
6017	026464	000137	027362			JMP	#CCER1		:DATA ERROR.D
6018	026464	077205			CC5:	SOB	R2,CC4		
6019	026464	000137	027420			JMP	#CCER2		:BAD CONSTANT.D
6020	026464	077220			CC6:	SOB	R2,CC3		
6021	026464	020405				CMP	R4,R5		:FPS CORRECT?
6022	026464	001402				BEG	CC7		
6023	026464	000137	027326			JMP	#CCERO		:BAD FPS.
6024									:EXPONENT DIFFERENCE=56=70 (OCT) FD=1
6025	026464				CC7:				
6026	026464	104413				LPERR			:SET UP THE LOOP ON ERROR ADDRESS.
6027	026464	012704	003200			MOV	#3200,R4		:SET FIV,FIV, AND FD
6028	026464	170104				LDFPS	R4		
6029	026472	012737	026514	001236		MOV	#CC8,#STMP2		
6030	026502	012700	027774			MOV	#CCP0,R0		:SET ACD OPERAND
6031	026506	172410				LDD	(R0),ACD		
6032	026510	012700	030004			MOV	#CCP1,R0		:FSRC
6033	026514	172010			CC8:	ADD	(R0),ACD		:TEST INSTRUCTION
6034	026516	170205				STFPS	R5		:GET FPS
6035	026520	012700	027764			MOV	#CCDAT0,R0		:GET THE RESULT
6036	026524	174010				STD	ACD,(R0)		
6037	026526	012701	030064			MOV	#CCP7,R1		:IS IT CORRECT
6038	026532	012702	000004			MOV	#4,R2		
6039	026536	022021			CC9:	CMP	(R0)+,(R1)+		
6040	026540	001415				BEG	CC12		
6041	026542	012700	027764			MOV	#CCDAT0,R0		:DID A BAD
6042	026546	012701	030004			MOV	#CCP1,R1		:CONSTANT (NOT 57)
6043	026552	012702	000004			MOV	#4,R2		:GET GENERATED
6044	026556	022021			CC10:	CMP	(R0)+,(R1)+		:FOR THE ALIGNMENT
6045	026560	001402				BEG	CC11		:FLOWS?
6046	026562	000137	027456			JMP	#CCER3		:DATA ERROR.D
6047	026566	077205			CC11:	SOB	R2,CC10		
6048	026570	000137	027474			JMP	#CCER4		:BAD CONSTANT.D
6049	026574	077220			CC12:	SOB	R2,CC9		
6050	026576	020405				CMP	R4,R5		:FPS CORRECT?
6051	026600	001402				BEG	CC13		
6052	026602	000137	027326			JMP	#CCERO		:BAD FPS.
6053									:EXPONENT DIFFERENCE=25=31 (OCT) FD=0
6054	026606				CC13:				
6055	026606	104413				LPERR			:SET UP THE LOOP ON ERROR ADDRESS.
6056	026610	012737	026636	001236		MOV	#CC14,#STMP2		
6057	026616	012700	027774			MOV	#CCP0,R0		:SET UP ACD OPERAND.

```

6058 026622 172410 LDD (R0),AC0
6059 026624 012704 003000 MOV #3000,R4 ;SET FIV,FIV. CLEAR FD.
6060 026630 170104 LDFPS R4
6061 026632 012700 030054 MOV #CCP6,R0 ;FSRC
6062 026636 172010 CC14: ADDF (R0),AC0 ;TEST INSTRUCTION
6063 026640 170205 STFPS R5
6064 026642 170011 SETD ;REENTER DOUBLE MOVE
6065 026644 012700 027764 MOV #CCDAT0,R0 ;GET THE RESULT
6066 026650 174010 STD AC0,(R0)
6067 026652 012701 030054 MOV #CCP6,R1 ;IS THE RESULT CORRECT?
6068 026656 012702 000002 MOV #2,R2
6069 026662 022021 CC15: CMP (R0)+,(R1)+
6070 026664 001415 BEQ CC18
6071 026666 012700 027764 MOV #CCDAT0,R0 ;WAS A BAD CONSTANT
6072 026672 012701 030024 MOV #CCP3,R1 ;USED (NOT 25) IN
6073 026676 012702 000002 MOV #2,R2 ;THE ALIGN FLOWS?
6074 026702 022021 CC16: CMP (R0)+,(R1)+
6075 026704 001402 BEQ CC17
6076 026706 000137 027532 JMP #CCER5 ;DATA ERROR F
6077 026712 077205 CC17: SOB R2,CC16
6078 026714 000137 027566 JMP #CCER6 ;BAD CONSTANT F
6079 026720 077220 CC18: SOB R2,CC15
6080 026722 020405 CMP R4,R5
6081 026724 001402 BEQ CC19
6082 026726 000137 027344 JMP #CCER90 ;BAD FPS.
6083 ;EXPONENT DIFFERENCE=24=30 (OCT) FD=0
6084 026732 CC19: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
6085 026732 104413 MOV #CC20,#STMP2
6086 026734 012737 026762 001236 MOV #CCP3,R0 ;SET UP AC0 OPERAND.
6087 026742 012700 030024 LDD (R0),AC0
6088 026746 172410 LDD (R0),AC0
6089 026750 012704 003000 MOV #3000,R4 ;SET FIV,FIV. CLEAR FD.
6090 026754 170104 LDFPS R4
6091 026756 012700 030044 MOV #CCP5,R0 ;FSRC
6092 026762 172010 CC20: ADDF (R0),AC0 ;TEST INSTRUCTION
6093 026764 170205 STFPS R5
6094 026766 170011 SETD ;REENTER DOUBLE MOVE
6095 026770 012700 027764 MOV #CCDAT0,R0 ;GET THE RESULT
6096 026774 174010 STD AC0,(R0)
6097 026776 012701 030074 MOV #CCP10,R1 ;IS THE RESULT CORRECT?
6098 027002 012702 000002 MOV #2,R2
6099 027006 022021 CC21: CMP (R0)+,(R1)+
6100 027010 001415 BEQ CC24
6101 027012 012700 027764 MOV #CCDAT0,R0 ;WAS A BAD CONSTANT
6102 027016 012701 030044 MOV #CCP5,R1 ;USED (NOT 25) IN
6103 027022 012702 000002 MOV #2,R2 ;THE ALIGN FLOWS?
6104 027026 022021 CC22: CMP (R0)+,(R1)+
6105 027030 001402 BEQ CC23
6106 027032 000137 027622 JMP #CCER7 ;DATA ERROR F
6107 027036 077205 CC23: SOB R2,CC22
6108 027040 000137 027640 JMP #CCER8 ;BAD CONSTANT F
6109 027044 077220 CC24: SOB R2,CC21
6110 027046 020405 CMP R4,R5
6111 027050 001402 BEQ CC25
6112 027052 000137 027344 JMP #CCER90 ;BAD FPS.
6113 ;EXPONENT DIFFERENCE=1 FD=1

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6:1:4	027106	012704	003200		CC25:	LPERR		:SET UP THE LOOP ON ERROR ADDRESS.
6:1:5	027107	012704				MOV	#3200,R4	:SET FIV,FIV, AND FD
6:1:6	027108	170104				LDFPS	R4	
6:1:7	027109	012737	027106	001236		MOV	#CC26,2#STMP2	
6:1:8	027110	012700	027774			MOV	#CCP0,R0	:SET ACC OPERAND
6:1:9	027111	172410				LDD	(R0),ACC	
6:1:10	027112	012700	030024			MOV	#CCP3,R0	:FSRC
6:1:11	027113	172010			CC26:	ADDD	(R0),ACC	:TEST INSTRUCTION
6:1:12	027114	170205				STFPS	R5	:GET FPS
6:1:13	027115	012700	027764			MOV	#CCDAT0,R0	:GET THE RESULT
6:1:14	027116	174010				STD	ACC,(R0)	
6:1:15	027117	012701	030104			MOV	#CCP11,R1	:IS IT CORRECT
6:1:16	027118	012702	000004			MOV	#4,R2	
6:1:17	027119	022021			CC27:	CMP	(R0)+,(R1)+	
6:1:18	027120	001415				BEQ	CC30	
6:1:19	027121	012700	027764			MOV	#CCDAT0,R0	:DID A BAD
6:1:20	027122	012701	030024			MOV	#CCP3,R1	:CONSTANT (NOT 57)
6:1:21	027123	012702	000004			MOV	#4,R2	:GET GENERATED
6:1:22	027124	022021			CC28:	CMP	(R0)+,(R1)+	:FOR THE ALIGNMENT
6:1:23	027125	001402				BEQ	CC29	:FLOWS?
6:1:24	027126	000137	027674			JMP	2#CCER10	:DATA ERROR.D
6:1:25	027127	077205			CC29:	SOB	R2,CC28	
6:1:26	027128	000137	027712			JMP	2#CCER11	:BAD CONSTANT.D
6:1:27	027129	077220			CC30:	SOB	R2,CC27	
6:1:28	027130	020405				CMP	R4,R5	:FPS CORRECT?
6:1:29	027131	001402				BEQ	CC31	
6:1:30	027132	000137	027326			JMP	2#CCER0	:BAD FPS.
6:1:31	027200						:EXPONENT DIFFERENCE=100=144 (OCT) FD=1	
6:1:32	027200	104413			CC31:	LPERR		:SET UP THE LOOP ON ERROR ADDRESS.
6:1:33	027201	012704	003200			MOV	#3200,R4	:SET FIV,FIV, AND FD
6:1:34	027202	170104				LDFPS	R4	
6:1:35	027203	012737	027230	001236		MOV	#CC32,2#STMP2	
6:1:36	027204	012700	027774			MOV	#CCP0,R0	:SET ACC OPERAND
6:1:37	027205	172410				LDD	(R0),ACC	
6:1:38	027206	012700	030034			MOV	#CCP4,R0	:FSRC
6:1:39	027207	172010			CC32:	ADDD	(R0),ACC	:TEST INSTRUCTION
6:1:40	027208	170205				STFPS	R5	:GET FPS
6:1:41	027209	012700	027764			MOV	#CCDAT0,R0	:GET THE RESULT
6:1:42	027210	174010				STD	ACC,(R0)	
6:1:43	027211	012701	030034			MOV	#CCP4,R1	:IS IT CORRECT
6:1:44	027212	012702	000004			MOV	#4,R2	
6:1:45	027213	022021			CC33:	CMP	(R0)+,(R1)+	
6:1:46	027214	001415				BEQ	CC36	
6:1:47	027215	012700	027764			MOV	#CCDAT0,R0	:DID A BAD
6:1:48	027216	012701	030034			MOV	#CCP4,R1	:CONSTANT (NOT 57)
6:1:49	027217	012702	000004			MOV	#4,R2	:GET GENERATED
6:1:50	027218	022021			CC34:	CMP	(R0)+,(R1)+	:FOR THE ALIGNMENT
6:1:51	027219	001402				BEQ	CC35	:FLOWS?
6:1:52	027220	000137	027730			JMP	2#CCER12	:DATA ERROR.D
6:1:53	027221	077205			CC35:	SOB	R2,CC34	
6:1:54	027222	000137	027746			JMP	2#CCER13	:BAD CONSTANT.D
6:1:55	027223	077220			CC36:	SOB	R2,CC33	
6:1:56	027224	020405				CMP	R4,R5	:FPS CORRECT?
6:1:57	027225	001402				BEQ	CC37	



6170	027316	000137	027326			JMP	2#CCERO		:BAD FPS.
6171	027322	000137	030124			CC37: JMP	2#CCDONE		
6172	027326	010437	001242			CCERO: MOV	R4,2#STMP4		:FPS ERROR D
6173	027332	010537	001240			MOV	R5,2#STMP3		
6174	027336	104164				1S: ERROR	164		
6175	027340	000137	030124			JMP	2#CCDONE		
6176	027344	010437	001242			CCER90: MOV	R4,2#STMP4		:FPS ERROR F
6177	027350	010537	001240			MOV	R5,2#STMP3		
6178	027354	104165				1S: ERROR	165		
6179	027356	000137	030124			JMP	2#CCDONE		
6180	027362	012737	030014	001240		CCER1: MOV	#CCP2,2#STMP3		:DATA ERROR D
6181	027370	012737	030014	001246		MOV	#CCP2,2#STMP6		
6182	027376	012737	027774	001242		CCER50: MOV	#CCP0,2#STMP4		
6183	027404	012737	027764	001244		MOV	#CCDAT0,2#STMP5		
6184	027412	104166				1S: ERROR	166		
6185	027414	000157	030124			JMP	2#CCDONE		
6186	027420	012737	030014	001240		CCER2: MOV	#CCP2,2#STMP3		:CONSTANT BAD D(B)
6187	027426	012737	030014	001246		MOV	#CCP2,2#STMP6		
6188	027434	012737	027774	001242		CCER22: MOV	#CCP0,2#STMP4		
6189	027442	012737	027764	001244		MOV	#CCDAT0,2#STMP5		
6190	027450	104172				1S: ERROR	172		
6191	027452	000137	030124			JMP	2#CCDONE		
6192	027456	012737	030004	001240		CCER3: MOV	#CCP1,2#STMP3		
6193	027464	012737	030064	001246		MOV	#CCP7,2#STMP6		
6194	027472	000741				BR	CCER50		
6195	027474	012737	030004	001240		CCER4: MOV	#CCP1,2#STMP3		:CONSTANT BAD D(G)
6196	027502	012737	030064	001246		MOV	#CCP7,2#STMP6		
6197	027510	012737	027774	001242		CCER44: MOV	#CCP0,2#STMP4		
6198	027516	012737	027764	001244		MOV	#CCDAT0,2#STMP5		
6199	027524	104173				1S: ERROR	173		
6200	027526	000137	030124			JMP	2#CCDONE		
6201	027532	012737	030054	001240		CCER5: MOV	#CCP6,2#STMP3		:DATA ERROR F
6202	027540	012737	030054	001246		MOV	#CCP6,2#STMP6		
6203	027546	012737	027774	001242		CCER55: MOV	#CCP0,2#STMP4		
6204	027554	012737	027764	001244		MOV	#CCDAT0,2#STMP5		
6205	027562	104170				1S: ERROR	170		
6206	027564	000557				BR	CCDONE		
6207	027566	012737	030054	001240		CCER6: MOV	#CCP6,2#STMP3		:CONSTANT BAD F(B)
6208	027574	012737	030054	001246		MOV	#CCP6,2#STMP6		
6209	027602	012737	027774	001242		MOV	#CCP0,2#STMP4		
6210	027610	012737	027764	001244		MOV	#CCDAT0,2#STMP5		
6211	027616	104174				1S: ERROR	174		
6212	027620	000541				BR	CCDONE		
6213	027622	012737	030044	001240		CCER7: MOV	#CCP5,2#STMP3		:DATA ERROR F
6214	027630	012737	030074	001246		MOV	#CCP10,2#STMP6		
6215	027636	000743				BR	CCER55		
6216	027640	012737	030044	001240		CCER8: MOV	#CCP5,2#STMP3		:CONSTANT BAD F(G)
6217	027646	012737	030074	001246		MOV	#CCP10,2#STMP6		
6218	027654	012737	027764	001244		MOV	#CCDAT0,2#STMP5		
6219	027662	012737	027774	001242		MOV	#CCP0,2#STMP4		
6220	027670	104175				1S: ERROR	175		
6221	027672	000514				BR	CCDONE		
6222	027674	012737	030024	001240		CCER10: MOV	#CCP3,2#STMP3		:DATA ERROR D
6223	027702	012737	030104	001246		MOV	#CCP11,2#STMP6		
6224	027710	000632				BR	CCER50		
6225	027712	012737	030024	001240		CCER11: MOV	#CCP3,2#STMP3		:CONSTANT BAD D(G)

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6226 027727 012737 030104 001246 . MOV #CCP11,2#STMP6
6227 027728 000670 BR CCER44
6228 027730 012737 030034 001240 CCER12: MOV #CCP4,2#STMP3 ;DATA ERROR D
6229 027736 012737 030034 001246 MOV #CCP4,2#STMP6
6230 027744 000614 BR CCER50
6231 027746 012737 030034 001240 CCER13: MOV #CCP4,2#STMP3 ;CONSTANT BAD D(B)
6232 027754 012737 030034 001246 MOV #CCP4,2#STMP6
6233 027762 000624 BR CCER22
6234 027764 000000 CCDATO: 0
6235 027766 000000 0
6236 027770 000000 0
6237 027772 030000 0
6238 027774 000200 CCP0: 200 ;E(AC)=1
6239 027776 000000 0
6240 030000 000000 0
6241 030002 000000 0
6242 030004 016200 CCP1: 16200 ;E(FSRC)=E(AC)+56=57
6243 030006 000000 ; =71(OCT)
6244 030010 000000 0
6245 030012 000000 0
6246 030014 016400 CCP2: 16400 ;E(FSRC)=E(AC)+57=58
6247 030016 000000 ; =72(OCT)
6248 030020 000000 0
6249 030022 000000 0
6250 030024 000400 CCP3: 400 ;E(FSRC)=E(AC)+1=2
6251 030026 000000 0
6252 030030 000000 0
6253 030032 000000 0
6254 030034 031200 CCP4: 31200 ;E(FSRC)=E(AC)+100=101=145(OCT)
6255 030036 000000 0
6256 030040 000000 0
6257 030042 030000 0
6258 030044 006200 CCP5: 6200 ;E(FSRC)=E(AC)+24=25=31(OCT)
6259 030046 000000 0
6260 030050 000000 0
6261 030052 000000 0
6262 030054 006400 CCP6: 6400 ;E(FSRC)=E(AC)+25=26=32(OCT)
6263 030056 000000 0
6264 030060 000000 0
6265 030062 000000 0
6266 030064 016200 CCP7: 16200 ;CCP1 RES
6267 030066 000000 0
6268 030070 000000 0
6269 030072 000001 1
6270 030074 006200 CCP10: 6200 ;CCP5 RES
6271 030076 000001 1
6272 030100 000000 0
6273 030102 000000 0
6274 030104 000500 CCP11: 500 ;CCP3 RES
6275 030106 000000 0
6276 030110 000000 0
6277 030112 000000 0
6278 030114 000200 CCP12: 200 ;BAD CONSTANT
6279 030116 000000 ;RES CCP2,CCP4
6280 030120 000000 0
6281 030122 000000 0

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# K09

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6283 030124
6284 030124 104412
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6301 030126 000004
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6303 030130
6304 030130 104413
6305 030132 012704 003200
6306 030136 170104
6307 030140 012737 030766 000244
6308 030146 012737 030166 001236
6309
6310 030154 012700 031330
6311 030160 172410
6312 030162 012700 031320
6313 030166 172010
6314 030170 170205
6315 030172 012700 031300
6316 030176 174010
6317 030200 012701 031330
6318 030204 012702 000004
6319 030210 022021
6320 030212 001402
6321 030214 000137 031026
6322 030220 077205
6323
6324 030222 020405
6325 030224 001402
6326 030226 000137 030766
6327
6328 030232
6329 030232 104413
6330 030234 012704 003200
6331 030240 170104
6332 030242 012737 030262 001236
6333 030250 012700 031350
6334 030254 172410
6335 030256 012700 031320
6336 030262 172010
6337 030264 170205

CCDONE:
RSETUP
;GO INITIALIZE THE FPS AND STACK; AND
;SEE IF THE USER HAS EXPRESSED
;THE DESIRE TO CHANGE THE SOFTWARE
;VIRTUAL CONSOLE SWITCH REGISTER (HAS
;THE USER TYPED CONTROL G?).

;*****
;*TEST 33 ADDF AND ADD WITH E(AC) GREATER THAN E(FSRC) TEST
;*
;*THIS IS A TEST OF THE ADDD AND ADDF
;*INSTRUCTIONS AND THE ALIGN FSRC ALGORITHM
;*FLOWS. FIRST THE CONSTANT USED IS CHECKED.
;*THEN SIMPLE AND WORST CASE ALIGNMENT
;*SITUATIONS ARE TRIED. NOTE E(AC)
;*IS GREATER THAN E(FSRC).
;*
;*****
†ST33: SCOPE
;EXPONENT DIFFERENCE=57=71 (OCT) FD=1
BB1:
LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV #3200,R4 ;SET FIV FIV, AND FD
LDFPS R4
MOV #BBERO,2#FPVECT ;SET UP FOR ERROR
MOV #BB2,2#STMP2 ;IN CASE THE OVER\
;UNDER FLOWS FAIL.
;SET ACO OPERAND.
MOV #BBPAT2,R0
LDD (R0),ACO
MOV #BBPAT1,R0 ;FSRC
BB2: ADDD (R0),ACO ;TEST INSTRUCTION
STFPS R5
BB3: MOV #BBDAT0,R0 ;GET THE RESULT
STD ACO,(R0)
MOV #BBPAT2,R1 ;RESULT CORRECT?
MOV #4,R2
BB4: CMP (R0)+,(R1)+
BEQ BB5
JMP 2#BBER1 ;DATA ERROR D
BB5: SOB R2,BB4 ;WAS FPS CORRECT?
CMP R4,R5
BEQ BB6
JMP 2#BBERO ;FPS ERROR
;EXPONENT DIFFERENCE=56=70 (OCT) FD=1
BB6:
LPERR ;SET UP THE LOOP ON ERROR ADDRESS.
MOV #3200,R4 ;SET FIV,FIV, AND FD
LDFPS R4
MOV #BB7,2#STMP2
MOV #BBPAT4,R0 ;SET ACO OPERAND
LDD (R0),ACO
MOV #BBPAT1,R0 ;FSRC
BB7: ADDD (R0),ACO ;TEST INSTR' CTION
STFPS R5 ;GET FPS
  
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# L09

MAINDEC-11-DFFPA-A PDP 11 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 115  
 DFFPAA.P11 01-NOV-76 21:03 T33 ADDF AND ADD WITH E(AC) GREATER THAN E(FSRC) TEST

6338	030266	012700	031300			MOV	#BBDAT0,RO	;GET THE RESULT
6339	030272	174010				STD	AC0,(R0)	
6340	030274	012701	031410			MOV	#BBP10,R1	;IS IT CORRECT
6341	030300	012702	000004			MOV	#4,R2	
6342	030304	022021			BB10:	CMP	(R0)+,(R1)+	
6343	030306	001415				BEQ	BB13	
6344	030310	012700	031300			MOV	#BBDAT0,RO	;DID A BAD
6345	030314	012701	031350			MOV	#BBPAT4,R1	;CONSTANT (NOT 57)
6346	030320	012702	000004			MOV	#4,R2	;GET GENERATED
6347	030324	022021			BB11:	CMP	(R0)+,(R1)+	;FOR THE ALIGNMENT
6348	030326	001402				BEQ	BB12	;FLOWS?
6349	030330	000137	031064			JMP	#BBER2	;DATA ERROR.D
6350	030334	077205			BB12:	SOB	R2,BB11	
6351	030336	000137	031102			JMP	#BBER3	;BAD CONSTANT.D
6352	030342	077220			BB13:	SOB	R2,BB10	
6353	030344	020405				CMP	R4,R5	;FPS CORRECT?
6354	030346	001402				BEQ	BB14	
6355	030350	000137	030766			JMP	#BBER0	;BAD FPS.
6356								;EXPONENT DIFFERENCE=25=31 (OCT) FD=0
6357	030354				BB14:			
6358	030354	104413				LPERR		;SET UP THE LOOP ON ERROR ADDRESS.
6359	030356	012737	030404	001236		MOV	#BB15,#\$TMP2	
6360	030364	012700	031310			MOV	#BBPAT0,RO	;SET UP ACC OPERAND
6361	030370	172410				LDD	(R0),AC0	
6362	030372	012704	003000			MOV	#3000,R4	;SET FIV AND FIV
6363								;CLEAR FD
6364	030376	170104				LDFPS	R4	
6365	030400	012700	031320			MOV	#BBPAT1,RO	;FSRC
6366	030404	172010			BB15:	ADDF	(R0),AC0	;TEST INSTRUCTION
6367	030406	170205				STFPS	R5	
6368	030410	170011				SETD		;REENTERED DOUBLE MODE.
6369	030412	012700	031300			MOV	#BBDAT0,RO	;GET THE RESULT
6370	030416	174010				STD	AC0,(R0)	
6371	030420	012701	031310			MOV	#BBPAT0,R1	;IS THE RESULT
6372	030424	012702	000002			MOV	#2,R2	;CORRECT?
6373	030430	022021			BB16:	CMP	(R0)+,(R1)+	
6374	030432	001402				BEQ	BB17	
6375	030434	000137	031136			JMP	#BBER4	;DATA ERROR F
6376	030440	077205			BB17:	SOB	R2,BB16	
6377	030442	020405				CMP	R4,R5	;IS FPS CORRECT?
6378	030444	001402				BEQ	BB20	
6379	030446	000137	031006			JMP	#BBER10	;FPS ERROR.
6380								;EXPONENT DIFFERENCE=24=30 (OCT)
6381	030452				BB20:			
6382	030452	104413				LPERR		;SET UP THE LOOP ON ERROR ADDRESS.
6383	030454	012737	030502	001236		MOV	#BB21,#\$TMP2	
6384	030462	012700	031340			MOV	#BBPAT3,RO	;SET UP ACC OPERAND.
6385	030466	172410				LDD	(R0),AC0	
6386	030470	012704	003000			MOV	#3000,R4	;SET FIU,FIV. CLEAR FD.
6387	030474	170104				LDFPS	R4	
6388	030476	012700	031320			MOV	#BBPAT1,RO	;FSRC
6389	030502	172010			BB21:	ADDF	(R0),AC0	;TEST INSTRUCTION
6390	030504	170205				STFPS	R5	
6391	030506	170011				SETD		;REENTER DOUBLE MODE
6392	030510	012700	031300			MOV	#BBDAT0,RO	;GET THE RESULT
6393	030514	174010				STD	AC0,(R0)	

# M09

M2: NOEC-11-0FFPA-A PDP 11 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 116  
 0FFPA.F11 C1-NOV-76 21:03 T33 ADDF AND ADD WITH E(AC) GREATER THAN E(FSRC) TEST

6394	030516	012701	031400		MOV	#BBP7,R1	;IS THE RESULT CORRECT?
6395	030522	012702	000002		MOV	#2,R2	
6396	030526	022021		BB22:	CMP	(R0)+,(R1)+	
6397	030530	001415			BEQ	BB25	
6398	030532	012700	031300		MOV	#BBDAT0,R0	;WAS A BAD CONSTANT
6399	030536	012701	031340		MOV	#BBPAT3,R1	;USED (NOT 25) IN
6400	030542	012702	000002		MOV	#2,R2	;THE ALLIGN FLOWS?
6401	030546	022021		BB23:	CMP	(R0)+,(R1)+	
6402	030550	001402			BEQ	BB24	
6403	030552	000137	031172		JMP	#BBBER5	;DATA ERROR F
6404	030556	077205		BB24:	SOB	R2, BB23	
6405	030560	000137	031210		JMP	#BBBER6	;BAD CONSTANT F
6406	030564	077220		BB25:	SOB	R2, BB22	
6407	030566	020405			CMP	R4, R5	
6408	030570	001402			BEQ	BB26	
6409	030572	000137	031006		JMP	#BBBER10	;BAD FPS.
6410							
6411	030576						;EXPONENT DIFFERENCE=1
6412	030576	104413		BB26:	LPERR		;SET UP THE LOOP ON ERROR ADDRESS.
6413	030600	012737	030626	001236	MOV	#BB27,#\$STMP2	
6414	030606	012704	003200		MOV	#3200,R4	
6415	030612	170104			LDFPS	R4	;SET UP ACC OPERAND
6416	030614	012700	031360		MOV	#BBPAT5,R0	
6417	030620	172410			LDD	(R0),ACC	
6418	030622	012700	031320		MOV	#BBPAT1,R0	;FSRC
6419	030626	172010		BB27:	ADDD	(R0),ACC	;TEST INSTRUCTION
6420	030630	170205			STFPS	R5	
6421	030632	012700	031300		MOV	#BBDAT0,R0	;GET THE RESULT.
6422	030636	174010			STD	ACC,(R0)	
6423	030640	012701	031420		MOV	#BBP11,R1	;IS IT CORRECT?
6424	030644	012702	000004		MOV	#4,R2	
6425	030650	022021		BB30:	CMP	(R0)+,(R1)+	
6426	030652	001402			BEQ	BB31	
6427	030654	000137	031244		JMP	#BBBER7	;DATA ERROR D
6428	030660	077205		BB31:	SOB	R2, BB30	
6429	030662	020405			CMP	R4, R5	;IS FPS CORRECT
6430	030664	001402			BEQ	BB32	
6431	030666	000137	030766		JMP	#BBBER0	
6432							;EXPONENT DIFFERENCE=100=144 (OCT)
6433	030672			BB32:	LPERR		;SET UP THE LOOP ON ERROR ADDRESS.
6434	030672	104413			MOV	#BB33,#\$STMP2	
6435	030674	012737	030722	001236	MOV	#3200,R4	
6436	030702	012704	003200		MOV	#3200,R4	
6437	030706	170104			LDFPS	R4	;SET FIV, FIV AND FD
6438	030710	012700	031370		MOV	#BBPAT6,R0	;SET UP ACC OPERAND.
6439	030714	172410			LDD	(R0),ACC	
6440	030716	012700	031320		MOV	#BBPAT1,R0	;FSRC
6441	030722	172010		BB33:	ADDD	(R0),ACC	;TEST INSTRUCTION
6442	030724	170205			STFPS	R5	
6443	030726	012700	031300		MOV	#BBDAT0,R0	;GET THE RESULT
6444	030732	174010			STD	ACC,(R0)	
6445	030734	012701	031370		MOV	#BBPAT6,R1	;IS IT CORRECT
6446	030740	012702	000004		MOV	#4,R2	
6447	030744	022021		BB34:	CMP	(R0)+,(R1)+	
6448	030746	001402			BEQ	BB35	
6449	030750	000137	031262		JMP	#BBBER8	;DATA ERROR D

6470	031000	000137	031430		BB35:	SOB	R2, BB34		
6471	031000	012737	031330	001242		CMF	R4, R5		: IS FPS CORRECT
6472	031000	012737	031330	001246		BNE	BB3C		
6473	031034	012737	031330	001246		JMP	BB3C		
6474	031042	012737	031320	001240		BB3C:	MOV	R4, 2*STMP4	: FPS ERROR D
6475	031050	012737	031300	001244		MOV	R5, 2*STMP3		
6476	031056	104166			IS:	ERROR	164		
6477	031060	000137	031430			RSETUP			: GO INITIALIZE THE FPS AND STACK; AND
6478	031064	012737	031350	001242					: SEE IF THE USER HAS EXPRESSED
6479	031072	012737	031410	001246					: THE DESIRE TO CHANGE THE SOFTWARE
6480	031100	000760							: VIRTUAL CONSOLE SWITCH REGISTER HAS
6481	031102	012737	031350	001242					: THE USER TYPED CONTROL G?
6482	031110	012737	031410	001246					
6483	031116	012737	031320	001240					
6484	031124	012737	031300	001244					
6485	031132	104167			IS:	ERROR	165		
6486	031134	000535				RSETUP			: GO INITIALIZE THE FPS AND STACK; AND
6487	031136	012737	031310	001242					: SEE IF THE USER HAS EXPRESSED
6488	031144	012737	031310	001246					: THE DESIRE TO CHANGE THE SOFTWARE
6489	031152	012737	031320	001240					: VIRTUAL CONSOLE SWITCH REGISTER HAS
6490	031160	012737	031300	001244					: THE USER TYPED CONTROL G?
6491	031166	104170			IS:	ERROR	166		
6492	031170	000517				JMP	2*BB3C		
6493	031172	012737	031340	001242	BB3C:	MOV	R4, 2*STMP4		: DATA ERROR C
6494	031200	012737	031400	001246		MOV	2*BB3C		
6495	031206	0007E1				MOV	2*STMP6		
6496	031210	012737	031340	001242		BR	BB3C		
6497	031216	012737	031400	001246		BB3C:	MOV	2*STMP4	: DATA ERROR F
6498	031224	012737	031320	001240		MOV	2*STMP6		
6499	031232	012737	031300	001244		BB3C:	MOV	2*STMP3	
6500	031240	104171			IS:	MOV	2*STMP5		
6501	031242	000472				ERROR	167		
6502	031244	012737	031360	001242		BR	BB3C		
6503	031252	012737	031320	001246		BB3C:	MOV	2*STMP4	
6504	031260	000670				MOV	2*STMP6		
6505	031262	012737	031370	001242		BR	BB3C		: CONSTANT ERROR F
					BB3C:	MOV	2*STMP4		

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104412

031370 001246

MOV  
BR  
BBPAT0:  
BBPAT0:  
BBPAT1:  
BBPAT2:  
BBPAT3:  
BBPAT4:  
BBPAT5:  
BBPAT6:  
BBPAT7:  
BBPAT10:  
BBPAT11:  
BBDONE:

BBPAT6,2#STMP6  
BBPAT11

:E(AC)=E(FSRC)+25=26  
=32(OCT)

:E(FSRC)=1

:E(AC)=E(FSRC)+57=58  
=72(OCT)

:E(AC)=E(FSRC)+24=25  
=31(OCT)

:E(AC)=E(FSRC)+56=57  
=71(OCT)

:E(AC)=E(FSRC)+1=2

:E(AC)=E(FSRC)+100=101  
=145(OCT)

:BBPAT3 RES

:BBPAT4 RES

:BBPAT5 RES

:GO INITIALIZE THE FPS AND STACK; AND  
:SEE IF THE USER HAS EXPRESSED  
:THE DESIRE TO CHANGE THE SOFTWARE  
:VIRTUAL CONSOLE SWITCH REGISTER (HAS  
:THE USER TYPED CONTROL G?).

\*\*\*\*\*  
:TEST 34 ADD WITH NEGATIVE OPRANDS TEST  
:  
:THIS IS A TEST OF THE ADD INSTRUCTION

C10

031432 000004  
031434 004413  
031436 012704 003200  
031438 170104  
031440 012737 031464 001236  
031442 012700 033314  
031444 172410  
031446 012700 033314  
031448 170205  
031450 012700 033274  
031452 174010  
031454 012701 033414  
031456 012702 000004  
031458 022021  
031460 001415  
031462 012700 033274  
031464 012701 033344  
031466 012702 000004  
031468 022021  
031470 001402  
031472 000137 032524  
031474 077205  
031476 000137 032562  
031478 077220  
031480 052704 000010  
031482 020405  
031484 001402  
031486 000137 032506  
031488  
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031650

\*WITH NEGATIVE OPERANDS. EVERY COMBINATION OF  
\*OPERAND SIGNS IS TRIED.  
\*\*\*\*\*  
\*ST34: SCOPE  
\*BOTH OPERANDS NEGATIVE  
001: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
MOV #3200,R4 ;SET F10, F1V, AND FD  
LDFPS R4  
MOV #DD2,2#STMP2  
MOV #DDP1,R0 ;SET ACC OPERAND  
LDD (R0),ACC ;ESRC  
MOV #DDP1,R0 ;TEST INSTRUCTION  
ADDD (R0),ACC ;GET FPS  
STFPS R5 ;GET THE RESULT  
MOV #DDDATC,R0 ;IS IT CORRECT  
STD ACC,(R0)  
MOV #DDP9,R1  
CMP #4,R2  
BEQ DD6 ;DID A ADD-SUB  
MOV #DDDATC,R0 ;FLOW A FAILURE  
MOV #DDP4,R1  
MOV #4,R2  
CMP (R0)+,(R1)+  
BEQ DD5 ;216,442,503  
JMP #DDDER1 ;DATA ERROR.D  
SOB R2,DD4  
JMP #DDDER2 ;FLOW FAILURE.D  
SOB R2,DD3  
BIS #10,R4  
CMP R4,R5 ;FPS CORRECT?  
BEQ DD7  
JMP #DDDER0 ;BAD,FPS  
;AC POS FSRC NEG AC=-FSRC  
007: LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
MOV #3200,R4 ;SET F10, F1V, AND FD  
LDFPS R4  
MOV #DD8,2#STMP2  
MOV #DDP2,R0 / ;SET ACC OPERAND  
LDD (R0),ACC ;FSRC  
MOV #DDP1,R0 ;TEST INSTRUCTION  
ADDD (R0),ACC ;GET FPS  
STFPS R5 ;GET THE RESULT  
MOV #DDDATC,R0 ;IS IT CORRECT  
STD ACC,(R0)  
MOV #DDP0,R1  
MOV #4,R2  
CMP (R0)+,(R1)+  
BEQ DD11  
JMP #DDDER3 ;FLOW FAILURE  
SOB R2,DD10  
BIS #4,R4  
CMP R4,R5 ;FPS CORRECT?



D10

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001402
000137 032506
:AC NEG FSRC POS
DC12:
LPERR
MOV #3200,R4
LDFPS R4
MOV #DC13,2#STMP2
MOV #DDP1,R0
LDD (R0),ACD
MOV #DDP2,R0
DC13: ADD (R0),ACD
STFPS R5
MOV #DDDAT0,R0
STD ACD,(R0)
MOV #DDP0,R1
MOV #4,R2
DC14: CMP (R0)+,(R1)+
BEQ DD15
JMP #DDDER4
SOB R2,DD14
BIS #4,R4
CMP R4,R5
BEQ DD16
JMP #DDDER0
:ACD POC
DC16:
LPERR
MOV #3200,R4
LDFPS R4
MOV #DD17,2#STMP2
MOV #DDP3,R0
LDD (R0),ACD
MOV #DDP6,R0
DC17: ADD (R0),ACD
STFPS R5
MOV #DDDAT0,R0
STD ACD,(R0)
MOV #DDP7,R1
MOV #4,R2
DC18: CMP (R0)+,(R1)+
BEQ DD21
MOV #DDDAT0,R0
MOV #DDP8,R1
MOV #4,R2
DC19: CMP (R0)+,(R1)+
BEQ DD20
JMP #DDDER5
SOB R2,DD19
JMP #DDDER6
SOB R2,DD19
CMP R4,R5
BEQ DD22
JMP #DDDER0
:AC NEG FSRC
DC22:

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:BAD FPS

AC=-FSRC

:SET UP THE LOOP ON ERROR ADDRESS.

:SET FIV, FIV, AND FD

:SET ACD OPERAND

:FSRC

:TEST INSTRUCTION

:GET FPS

:GET THE RESULT

:IS IT CORRECT

:FLOW FAILURE 216,440,121

:EPS CORRECT?

:BAD FPS

AC/ : /FSRC/

:SET UP THE LOOP ON ERROR ADDRESS.

:SET FIV, FIV AND FD

:SET ACD OPERAND

:ESPC

:TEST INSTRUCTION

:GET FPS

:GET THE RESULT

:IS IT CORRECT

: FLOWS FAILURE

: 216,440,101

:GET GENERATED

:DATA ERROR.

:EPS CORRECT?

:BAD FPS

/FSRC/ : /AC/

# E10

MINCEC-11-DEPPA-A POP 11 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 121  
DEPPA.P11 01-NOV-76 21:03 T34 ADD0 WITH NEGATIVE OPRANDS TEST

Address	OpCode	Operand 1	Operand 2	Label	Instruction	Comments
6674	032134	104413			LPERR	;SET UP THE LOOP ON ERROR ADDRESS.
6675	032136	012704	003200		MOV #3200,R4	;SET FIO,FIV,AND FD
6676	032112	170104			LDFPS R4	
6677	032114	012737	032134	001236	MOV #DD23,2#STMP2	
6678	032122	012700	033364		MOV #DDP6,R0	;SET ACO OPERAND
6679	032126	172410			LDD (R0),ACO	
6680	032130	012700	033334		MOV #DDP3,R0	;FSPC
6681	032134	172010		DD23:	ADD0 (R0),ACO	;TEST INSTRUCTION
6682	032136	170205			STFPS R5	;GET FPS
6683	032140	012700	033274		MOV #DDDAT0,R0	;GET THE RESULT
6684	032144	174010			STD ACO,(R0)	
6685	032146	012701	033374		MOV #DDP7,R1	;IS IT CORRECT?
6686	032152	012702	000004		MOV #4,R2	
6687	032156	022021		DD24:	CMP (R0)+,(R1)+	
6688	032160	001415			BEQ DD27	
6689	032162	012700	033274		MOV #DDDAT0,R0	;FLO,S FAILURE
6690	032166	012701	033404		MOV #DDP8,R1	;CONSTANT (NOT 57)
6691	032172	012702	000004		MOV #4,R2	;216.042,101
6692	032176	021011		DD25:	CMP (R0),(R1)	
6693	032200	001402			BEQ DD26	
6694	032202	000137	033010		JMP 2#DDER7	;DATA ERROR.
6695	032206	077205		DD26:	SOB R2,DD25	
6696	032210	000137	033046		JMP 2#DDER8	
6697	032214	077220		DD27:	SOB R2,DD24	
6698	032216	020405			CMP R4,R5	;FPS CORRECT?
6699	032220	001402			BEQ DD30	
6700	032222	000137	032506		JMP 2#DDER0	;BAD FPS
6701				;ACO POS	FSRC NEG	;AC<<FRSRC
6702	032226			DD30:	LPERR	;SET UP THE LOOP ON ERROR ADDRESS.
6703	032226	104413			MOV #3200,R4	;SET FIO,FIV,AND FD
6704	032230	012704	003200		LDFPS R4	
6705	032234	170104			MOV #DD31,2#STMP2	
6706	032236	012737	032256	001236	MOV #DDP4,R0	;SET ACO OPERAND
6707	032244	012700	033344		LDD (R0),ACO	
6708	032250	172410			MOV #DDP5,R0	;FSPC
6709	032252	012700	033354		ADD0 (R0),ACO	;TEST INSTRUCTION
6710	032256	172010		DD31:	STFPS R5	;GET FPS
6711	032260	170205			MOV #DDDAT0,R0	;GET THE RESULT
6712	032262	012700	033274		STD ACO,(R0)	
6713	032266	174010			MOV #DDP8,R1	;IS IT CORRECT
6714	032270	012701	033404		MOV #4,R2	
6715	032274	012702	000004		CMP (R0)+,(R1)+	
6716	032300	022021		DD32:	BEQ DD35	;ADD-SUB
6717	032302	001415			MOV #DDDAT0,R0	;FLOWAS FAILURE
6718	032304	012700	033274		MOV #DDP7,R1	;CON 216 N440 NOT 141
6719	032310	012701	033374		MOV #4,R2	;GET GENERATED
6720	032314	012702	000004		CMP (R0)+,(R1)+	;FOR THE ALLIGNMENT
6721	032320	022021		DD33:	BEQ DD34	;FLOWS?
6722	032322	001402			JMP 2#DDER9	;DATA ERROR, D
6723	032324	000137	033104		SOB R2,DD33	
6724	032330	077205		DD34:	JMP 2#DDER10	
6725	032332	000137	033142		SOB R2,DD32	
6726	032336	077220		DD35:	BIS #10,R4	
6727	032340	052704	000010		CMP R4,R5	;FPS CORRECT?
6728	032344	020405			BEQ DD36	
6729	032346	001402				

# F10

Address	Instruction	Operand 1	Operand 2	Label	Comments
032350	JMP	#DDERO			:BAD FPS
032354	ACD NEG	FSRC	POS		/FSRC//AC/
032354	DC35:	LPERR			:SET UP THE LOOP ON ERROR ADDRESS.
032356	MOV	#3200,R4			:SET F10, FIV, ANC FD
032362	LDFPS	R4			
032364	MOV	#DD37,2#STMP2			
032372	MOV	#DDP5,R0			:SET ACD OPERAND
032376	LDD	(R0),ACD			
032404	MOV	#DDP4,R0			:FSPC
032404	DC37:	ADD	(R0),ACD		:TEST INSTRUCTION
032404	STFPS	R5			:GET FPS
032404	MOV	#DDATO,R0			:GET THE RESULT
032404	STD	ACD,R0			
032404	MOV	#DDP8,R1			:IS IT CORRECT
032404	MOV	#4,R2			
032426	DC38:	CMP	(R0)+,(R1)+		
032430	BEQ	DD41			
032430	MOV	#DDATO,R0			:ADD SUB
032436	MOV	#DDP7,R1			:FLOWS FAILURES
032436	MOV	#4,R2			:GET 216,042,141
032436	DC39:	CMP	(R0)+,(R1)+		:FOR THE ALLIGNMENT
032436	BEQ	DD40			:FLOWS?
032436	JMP	#DDER11			:DATA ERROR. D
032436	SOB	R2,DD39			
032436	DC40:	JMP	#DDER12		:BAD CONSTANT.D
032436	SOB	R2,DD38			
032456	BIS	#10,R4			
032472	CMP	R4,R5			:FPS CORRECT?
032474	BEQ	DD42			
032476	JMP	#DDERO			:BAD FPS
032502	DC42:	JMP	#DDDONE		
032506	DDERO:	MOV	R4,2#STMP4		:FPS ERROR
032512	MOV	R5,2#STMP3			
032516	IS:	ERROR	164		
032520	JMP	#DDDONE			
032524	DCER1:	MOV	#DDP1,2#STMP3		
032524	MOV	#DDP1,2#STMP4			
032532	MOV	#DDATO,2#STMP5			
032540	MOV	#DDP9,2#STMP6			
032546	IS:	ERROR	165		
032554	JMP	#DDDONE			
032556	DDER2:	MOV	#DDP1,2#STMP3		
032562	MOV	#DDP1,2#STMP4			
032570	MOV	#DDATO,2#STMP5			
032576	MOV	#DDP9,2#STMP6			
032604	IS:	ERROR	176		
032612	JMP	#DDDONE			
032614	DDER3:	MOV	#DDP1,2#STMP3		
032620	MOV	#DDP2,2#STMP4			
032626	MOV	#DDATO,2#STMP5			
032634	MOV	#DDP9,2#STMP6			
032642	IS:	ERROR	177		
032650	JMP	#DDDONE			

G10

6796	032652	000137	033424		JMP	2#DDDONE
6797	032656			DDER4:	MOV	#DDP2,2#STMP3
6798	032656	012737	033324	001240	MOV	#DDP1,2#STMP4
6799	032664	012737	033314	001242	MOV	#DDDAT0,2#STMP5
6790	032672	012737	033274	001244	MOV	#DDP0,2#STMP6
6791	032700	012737	033304	001246	IS:	ERROR 200
6792	032706	104200			JMP	2#DDDONE
6793	032710	000137	033424		DDER5:	MOV #DDP6,2#STMP3
6794	032714				MOV	#DDP3,2#STMP4
6795	032714	012737	033334	001240	MOV	#DDDAT0,2#STMP5
6796	032722	012737	033334	001242	MOV	#DDP7,2#STMP6
6797	032730	012737	033274	001244	IS:	ERROR 165
6798	032736	012737	033374	001246	JMP	2#DDDONE
6799	032744	104165			DDER6:	MOV #DDP6,2#STMP3
6800	032746	000137	033424		MOV	#DDP3,2#STMP4
6801	032752				MOV	#DDDAT0,2#STMP5
6802	032752	012737	033364	001240	MOV	#DDP7,2#STMP6
6803	032760	012737	033334	001242	IS:	ERROR 201
6804	032766	012737	033274	001244	JMP	2#DDDONE
6805	032774	012737	033374	001246	DDER7:	MOV #DDP3,2#STMP3
6806	033002	104201			MOV	#DDP6,2#STMP4
6807	033004	000137	033424		MOV	#DDDAT0,2#STMP5
6808	033010				MOV	#DDP7,2#STMP6
6809	033010	012737	033334	001240	IS:	ERROR 165
6810	033016	012737	033364	001242	JMP	2#DDDONE
6811	033024	012737	033274	001244	DDER8:	MOV #DDP3,2#STMP3
6812	033032	012737	033274	001244	MOV	#DDP6,2#STMP4
6813	033040	104165			MOV	#DDDAT0,2#STMP5
6814	033042	000137	033424		MOV	#DDP7,2#STMP6
6815	033046				IS:	ERROR 165
6816	033046	012737	033334	001240	JMP	2#DDDONE
6817	033054	012737	033364	001242	DDER9:	MOV #DDP5,2#STMP3
6818	033062	012737	033274	001244	MOV	#DDP4,2#STMP4
6819	033070	012737	033374	001246	MOV	#DDDAT0,2#STMP5
6820	033076	104202			MOV	#DDP8,2#STMP6
6821	033103	000137	033424		IS:	ERROR 202
6822	033104				JMP	2#DDDONE
6823	033104	012737	033354	001240	DDER10:	MOV #DDP5,2#STMP3
6824	033112	012737	033344	001242	MOV	#DDP4,2#STMP4
6825	033120	012737	033274	001244	MOV	#DDDAT0,2#STMP5
6826	033126	012737	033404	001246	MOV	#DDP8,2#STMP6
6827	033134	104165			IS:	ERROR 165
6828	033136	000137	033424		JMP	2#DDDONE
6829	033142				DDER11:	MOV #DDP4,2#STMP3
6830	033142	012737	033354	001240	MOV	#DDP5,2#STMP4
6831	033150	012737	033344	001242	MOV	#DDDAT0,2#STMP5
6832	033156	012737	033274	001244	MOV	#DDP8,2#STMP6
6833	033164	012737	033404	001246	IS:	ERROR 203
6834	033172	104203			JMP	2#DDDONE
6835	033174	000137	033424		DDER11:	MOV #DDP4,2#STMP3
6836	033200				MOV	#DDP5,2#STMP4
6837	033200	012737	033344	001240	MOV	#DDDAT0,2#STMP5
6838	033206	012737	033354	001242	MOV	#DDP8,2#STMP6
6839	033214	012737	033274	001244	IS:	ERROR 165
6840	033222	012737	033404	001246	JMP	2#DDDONE
6841	033230	104165			IS:	ERROR 165

H10

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6842 033232 000137 033424          JMP      2#DDDONE
6843 033236          DDER12: MOV     #DDP4,2#STMP3
6844 033236 012737 033344 001240          MOV     #DDP5,2#STMP4
6845 033244 012737 033354 001242          MOV     #DDDAT0,2#STMP5
6846 033252 012737 033274 001244          MOV     #DDP8,2#STMP6
6847 033260 012737 033404 001246          IS:    ERROR 204
6848 033266 104204          JMP     2#DDDONE
6849 033270 000137 033424          DDDAT0: 0
6850 033274 000000          0
6851 033276 000200          0
6852 033300 000000          0
6853 033302 000000          0
6854 033304 000000          DDP0:  0
6855 033306 000000          0
6856 033310 000000          0
6857 033312 000000          0
6858 033314 100200          DDP1: 100200      :-DDP2
6859 033316 000000          0
6860 033320 000000          0
6861 033322 000000          0
6862 033324 000200          DDP2:  200      :-DDP1
6863 033326 000000          0
6864 033330 000000          0
6865 033332 000000          0
6866 033334 001100          DDP3:  100      :EXP=4
6867 033336 000000          :FRAC=...110...
6868 033340 000000          0
6869 033342 000000          0
6870 033344 000600          DDP4:  600      :EXP=3
6871 033346 000000          :FRAC=...100...
6872 033350 000000          0
6873 033352 000000          0
6874 033354 101100          DDP5: 101100      :-DDP3
6875 033356 000000          0
6876 033360 000000          0
6877 033362 000000          0
6878 033364 100600          DDP6: 100600      :-DDP4
6879 033366 000000          0
6880 033370 000000          0
6881 033372 000000          0
6882 033374 001000          DDP7:  1000      :DDP3+DDP6
6883 033376 000000          0
6884 033400 000000          0
6885 033402 000000          0
6886 033404 101000          DDP8: 101000      :DDP5+DDP4
6887 033406 000000          0
6888 033410 000000          0
6889 033412 000000          0
6890 033414 100400          DDP9: 100400      :DDP1+DDP1
6891 033416 000000          0
6892 033420 000000          0
6893 033422 000000          0
6894 033424 000005          DDDONE: RESET
6895          :*****
6896          :*TEST 35      SUBD TEST
6897          :*
```

6898  
6899  
6900  
6901  
6902  
6903 033426 000004  
6904  
6905 033430  
6906 033430 104413  
6907 033432 012704 003200  
6908 033436 170104  
6909 033440 012737 033460 001236  
6910 033446 012700 034140  
6911 033452 172410  
6912 033454 012700 034140  
6913 033460 173010  
6914 033462 170205  
6915 033464 012700 034116  
6916 033470 174010  
6917 033472 012701 034126  
6918 033476 012702 000004  
6919 033502 022021  
6920 033504 001415  
6921 033506 012700 034116  
6922 033512 012701 034150  
6923 033516 012702 000004  
6924 033522 022021  
6925 033524 001402  
6926 033526 000137 033726  
6927 033532 077205  
6928 033534 000137 033764  
6929 033540 077220  
6930 033542 052704 000004  
6931 033546 020405  
6932 033550 001402  
6933 033552 000137 033710  
6934  
6935 033556  
6936 033556 104413  
6937 033560 012704 003200  
6938 033564 170104  
6939 033566 012737 033606 001236  
6940 033574 012700 034160  
6941 033600 172410  
6942 033602 012700 034160  
6943 033606 173010  
6944 033610 170205  
6945 033612 012700 034116  
6946 033616 174010  
6947 033620 012701 034126  
6948 033624 012702 000004  
6949 033630 022021  
6950 033632 001415  
6951 033634 012700 034116  
6952 033640 012701 034170  
6953 033644 012702 000004

```

: * THIS IS A TEST OF THE SUBD INSTRUCTION.
: * BOTH A POSITIVE AND A NEGATIVE NUMBER
: * IS SUBTRACTED FROM IT SELF
: *
: *****
+ST35: SCOPE
: USE POSITIVE OPERANDS
EE1:
      LPERR                ;SET UP THE LOOP ON ERROR ADDRESS.
      MOV      #3200,R4    ;SET F10, FIV, AND FD
      LDFPS   R4
      MOV     #EE2,2#STMP2
      MOV     #EEP1,R0     ;SET ACC OPERAND
      LDD     (R0),ACC
      MOV     #EEP1,R0     ;FSPC
      SUBD   (R0),ACC     ;TEST INSTRUCTION
      STFPS   R5          ;GET FPS
      MOV     #EEDATO,R0  ;GET THE RESULT
      STD     ACC,(R0)
      MOV     #EEO,R1     ;IS IT CORRECT?
      MOV     #4,R2
      EE3:   CMP     (R0)+,(R1)+
      BEQ     EE6
      MOV     #EEDATO,R0  ;DID A BAD
      MOV     #EEP2,R1   ;CONSTANT (NOT 57)
      MOV     #4,R2     ;GET GENERATED
      EE4:   CMP     (R0)+,(R1)+ ;FOR THE ALIGNMENT
      BEQ     EE5       ;FLOWS?
      JMP     @#EEER1    ;DATA ERROR.D
      SOB    R2,EE4
      EE5:   JMP     @#EEER2 ;BAD CONSTANT.D
      SOB    R2,EE3
      EE6:   BIS     #4,R4
      CMP     R4,R5     ;FPS CORRECT?
      BEQ     EE7
      JMP     @#EEERO    ;BAD FPS
      ;USE NEGATIVE OPERANDS
EE7:
      LPERR                ;SET UP THE LOOP ON ERROR ADDRESS.
      MOV     #3200,R4    ;SET F10, FIV, AND FD
      LDFPS   R4
      MOV     #EE8,2#STMP2
      MOV     #EEP3,R0     ;SET ACC OPERAND
      LDD     (R0),ACC
      MOV     #EEP3,R0     ;FSPC
      SUBD   (R0),ACC     ;TEST INSTRUCTION
      STFPS   R5          ;GET FPS
      MOV     #EEDATO,R0  ;GET THE RESULT
      STD     ACC,(R0)
      MOV     #EEO,R1     ;IS IT CORRECT?
      MOV     #4,R2
      EE9:   CMP     (R0)+,(R1)+
      BEQ     EE12
      MOV     #EEDATO,R0  ;DID A BAD
      MOV     #EEP4,R1   ;CONSTANT (NOT 57)
      MOV     #4,R2     ;GET GENERATED

```

```

6954 033650 022021 EE10: CMP (R0)+,(R1)+ ;FOR THE ALLIGNMENT
6955 033652 021402 BEQ EE11 ;FLOWS?
6956 033654 000137 034022 JMP @EEER3 ;DATA ERROR.D
6957 033660 077205 EE11: SOB R2,EE10
6958 033662 000137 034060 JMP @EEER4 ;BAD CONSTANT.D
6959 033666 077220 EE12: SOB R2,EE9
6960 033670 052704 000004 BVS #4,R4
6961 033674 020405 CMP R4,R5 ;FPS CORRECT?
6962 033676 001402 BEQ EE13
6963 033700 000137 033710 JMP @EEER0 ;BAD FPS.
6964 033704 000137 034200 EE13: JMP @EEDONE
6965 033710 010437 001242 EEER0: MOV R4,@STMP4 ;BAD FPS
6966 033714 010537 001240 MOV R5,@STMP3
6967 033720 104205 IS: ERROR 205
6968 033722 000137 034200 JMP @EEDONE
6969 033726
6970 033726 012737 034140 001240 MOV @EEP1,@STMP3
6971 033734 012737 034140 001242 MOV @EEP1,@STMP4
6972 033742 012737 034116 001244 MOV @EEDATO,@STMP5
6973 033750 012737 034126 001246 MOV @EEPO,@STMP6
6974 033756 104206 IS: ERROR 206
6975 033760 000137 034200 JMP @EEDONE
6976 033764
6977 033764 012737 034140 001240 MOV @EEP1,@STMP3
6978 033772 012737 034140 001242 MOV @EEP1,@STMP4
6979 034000 012737 034116 001244 MOV @EEDATO,@STMP5
6980 034006 012737 034126 001246 MOV @EEPO,@STMP6
6981 034014 104207 IS: ERROR 207
6982 034016 000137 034200 JMP @EEDONE
6983 034022
6984 034022 012737 034160 001240 MOV @EEP3,@STMP3
6985 034030 012737 034160 001242 MOV @EEP3,@STMP4
6986 034036 012737 034116 001244 MOV @EEDATO,@STMP5
6987 034044 012737 034126 001246 MOV @EEPO,@STMP6
6988 034052 104206 IS: ERROR 206
6989 034054 000137 034200 JMP @EEDONE
6990 034060
6991 034060 012737 034160 001240 MOV @EEP3,@STMP3
6992 034066 012737 034160 001242 MOV @EEP3,@STMP4
6993 034074 012737 034116 001244 MOV @EEDATO,@STMP5
6994 034102 012737 034126 001246 MOV @EEPO,@STMP6
6995 034110 104207 IS: ERROR 207
6996 034112 000137 034200 JMP @EEDONE
6997 034116 000000 EECATO: 0
6998 034120 000000 0
6999 034122 000000 0
7000 034124 000000 0
7001 034126 000000 EEPO: 0
7002 034130 000000 0
7003 034132 000000 00000
7004 034134 000000 0
7005 034136 000000 0
7006 034140 000200 EEP1: 200
7007 034142 000000 0
7008 034144 000000 0
7009 034146 000000 0

```

# K10

MAINDEC-11-DFPRA-A POP 11 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 127  
 DFFPRA.F11 01-NOV-76 21:03 T35 SUBC TEST

7010	034150	000400	
7011	034152	000000	
7012	034154	000000	
7013	034156	000000	
7014	034160	100200	
7015	034162	000000	
7016	034164	000000	
7017	034166	000000	
7018	034170	100400	
7019	034172	000000	
7020	034174	000000	
7021	034176	000000	
7022	034200		
7023	034200	104412	
7024			
7025			
7026			
7027			
7028			
7029			
7030			
7031			
7032			
7033			
7034			
7035			
7036			
7037			
7038	034202	000004	
7039			
7040	034204		
7041	034204	104413	
7042	034206	012704	003200
7043	034212	170104	
7044	034214	012737	034234 001236
7045	034222	012700	034526
7046	034226	172410	
7047	034230	012700	034536
7048	034234	172010	
7049	034236	170205	
7050	034240	012700	034476
7051	034244	174010	
7052	034246	012701	034546
7053	034252	012702	000004
7054	034256	022021	
7055	034260	001401	
7056	034262	000466	
7057	034264	077204	
7058	034266	020405	
7059	034270	001401	
7060	034272	000435	
7061			
7062			
7063	034274		
7064	034274	104413	
7065	034276	012704	003200

EEP2: 400  
 0  
 0  
 0  
 EEP3: 100200  
 0  
 0  
 0  
 0  
 EEP4: 100400  
 0  
 0  
 0  
 EEDONE:  
 RSETJP

:GO INITIALIZE THE FPS AND STACK; AND  
 :SEE IF THE USER HAS EXPRESSED  
 :THE DESIRE TO CHANGE THE SOFTWARE  
 :VIRTUAL CONSOLE SWITCH REGISTER (HAS  
 :THE USER TYPED CONTROL G?).

:\*\*\*\*\*  
 :\*TEST 36 NORMALIZE ALGORITHM TEST  
 :\*  
 :\* THIS IS A TEST OF THE NORMALIZE  
 :\* FLOW ALGORITHM. TWO PATTERNS ARE USED,  
 :\* FIRST THE MINIMUM SITUATION REQUIRING ONE  
 :\* LEFT SHIFT AND THEN THE MAXIMUM SITUATION  
 :\* REQUIRING 56 SHIFTS.  
 :\*

:\*\*\*\*\*  
 :\*ST36: SCOPE  
 :USE DATA PATTERNS THAT REQUIRE ONLY ONE LEFT SHIFT TO NORMALIZE  
 FF1:  
 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
 MOV #3200,R4 ;SET F10, F1V, AND FD  
 LDFPS R4  
 MOV #FF2, @#STMP2  
 MOV #FFP2, RO ;SET ACO OPERAND  
 LDD (RO), ACO  
 MOV #FFP3, RO ;FSPC  
 ADD (RO), ACO ;TEST INSTRUCTION  
 STFPS R5 ;GET FPS  
 MOV #FFDAT0, RO ;GET THE RESULT  
 STD ACO, (RO)  
 MOV #FFP4, R1 ;IS IT CORRECT  
 MOV #4, R2  
 FF3: CMP (R0)+, (R1)+  
 BEQ FF4  
 BR FFER2 ;BAD DATA  
 FF4: SOB R2, FF3  
 CMP R4, R5 ;FPS CORRECT?  
 BEQ FF5  
 BR FFER0 ;BAD FPS  
 :USE DATA PATTERNS WHICH REQUIRE 56 LEFT SHIFTS TO NORMALIZE  
 :THE RESULT  
 FF5:  
 LPERR ;SET UP THE LOOP ON ERROR ADDRESS.  
 MOV #3200,R4 ;SET F1U, F1V, AND FD



# L10

7066	034302	170104			LDFPS	R4	
7067	034304	012737	034324	001236	MOV	#FF6, 2*STMP2	
7068	034312	012700	034506		MOV	#FFP0, R0	;SET ACO OPERANC
7069	034316	172410			LDD	(R0), ACO	
7070	034320	012700	034516		MOV	#FFP1, R0	;FSRC
7071	034324	172010			FF6: ADDD	(R0), ACO	;TEST INSTRUCTION
7072	034326	170205			STFPS	R5	;GET FPS
7073	034330	012700	034476		MOV	#FFDAT0, R0	;GET THE RESULT
7074	034334	174010			STD	ACO, (R0)	
7075	034336	012701	034546		MOV	#FFP4, R1	;IS IT CORRECT
7076	034342	012702	000004		MOV	#4, R2	
7077	034346	022021			FF7: CMP	(R0)+, (R1)+	
7078	034350	001401			BEQ	FF10	
7079	034352	000413			BR	FFER1	;BATA
7080	034354	077204			FF10: SCB	R2, FF7	
7081	034356	020405			CMP	R4, R5	;FPS CORRECT
7082	034360	001401			BEG	FF11	
7083	034362	000401			BR	FFER0	;BAD FPS
7084	034364	000474			FF11: BR	FFDONE	
7085							
7086	034366	010537	001240		FFER0: MOV	R5, 2*STMP3	
7087	034372	010437	001242		MOV	R4, 2*STMP4	
7088	034376	104164			1\$: ERROR	164	
7089	034400	000466			BR	FFDONE	
7090							
7091	034402				FFER1:		
7092	034402	012737	034516	001240	MOV	#FFP1, 2*STMP3	
7093	034410	012737	034506	001242	MOV	#FFP0, 2*STMP4	
7094	034416	012737	034476	001244	MOV	#FFDAT0, 2*STMP5	
7095	034424	012737	034546	001246	MOV	#FFP4, 2*STMP6	
7096	034432	104210			1\$: ERROR	210	
7097	034434	000137	034556		JMP	2*FFDONE	
7098							
7099	034440				FFER2:		
7100	034440	012737	034536	001240	MOV	#FFP3, 2*STMP3	
7101	034446	012737	03452E	001242	MOV	#FFP2, 2*STMP4	
7102	034454	012737	034476	001244	MOV	#FFDAT0, 2*STMP5	
7103	034462	012737	034546	001246	MOV	#FFP4, 2*STMP6	
7104	034470	104210			1\$: ERROR	210	
7105	034472	000137	034556		JMP	2*FFDONE	
7106							
7107							
7109	034476	000000			FFDAT0:	0	
7109	034500	000000				0	
7110	034502	000000				0	
7111	034504	000000				0	
7112							
7113	034506	016000			FFP0:	16000	
7114	034510	000000				0	
7115	034512	000000				0	
7116	034514	000001				1	
7117	034516	116000			FFP1:	116000	
7118	034520	000000				0	
7119	034522	000000				0	
7120	034524	000000				0	
7121	034526	000500			FFP2:	500	

M10

7122 034530 000000  
 7123 034532 000000  
 7124 034534 000000  
 7125 034536 100400  
 7126 034540 000000  
 7127 034542 000000  
 7128 034544 000000  
 7129 034546 000200  
 7130 034550 000000  
 7131 034552 000000  
 7132 034554 000000  
 7133  
 7134 034556  
 7135  
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 7137 034556  
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 7148  
 7149  
 7150 034556  
 7151 034556 000004  
 7152 034560 005067 144316  
 7153 034564 005067 144512  
 7154 034570 005267 144530  
 7155 034574 042767 100000 144522  
 7156 034602 005327  
 7157 034604 000301  
 7158 034606 003074  
 7159 034610 012737  
 7160 034612 000001  
 7161 034614 034604  
 7162 034616 104401 034624  
 7163 034622 000407  
 7164  
 7165 034642  
 7166 034642 016746 144456  
 7167  
 7168 034646 104403  
 7169 034650 006  
 7170 034651 000  
 7171 034652 104401 034660  
 7172 034656 000421  
 7173  
 7174 034722  
 7175 034722 016746 144164  
 7176  
 7177 034726 104403

```

0
00
00
FFP2: 100400
0
00
00
FFP4: 200 ;FFP4=FFP0+FFP1
; =FFP3+FFP4
00
00
FFDONE:
TST37:

.SBTTL END OF PASS ROUTINE
;*****
; INCREMENT THE PASS NUMBER ($PASS)
; INDICATE END-OF-PROGRAM AFTER 1 PASSES THRU THE PROGRAM
; IF SW12=1 INHIBIT TRACE TRAP
; IF THERES A MONITOR GO TO IT
; IF THERE ISN'T JUMP TO LOOP
SEOP:
SCOPE
CLR $TSTNM ;: ZERO THE TEST NUMBER
CLR $TIMES ;: ZERO THE NUMBER OF ITERATIONS
INC $PASS ;: INCREMENT THE PASS NUMBER
BIC #100000,$PASS ;: DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ;: LOOP?
SEOPCT: .WORD 1
BGT $DOAGN ;: YES
MOV (PC)+,2(PC)+ ;: RESTORE COUNTER
SENDCT: .WORD 1
TYPE ,65$ ;: TYPE ASCIZ STRING
BR ,64$ ;: GET OVER THE ASCIZ
;:65$: .ASCIZ <12><15>/END PASS #/
64$:
MOV $PASS,-(SP) ;: SAVE $PASS FOR TYPEOUT
;: TYPE PASS NUMBER IN OCTAL
TYPOS ;: GO TYPE--OCTAL ASCII
.BYTE 6 ;: TYPE 6 DIGITS
.BYTE 0 ;: SUPPRESS LEADING ZEROS
TYPE ,67$ ;: TYPE ASCIZ STRING
BR ,66$ ;: GET OVER THE ASCIZ
;:67$: .ASCIZ / TOTAL ERRORS SINCE LAST REPORT /
66$:
MOV $ERTTL,-(SP) ;: SAVE $ERTTL FOR TYPEOUT
;: TOTAL NUMBER OF ERRORS IN OCTAL
;: GO TYPE--OCTAL ASCII
TYPOS

```

N10

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7178 034730 006 .BYTE 6 ;;TYPE 6 DIGITS
7179 034731 000 .BYTE 0 ;;SUPPRESS LEADING ZEROS
7180 034732 104401 001313 TYPE SCRLF ;;TYPE CARRIAGE RETURN, LINE FEED
7181 034736 005067 144150 CLR $ERTL ;;CLEAR ERROR TOTAL
7182 034742 013700 000042 $GET42: MOV 2042,RO ;;GET MONITOR ADDRESS
7183 034746 001414 BEQ $DOAGN ;;BRANCH IF NO MONITOR
7184 034750 005046 CLR -(SP) ;;INSURE THE "T" BIT IS CLEAR
7185 034752 012746 034760 MOV $CLR.T,-(SP) ;;SETUP FOR AN RTI OR RTT
7186 034756 000426 BR $RTRN ;;GO DO AN RTI OR RTT TO LOAD THE PSW
7187 ;;WITH A CLEARED "T" BIT
7188 034760 $CLR.T: MOV 2042,RO ;;INSURE RO CONTAINS THE MONITORS
7189 034760 013700 000042 BEQ $DOAGN ;;RETURN ADDRESS
7190 034764 001405 RESET ;;CLEAR THE WORLD
7191 034766 000005 $ENDAD: JSR PC,(RO) ;;GO TO MONITOR
7192 034770 004710 NOP ;;SAVE ROOM
7193 034772 000240 NOP ;;FOR
7194 034774 000240 NOP ;;ACT11
7195 034776 000240 $DOAGN: TRAP ;;PUSH OLD PSW AND PC ON STACK
7196 035000 104400 BIC #20,SP) ;;CLEAR THE "T" BIT
7197 035000 042716 000020 BIT #BIT12,$SWR ;;RUN WITH TRACE TRAP
7198 035002 032777 010000 144124 BNE IS ;;BR IF NO
7199 035006 032777 010000 144124 COM $TBIT ;;IS IT TIME FOR TRACE TRAP
7200 035014 001005 IS ;;BR IF NO
7201 035016 005167 000020 BIS #20,(SP) ;;SET TRACE TRAP
7202 035022 100402 IS: MOV #SLOOP,-(SP) ;;JUMP TO START OF TEST
7203 035024 052716 000020 $RTRN: RTI ;;RETURN--THIS IS CHANGED TO
7204 035030 012746 035036 ;;AN "RTT" IF "RTT" IS A LEGAL
7205 035034 000002 ;;INSTRUCTION
7206
7207
7208 035036 $SLOOP: JMP 2(PC)+ ;;RETURN
7209 035036 000137 $RTNAD: .WORD LOOP
7210 035040 004304 $TBIT: .WORD 0 ;;;"T" BIT STATE INDICATOR
7211 035042 000000 $ENULL: .BYTE -1,-1,0 ;;NULL CHARACTER STRING
7212 035044 377 377 000 .EVEN
7213 035050
7214
7215 .SBTTL SCOPE HANDLER ROUTINE
7216
7217 ;;*****
7218 ;;*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
7219 ;;*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
7220 ;;*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
7221 ;;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
7222 ;;*SW14=1 LOOP ON TEST
7223 ;;*SW11=1 INHIBIT ITERATIONS
7224 ;;*SW09=1 LOOP ON ERROR
7225 ;;*SW08=1 LOOP ON TEST IN SWR<7:0>
7226 ;;*CALL
7227 ;;* SCOPE ;;SCOPE=IOT
7228
7229 035050 $SCOPE: CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
7230 035050 104406 040000 144060 IS: BIT #BIT14,$SWR ;;LOOP ON PRESENT TEST?
7231 035052 032777 BNE $OVER ;;YES IF SW14=1
7232 035060 001114 ;;*****START OF CODE FOR THE XOR TESTER*****
7233

```

```

7270 035062 000416          SXTSTR: BR      65      :: IF RUNNING ON THE "XOR" TESTER CHANGE
7271 035064 012746 000004          MOV      2@ERRVEC, -(SP)  :: THIS INSTRUCTION TO A "NOP" (NOP=240)
7272 035070 012737 035110 000004          MOV      255, 2@ERRVEC  :: SAVE THE CONTENTS OF THE ERROR VECTOR
7273 035076 005737 177060          TST      2@177060      :: SET FOR TIMEOUT
7274 035082 012637 000004          MOV      (SP)+, 2@ERRVEC  :: TIME OUT ON XOR?
7275 035106 000463          BR       $SVLAD        :: RESTORE THE ERROR VECTOR
7276 035110 022626          BR       $SVLAD        :: GO TO THE NEXT TEST
7277 035114 012637 000004          SS:      CMP      (SP)+, (SP)+  :: CLEAR THE STACK AFTER A TIME OUT
7278 035118 012637 000004          MOV      (SP)+, 2@ERRVEC  :: RESTORE THE ERROR VECTOR
7279 035120 000423          BR       75           :: LOOP ON THE PRESENT TEST
7280 035120 032777 000400 144012          65:      :: ***** END OF CODE FOR THE XOR TESTER *****
7281 035122 001424          BIT      @BIT08, 2$WR    :: LOOP ON SPEC. TEST?
7282 035126 127767 144004 143744          BEQ     25           :: BR IF NO
7283 035130 001465          CMPB    2$WR, $STSTM    :: ON THE RIGHT TEST?   SWR(7:0)
7284 035136 105767 143737          BEQ     $OVER         :: BR IF YES
7285 035140 001421          TSTB    $ERFLG        :: HAS AN ERROR OCCURRED?
7286 035144 126767 143743 143727          BEQ     35           :: BR IF NO
7287 035154 101015          CMPB    $ERMAX, $ERFLG  :: MAX. ERRORS FOR THIS TEST OCCURRED?
7288 035156 032777 001000 143754          BEQ     35           :: BR IF NO
7289 035164 001424          BIT      @BIT09, 2$WR    :: LOOP ON ERROR?
7290 035166 016767 143716 143712          75:      MOV      $LPERR, $LPADR  :: SET LOOP ADDRESS TO LAST SCOPE
7291 035174 000446          BR       $OVER         ::
7292 035176 105267 143701          45:      CLRB    $ERFLG        :: ZERO THE ERROR FLAG
7293 035202 005067 144074          CLR     $TIMES        :: CLEAR THE NUMBER OF ITERATIONS TO MAKE
7294 035206 000415          BR       15           :: ESCAPE TO THE NEXT TEST
7295 035210 032777 004000 143722          35:      BIT      @BIT11, 2$WR  :: INHIBIT ITERATIONS?
7296 035216 001011          BVE     15           :: BR IF YES
7297 035220 005767 144100          TST     $PASS        :: IF FIRST PASS OF PROGRAM
7298 035224 001406          BEQ     15           :: INHIBIT ITERATIONS
7299 035226 005267 143652          INC     $ICNT        :: INCREMENT ITERATION COUNT
7300 035232 026767 144044 143644          CMP     $TIMES, $ICNT  :: CHECK THE NUMBER OF ITERATIONS MADE
7301 035240 002024          BGE     $OVER        :: BR IF MORE ITERATION REQUIRED
7302 035242 012767 000001 143634          15:      MOV      @1, $ICNT     :: REINITIALIZE THE ITERATION COUNTER
7303 035250 016767 000052 144024          MOV     $MXCNT, $TIMES  :: SET NUMBER OF ITERATIONS TO DO
7304 035256 105267 143620          $SVLAD: INCB    $STSTM   :: COUNT TEST NUMBERS
7305 035262 116767 143614 144032          MOVB   $STSTM, $TESTN  :: SET TEST NUMBER IN APT MAILBOX
7306 035270 011667 143612          MOV     (SP), $LPADR   :: SAVE SCOPE LOOP ADDRESS
7307 035274 011667 143610          MOV     (SP), $LPERR   :: SAVE ERROR LOOP ADDRESS
7308 035300 005067 144000          CLR     $ESCAPE      :: CLEAR THE ESCAPE FROM ERROR ADDRESS
7309 035304 112767 000001 143603          MOVB   @1, $ERMAX     :: ONLY ALLOW ONE(1) ERROR ON NEXT TEST
7310 035312 016777 143564 143622          $OVER:  MOV     $STSTM, 2$DISPLAY  :: DISPLAY TEST NUMBER
7311 035320 016716 143562          MOV     $LPADR, (SP)  :: FUDGE RETURN ADDRESS
7312 035324 000002          RTI                    :: FIXES PS
7313 035326 000001          $MXCNT: 1             :: MAX. NUMBER OF ITERATIONS

```

.SBTTL ERROR HANDLER ROUTINE

```

7280 *****
7281 *THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT.
7282 *SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
7283 *AND GO TO ERTYPE ON ERROR
7284 *THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
7285 *SW15=1      HALT ON ERROR
7286 *SW13=1      INHIBIT ERROR TYPEOUTS
7287 *SW10=1      BELL ON ERROR

```

```

7290          *SW09=1          LOOP ON ERROR
7291          *CALL
7292          *          ERROR N          ;;ERROR=EMT AND N=ERROR ITEM NUMBER
7293
7294          ERROR:
7295          035330          104406          CKSWR          ;; TEST FOR CHANGE IN SOFT-SWP
7296          035332          105267          143545          75:          INCB          SERFLG          ;; SET THE ERROR FLAG
7297          035336          001775          BEQ          75          ;; DON'T LET THE FLAG GO TO ZERO
7298          035340          016777          143536          143574          MOV          $STN,02:SPRAY          ;; DISPLAY TEST NUMBER AND ERROR FLAG
7299          035346          032777          002000          143564          BIT          #BIT10,2SWR          ;; BELL ON ERROR?
7300          035354          001402          BEQ          15          ;; NO - SKIP
7301          035356          104401          001306          TYPE          SBELL          ;; RING BELL
7302          035362          005267          143524          15:          INC          $ERTTL          ;; COUNT THE NUMBER OF ERRORS
7303          035366          011667          143524          MOV          (SP),SERAPC          ;; GET ADDRESS OF ERROR INSTRUCTION
7304          035372          162767          000002          143516          SUB          #2,SERAPC
7305          035400          117767          143512          143506          MOVB          2SERAPC,$ITEMB          ;; STRIP AND SAVE THE ERROR ITEM CODE
7306          035406          032777          020000          143524          BIT          #BIT13,2SWR          ;; SKIP TYPEOUT IF SET
7307          035414          051004          BNE          20$          ;; SKIP TYPEOUTS
7308          035416          004767          002124          JSR          PC,ERTYPE          ;; GO TO USER ERROR ROUTINE
7309          035422          104401          001313          TYPE          ,SCLF
7310
7311          035426          122767          000001          143702          20$:          CMPB          #APTENV,$ENV          ;; RUNNING IN APT MODE
7312          035434          001007          BNE          25          ;; NO SKIP APT ERROR REPORT
7313          035436          116767          143452          000004          MOVB          $ITEMB,21$          ;; SET ITEM NUMBER AS ERROR NUMBER
7314          035444          004767          000740          JSR          PC,SATY4          ;; REPORT FATAL ERROR TO APT
7315          035450          000          21$:          .BYTE          0
7316          035451          000          .BYTE          0
7317          035452          000777          22$:          BR          22$          ;; APT ERROR LOOP
7318          035454          005777          143460          25:          TST          2SWR          ;; HALT ON ERROR
7319          035460          100002          BPL          35          ;; SKIP IF CONTINUE
7320          035462          000000          HALT          ;; HALT ON ERROR!
7321          035464          104406          CKSWR          ;; TEST FOR CHANGE IN SOFT-SWR
7322          035466          032777          001000          143444          35:          BIT          #BIT09,2SWR          ;; LOOP ON ERROR SWITCH SET?
7323          035474          001402          BEQ          45          ;; BR IF NO
7324          035476          016716          143406          MOV          $LPERR,(SP)          ;; FUDGE RETURN FOR LOOPING
7325          035502          005767          143576          45:          TST          $ESCAPE          ;; CHECK FOR AN ESCAPE ADDRESS
7326          035506          001402          BEQ          55          ;; BR IF NONE
7327          035510          016716          143570          MOV          $ESCAPE,(SP)          ;; FUDGE RETURN ADDRESS FOR ESCAPE
7328          035514
7329          035514          022737          034770          000042          55:          CMP          #SENDAC,2#42          ;; ACT-11 AUTO-ACCEPT?
7330          035522          001001          BNE          65          ;; BRANCH IF NO
7331          035524          000000          HALT          ;; YES
7332          035526
7333          035526          032777          001000          143404          65:          BIT          #BIT09,2SWR
7334          035534          001013          BNE          ERM10          ;;
7335          035536          011637          001162          MOV          (SP),2#$REG0          ;; SEE IF ERROR #377
7336          035542          062737          177776          001162          ADD          #-2,2#$REG0
7337          035550          122777          000377          143404          CMPB          #377,2$REG0
7338          035556          001002          BNE          ERM10          ;;
7339          035560          062716          000002          ADD          #2,(SP)
7340          035564          000002          ERM10:          RTI
7341
7342          .SBTTL          SAVE AND RESTORE R0-R5 ROUTINES
7343
7344          ;;*****
7345

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035566  
035566 010046  
035570 010146  
035572 010246  
035574 010346  
035576 010446  
035600 010546  
035602 016646 000022  
035606 016646 000022  
035612 016646 000022  
035616 016646 000022  
035622 000002  
  
035624  
035624 012666 000022  
035630 012666 000022  
035634 012666 000022  
035640 012666 000022  
035644 012605  
035646 012604  
035650 012603  
035652 012602  
035654 012601  
035656 012600  
035660 000002

```

: *SAVE R0-R5
: *CALL:
: * SAVREG
: *UPON RETURN FROM $SAVREG THE STACK WILL LOOK LIKE:
: *
: *TOP---(+16)
: * +2---(+18)
: * +4---R5
: * +6---R4
: * +8---R3
: *+10---R2
: *+12---R1
: *+14---R0

```

```

$SAVREG:
MOV R0,-(SP) ;; PUSH R0 ON STACK
MOV R1,-(SP) ;; PUSH R1 ON STACK
MOV R2,-(SP) ;; PUSH R2 ON STACK
MOV R3,-(SP) ;; PUSH R3 ON STACK
MOV R4,-(SP) ;; PUSH R4 ON STACK
MOV R5,-(SP) ;; PUSH R5 ON STACK
MOV 22(SP),-(SP) ;; SAVE PS OF MAIN FLOW
MOV 22(SP),-(SP) ;; SAVE PC OF MAIN FLOW
MOV 22(SP),-(SP) ;; SAVE PS OF CALL
MOV 22(SP),-(SP) ;; SAVE PC OF CALL
RTI

```

```

: *RESTORE R0-R5
: *CALL:
: * RESREG
$RESREG:
MOV (SP)+,22(SP) ;; RESTORE PC OF CALL
MOV (SP)+,22(SP) ;; RESTORE PS OF CALL
MOV (SP)+,22(SP) ;; RESTORE PC OF MAIN FLOW
MOV (SP)+,22(SP) ;; RESTORE PS OF MAIN FLOW
MOV (SP)+,R5 ;; POP STACK INTO R5
MOV (SP)+,R4 ;; POP STACK INTO R4
MOV (SP)+,R3 ;; POP STACK INTO R3
MOV (SP)+,R2 ;; POP STACK INTO R2
MOV (SP)+,R1 ;; POP STACK INTO R1
MOV (SP)+,R0 ;; POP STACK INTO R0
RTI

```

```

.SBTTL TYPE ROUTINE
:
: *****
: *ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
: *THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
: *NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
: *NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
: *NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
: *
: *CALL:
: *1) USING A TRAP INSTRUCTION
: * TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
: *OR

```

7406				::*	TYPE				
7407				::*	MESADR				
7408				::*					
7409	035662	105767	143271	STYPE:	TSTB	\$TFELG	:::	IS THERE A TERMINAL?	
7410	035666	100002			BPL	1\$	:::	BR IF YES	
7411	035670	000000			HALT		:::	HALT HERE IF NO TERMINAL	
7412	035672	000430			BR	3\$	:::	LEAVE	
7413	035674	010046		1\$:	MOV	RO, -(SP)	:::	SAVE RO	
7414	035676	017500	000002		MOV	#2(SP), RO	:::	GET ADDRESS OF ASCIZ STRING	
7415	035702	122767	000001		CMPB	#APTEMV, \$EMV	:::	RUNNING IN APT MODE	
7416	035710	031011			BNE	62\$	:::	NO, GO CHECK FOR APT CONSOLE	
7417	035712	132767	000100		BITB	#APTSPOOL, \$ENVM	:::	SPOOL MESSAGE TO APT	
7418	035720	001405			BEQ	62\$	:::	NO, GO CHECK FOR CONSOLE	
7419	035722	010067	000034		MOV	RO, 61\$	:::	SETUP MESSAGE ADDRESS FOR APT	
7420	035724	004767	000446		JSR	PC, \$ATY3	:::	SPOOL MESSAGE TO APT	
7421	035732	000000		61\$:	.WORD	0	:::	MESSAGE ADDRESS	
7422	035734	132767	000040	62\$:	BITB	#APTCSUP, \$ENVM	:::	APT CONSOLE SUPPRESSED	
7423	035742	001003			BNE	60\$	:::	YES, SKIP TYPE OUT	
7424	035744	112046		2\$:	MOVB	RO)+, -(SP)	:::	PUSH CHARACTER TO BE TYPED ONTO STACK	
7425	035746	001005			BNE	4\$	:::	BR IF IT ISN'T THE TERMINATOR	
7426	035750	005726			TST	(SP)+	:::	IF TERMINATOR POP IT OFF THE STACK	
7427	035752	012600		60\$:	MOV	(SP)+, RO	:::	RESTORE RO	
7428	035754	062716	000002	3\$:	ADD	#2, (SP)	:::	ADJUST RETURN PC	
7429	035760	000002			RTI		:::	RETURN	
7430	035762	122716	000011	4\$:	CMPB	#HT, (SP)	:::	BRANCH IF <HT>	
7431	035766	001430			BEQ	8\$	:::	BRANCH IF NOT <CRLF>	
7432	035770	122716	000200		CMPB	#CRLF, (SP)	:::		
7433	035774	001006			BNE	5\$	:::	POP <CR><LF> EQUIV	
7434	035776	005726			TST	(SP)+	:::	TYPE A CR AND LF	
7435	036000	104401			TYPE		:::		
7436	036002	001313			\$CRLF		:::		
7437	036004	105067	000130		CLRB	\$CHARCNT	:::	CLEAR CHARACTER COUNT	
7438	036010	000755			BR	2\$	:::	GET NEXT CHARACTER	
7439	036012	004767	000056	5\$:	JSR	PC, \$TYPEC	:::	GO TYPE THIS CHARACTER	
7440	036016	126726	143134	6\$:	CMPB	\$FILLC, (SP)+	:::	IS IT TIME FOR FILLER CHARS.?	
7441	036022	001350			BNE	2\$	:::	IF NO GO GET NEXT CHAR.	
7442	036024	016746	143124		MOV	\$NULL, -(SP)	:::	GET # OF FILLER CHARS. NEEDED	
7443							:::	AND THE NULL CHAR.	
7444	036030	105366	000001	7\$:	DECB	1(SP)	:::	DOES A NULL NEED TO BE TYPED?	
7445	036034	002770			BLT	6\$	:::	BR IF NO--GO POP THE NULL OFF OF STACK	
7446	036036	004767	000032		JSR	PC, \$TYPEC	:::	GO TYPE A NULL	
7447	036042	105367	000072		DECB	\$CHARCNT	:::	DO NOT COUNT AS A COUNT	
7448	036046	000770			BR	7\$	:::	LOOP	
7449					;HORIZONTAL TAB PROCESSOR				
7450	036050	112716	000040	8\$:	MOVB	#' (SP)	:::	REPLACE TAB WITH SPACE	
7451	036054	004767	000014	9\$:	JSR	PC, \$TYPEC	:::	TYPE A SPACE	
7452	036060	132767	000007		BITB	#7, \$CHARCNT	:::	BRANCH IF NOT AT	
7453	036066	001372			BNE	9\$	:::	TAB STOP	
7454	036070	005726			TST	(SP)+	:::	POP SPACE OFF STACK	
7455	036072	000724			BR	2\$	:::	GET NEXT CHARACTER	
7456	036074	105777	143050	STYPEC:	TSTB	\$STPS	:::	WAIT UNTIL PRINTER IS READY	
7457	036100	100375			BPL	\$TYPEC	:::		
7458	036102	116677	000002		MOVB	2 SP, 3\$TPB	:::	LOAD CHAR TO BE TYPED INTO DATA REG.	

```

7458 036110 122766 000015 000002      CMPB    #CR,2(SP)      ;; IS CHARACTER A CARRIAGE RETURN?
7459 036116 001003      BNE     IS            ;; BRANCH IF NO
7460 036120 105067 000014      CLR    $CHARCNT      ;; YES--CLEAR CHARACTER COUNT
7461 036124 000406      BR     $TYPEX        ;; EXIT
7462 036126 122766 000012 000002  IS:    CMPB    #LF,2(SP)      ;; IS CHARACTER A LINE FEED?
7463 036134 001402      BEQ    $TYPEX        ;; BRANCH IF YES
7464 036136 105227      INCB   (PC)+         ;; COUNT THE CHARACTER
7465 036140 000000      $CHARCNT: .WORD    0 ;; CHARACTER COUNT STORAGE
7466 036142 000207      $TYPEX:  RTS         PC

```

.SETTL BINARY TO OCTAL (ASCII) AND TYPE

```

*****
* THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
* OCTAL (ASCII) NUMBER AND TYPE IT.
* $TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
* CALL:

```

```

*      MOV     NUM,-(SP)      ;; NUMBER TO BE TYPED
*      TYPOS   ;; CALL FOR TYPEOUT
*      .BYTE  N              ;; N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*      .BYTE  M              ;; M=1 OR 0
*                               ;; 1=TYPE LEADING ZEROS
*                               ;; 0=SUPPRESS LEADING ZEROS

```

```

* $TYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
* $TYPOS OR $TYPOC

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```

* CALL:
*      MOV     NUM,-(SP)      ;; NUMBER TO BE TYPED
*      TYPON   ;; CALL FOR TYPEOUT

```

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* $TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER

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* CALL:
*      MOV     NUM,-(SP)      ;; NUMBER TO BE TYPED
*      TYPOC   ;; CALL FOR TYPEOUT

```

```

7494 036144 017646 000000      $TYPOS: MOV     #2(SP),-(SP)  ;; PICKUP THE MODE
7495 036150 016667 000001 000211  MCVB   1(SP), $OFILL    ;; LOAD ZERO FILL SWITCH
7496 036156 112667 000207      MCVB   (SP)+, $CMODE+1  ;; NUMBER OF DIGITS TO TYPE
7497 036162 062716 000002      ADD    #2, (SP)        ;; ADJUST RETURN ADDRESS
7498 036166 000406      BR     $TYPON
7499 036170 112767 000001 000171  $TYPOC: MOVB   #1, $OFILL  ;; SET THE ZERO FILL SWITCH
7500 036176 112767 000006 000165      MOVB   #6, $OMODE+1    ;; SET FOR SIX(6) DIGITS
7501 036204 112767 000005 000154  $TYPON: MCVB   #5, $OCNT  ;; SET THE ITERATION COUNT
7502 036212 010346      MOV    R3,-(SP)       ;; SAVE R3
7503 036214 010446      MOV    R4,-(SP)       ;; SAVE R4
7504 036216 010546      MOV    R5,-(SP)       ;; SAVE R5
7505 036220 116704 000145      MOVB   $OMODE+1, R4    ;; GET THE NUMBER OF DIGITS TO TYPE
7506 036224 005404      NEG    R4
7507 036226 062704 000006      ADD    #6, R4          ;; SUBTRACT IT FOR MAX. ALLOWED
7508 036232 110467 000132      MOVB   R4, $OMODE     ;; SAVE IT FOR USE
7509 036236 116704 000125      MOVB   $OFILL, R4     ;; GET THE ZERO FILL SWITCH
7510 036242 016605 000012      MOV    12(SP), R5     ;; PICKUP THE INPUT NUMBER
7511 036246 005003      CLR   R3              ;; CLEAR THE OUTPUT WORD
7512 036250 006105      IS:    ROL    R5        ;; ROTATE MSB INTO "C"
7513 036252 000404      BR     35            ;; GO DO MSB

```



G11

```

7544 036254 006105 25: ROL R5 ;;FORM THIS DIGIT
7545 036256 006105 ROL R5
7546 036260 006105 ROL R5
7547 036262 010503 MOV R5,R3
7548 036264 006103 35: ROL R3 ;;GET LSB OF THIS DIGIT
7549 036266 105367 000076 DECB $OMODE ;;TYPE THIS DIGIT?
7550 036272 100016 BPL 75 ;;BR IF NO
7551 036274 042703 177770 BIC #177770,R3 ;;GET RID OF JUNK
7552 036300 001002 BNE 45 ;;TEST FOR 0
7553 036302 005704 TST R4 ;;SUPPRESS THIS 0?
7554 036304 001403 BEQ 55 ;;BR IF YES
7555 036306 025204 45: INC R4 ;;DON'T SUPPRESS ANYMORE 0'S
7556 036310 052703 000060 BIS #0,R3 ;;MAKE THIS DIGIT ASCII
7557 036314 052703 000040 55: BIS #1,R3 ;;MAKE ASCII IF NOT ALREADY
7558 036320 110367 000040 MOVB R3,$5 ;;SAVE FOR TYPING
7559 036324 104401 036364 TYPE 85 ;;GO TYPE THIS DIGIT
7560 036330 105367 000032 75: DECB $OCNT ;;COUNT BY 1
7561 036334 003347 BGT 25 ;;BR IF MORE TO DO
7562 036336 002402 BLT 65 ;;BR IF DONE
7563 036340 005204 INC R4 ;;INSURE LAST DIGIT ISN'T A BLANK
7564 036342 000744 BR 25 ;;GO DO THE LAST DIGIT
7565 036344 012605 65: MOV (SP)+,R5 ;;RESTORE R5
7566 036346 012604 MOV (SP)+,R4 ;;RESTORE R4
7567 036350 012603 MOV (SP)+,R3 ;;RESTORE R3
7568 036352 016666 000002 000004 MOV 2(SP),4(SP) ;;SET THE STACK FOR RETURNING
7569 036354 003347 RTI ;;RETURN
7570 036362 000002 85: .BYTE 0 ;;STORAGE FOR ASCII DIGIT
7571 036364 000 .BYTE 0 ;;TERMINATOR FOR TYPE ROUTINE
7572 036365 000 .BYTE 0 ;;OCTAL DIGIT COUNTER
7573 036366 000 .BYTE 0 ;;ZERO FILL SWITCH
7574 036367 000 .WORD 0 ;;NUMBER OF DIGITS TO TYPE
7575 036370 000000
7576
7577
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7581
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```

.SBTTL APT COMMUNICATIONS ROUTINE

```

*****
SATY1: MOVB #1,$FFLG ;;TO REPORT FATAL ERROR
SATY3: MOVB #1,$MFLG ;;TO TYPE A MESSAGE
SATY4: MOVB #1,$FFLG ;;TO ONLY REPORT FATAL ERROR
SATYC:
MOV R0,-(SP) ;;PUSH R0 ON STACK
MOV R1,-(SP) ;;PUSH R1 ON STACK
TSTB $MFLG ;;SHOULD TYPE A MESSAGE?
BEQ 55 ;;IF NOT: BR
CMPB #APTENV,$ENV ;;OPERATING UNDER APT?
BNE 35 ;;IF NOT: BR
BITB #APTPOOL,$ENVM ;;SHOULD SPOOL MESSAGES?
BEQ 35 ;;IF NOT: BR
MOV 24(SP),R0 ;;GET MESSAGE ADDR.
ADD #2,4(SP) ;;BUMP RETURN ADDR.
15: TST $MSGTYPE ;;SEE IF DONE W/ LAST XMISSION?
BNE 15 ;;IF NOT: WAIT
MOV R0,$MSGAD ;;PUT ADDR IN MAILBOX
25: TSTB (R0)+ ;;FIND END OF MESSAGE
BNE 25

```

```

7570 036500 166700 142626 SUB $MSGAD,RO ::SUB START OF MESSAGE
7571 036504 006200 ASR RO ::GET MESSAGE LNTH IN WORDS
7572 036506 010067 142622 MOV RO,$MSGLEN ::PUT LENGTH IN MAILBOX
7573 036512 012767 000004 142576 MOV #4,$MSGTYPE ::TELL APT TO TAKE MSG.
7574 036520 000413 9R 55
7575 036522 017667 000004 000016 35: MOV #4(SP),45 ::PUT MSG ADDR IN JSR LINKAGE
7576 036530 062766 000002 000004 ADD #2,4(SP) ::BUMP RETURN ADDRESS
7577 036536 016746 141234 MOV 177776,-SP ::PUSH 177776 ON STACK
7578 036542 004767 177114 JSR PC,$TYPE ::CALL TYPE MACRO
7579 036546 000000 45: .WORD 0
7580 036550 55:
7581 036550 105767 000062 103: TSTB $FFLG ::SHOULD REPORT FATAL ERROR?
7582 036554 001416 BEQ 125 ::IF NOT: BR
7583 036556 005767 142554 TST $ENV ::RUNNING UNDER APT?
7584 036562 001413 BEQ 125 ::IF NOT: BR
7585 036564 005767 142526 115: TST $MSGTYPE ::FINISHED LAST MESSAGE?
7586 036570 001375 BNE 115 ::IF NOT: WAIT
7587 036572 017667 000004 142520 MOV #4(SP),$FATAL ::GET ERROR #
7588 036600 062766 000002 000004 ADD #2,4(SP) ::BUMP RETURN ADDR.
7589 036606 005267 142504 INC $MSGTYPE ::TELL APT TO TAKE ERROR
7590 036612 105067 000020 125: CLRB $FFLG ::CLEAR FATAL FLAG
7591 036616 105067 000013 CLRB $LFLG ::CLEAR LOG FLAG
7592 036622 105067 000006 CLRB $MFLG ::CLEAR MESSAGE FLAG
7593 036626 012601 MOV (SP)+,R1 ::POP STACK INTO R1
7594 036630 012600 MOV (SP)+,R0 ::POP STACK INTO R0
7595 036632 000207 RTS PC ::RETURN
7596 036634 000 $MFLG: .BYTE 0 ::MESSG. FLAG
7597 036635 000 $LFLG: .BYTE 0 ::LOG FLAG
7598 036636 000 $FFLG: .BYTE 0 ::FATAL FLAG
7599 036640 .EVEN
7600 000200 APTSIZE=200
7601 000001 APTENV=001
7602 000100 APTSPool=100
7603 000040 APTCSUP=040
7604
7605 .SBTTL TTY INPUT ROUTINE
7606
7607 *****
7608 .ENABL LSB
7609
7610 *****
7611 *SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
7612 *ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
7613 *SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
7614 *WHEN OPERATING IN TTY FLAG MODE.
7615 036640 022767 000176 142272 $CKSWR: CMP #SWREG,SWR ::IS THE SOFT-SWR SELECTED?
7616 036646 001074 BNE 155 ::BRANCH IF NO
7617 036650 105777 142270 TSTB $STKS ::CHAR THERE?
7618 036654 100071 BPL 155 ::IF NO, DON'T WAIT AROUND
7619 036656 117746 142264 MOVB $STKB,-(SP) ::SAVE THE CHAR
7620 036662 042716 177600 BIC #177,(SP) ::STRIP-OFF THE ASCII
7621 036666 022726 000007 CMP #7,(SP)+ ::IS IT A CONTROL G?
7622 036672 001062 BNE 155 ::NO, RETURN TO USER
7623 036674 126727 142234 000001 CMPB $AUTOB,#1 ::ARE WE RUNNING IN AUTO-MODE?
7624 036702 001456 BEQ 155 ::BRANCH IF YES
7625

```

```

7626 036704 104401 037247          TYPE      . $CNTLG          :: ECHO THE CONTROL-G (!G)
7627 036710 104401 037254          $GTSWR: TYPE      . $MSWR          :: TYPE CURRENT CONTENTS
7628 036714 016746 141256          MOV        $WREG, -(SP)      :: SAVE SWREG FOR TYPEOUT
7629 036720 104402          TYPOC          :: GO TYPE--OCTAL ASCII, ALL DIGITS.
7630 036722 104401 037265          TYPE      . $MNEW          :: PROMPT FOR NEW SWR
7631 036726 005046          19$: CLR      -(SP)          :: CLEAR COUNTER
7632 036730 005046          CLR      -(SP)          :: THE NEW SWR
7633 036732 105777 142206          7$: *STB      $STKS          :: CHAR THERE?
7634 036736 100375          BPL        7$            :: IF NOT TRY AGAIN
7635
7636 036740 117746 142202          MOVB      $STKB, -(SP)      :: PICK UP CHAR
7637 036744 042716 177600          BIC      *1C177, (SP)      :: MAKE IT 7-BIT ASCII
7638
7639
7640
7641 036750 021627 000025          9$: CMP      (SP), #25      :: IS IT A CONTROL-U?
7642 036754 001005          BNE      10$            :: BRANCH IF NOT
7643 036756 104401 037242          TYPE      . $CNTLJ          :: YES, ECHO CONTROL-U (!U)
7644 036762 062706 000006          20$: ADD     #6 SP          :: IGNORE PREVIOUS INPUT
7645 036766 000757          BR        19$          :: LET'S TRY IT AGAIN
7646
7647
7648 036770 021627 000015          10$: CMP     (SP), #15      :: IS IT A <CR>?
7649 036774 001022          BNE      16$            :: BRANCH IF NO
7650 036776 005766 000004          TST      4(SP)          :: YES, IS IT THE FIRST CHAR?
7651 037002 001403          BEQ      11$            :: BRANCH IF YES
7652 037004 016677 000002 142126          MOV      2(SP), $SWR      :: SAVE NEW SWR
7653 037012 062706 000006          11$: ADD     #6 SP          :: CLEAR UP STACK
7654 037016 104401 001313          14$: TYPE   $CR, LF        :: ECHO <CR> AND <LF>
7655 037022 126727 142107 000001          CMPB    $INTAG, #1        :: RE-ENABLE TTY KBD INTERRUPTS?
7656 037030 001003          BNE      15$            :: BRANCH IF NOT
7657 037032 012777 000100 142104          MOV     #100, $STKS        :: RE-ENABLE TTY KBD INTERRUPTS
7658 037040 000002          15$: RTI                    :: RETURN
7659 037042 004767 177026          16$: JSR     PC, $TYPEC      :: ECHO CHAR
7660 037046 021627 000060          CMP     (SP), #60         :: CHAR < 0?
7661 037052 002420          BLT     18$            :: BRANCH IF YES
7662 037054 021627 000067          CMP     (SP), #67         :: CHAR > 7?
7663 037060 003015          BGT     18$            :: BRANCH IF YES
7664 037062 042726 000060          BIC     #60, (SP), +      :: STRIP-OFF ASCII
7665 037066 005766 000002          TST     2(SP)            :: IS THIS THE FIRST CHAR
7666 037072 001403          BEQ     17$            :: BRANCH IF YES
7667 037074 006316          ASL     (SP)            :: NO, SHIFT PRESENT
7668 037076 006316          ASL     (SP)            :: CHAR OVER TO MAKE
7669 037100 006316          ASL     (SP)            :: ROOM FOR NEW ONE.
7670 037102 005266 000002          17$: INC     2(SP)          :: KEEP COUNT OF CHAR
7671 037106 056616 177776          BIS     -2(SP), (SP)      :: SET IN NEW CHAR
7672 037112 000707          BR      7$            :: GET THE NEXT ONE
7673 037114 104401 001312          18$: TYPE   $QUES          :: TYPE ?<CR><LF>
7674 037120 000720          BR      20$            :: SIMULATE CONTROL-U
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7676
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7678
7679
7680
7681
7682:

```

:: \*\*\*\*\*  
:: \*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY  
:: \*CALL:  
:: \* RDCHR :: INPUT A SINGLE CHARACTER FROM THE TTY

```

7682          ;*      RETURN HERE          ;; CHARACTER IS ON THE STACK
7683          ;*                               ;; WITH PARITY BIT STRIPPED OFF
7684          ;*
7685          ;*
7686 037122 011646          SRDCHR: MOV      (SP),-(SP)          ;; PUSH DOWN THE PC
7687 037124 016666 000004 000002      MOV      4(SP),2(SP)          ;; SAVE THE PS
7688 037132 105777 142006 15:      TSTB     @STKS          ;; WAIT FOR
7689 037136 100375          BPL      15          ;; A CHARACTER
7690 037140 117766 142002 000004      MOVB     @STKB,4(SP)          ;; READ THE TTY
7691 037146 042766 177600 000004      BIC      @C(177),4(SP)          ;; GET RID OF JUNK IF ANY
7692 037154 026627 000004 000023      CMP      4(SP),#23          ;; IS IT A CONTROL-S?
7693 037162 001013          BNE      35          ;; BRANCH IF NO
7694 037164 105777 141754 25:      TSTB     @STKS          ;; WAIT FOR A CHARACTER
7695 037170 100375          BPL      25          ;; LOOP UNTIL ITS THERE
7696 037172 117746 141750          MOVB     @STKB, -(SP)          ;; GET CHARACTER
7697 037176 042716 177600          BIC      @C(177), (SP)          ;; MAKE IT 7-BIT ASCII
7698 037202 022627 000021          CMP      (SP)+, #21          ;; IS IT A CONTROL-Q?
7699 037206 001366          BNE      25          ;; IF NOT DISCARD IT
7700 037210 000750          BR       15          ;; YES, RESUME
7701 037212 026627 000004 000140 35:  CMP      4(SP), #140          ;; IS IT UPPER CASE?
7702 037220 002407          BLT          45          ;; BRANCH IF YES
7703 037222 026627 000004 000175      CMP      4(SP), #175          ;; IS IT A SPECIAL CHAR?
7704 037230 003003          BGT          45          ;; BRANCH IF YES
7705 037232 042766 000040 000004      BIC      #40,4(SP)          ;; MAKE IT UPPER CASE
7706 037240 000002          RTI          ;; GO BACK TO USER
7707 037242 052536 005015 000      $CNTLU: .ASCIZ /@U<15><12>          ;; CONTROL "U"
7708 037247 136 006507 000012      $CNTLG: .ASCIZ /@G<15><12>          ;; CONTROL "G"
7709 037254 005015 053523 020122      $MSWR: .ASCIZ <15><12> /SWR =
7710 037262 020075 000
7711 037265 040 047040 053505      $MNEW: .ASCIZ / NEW =
7712 037272 036440 000040

```

.SBTTL TRAP DECODER

```

*****
; *THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
; *AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
; *OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
; *GO TO THAT ROUTINE.

```

```

7722 037276 010046          $TRAP: MOV      R0, -(SP)          ;; SAVE R0
7723 037300 016600 000002      MOV      2(SP), R0          ;; GET TRAP ADDRESS
7724 037304 005740          TST      -(R0)          ;; BACKUP BY 2
7725 037306 111000          MOVB     (R0), R0          ;; GET RIGHT BYTE OF TRAP
7726 037310 006300          ASL      R0          ;; POSITION FOR INDEXING
7727 037312 016000 037332      MOV      $TRPAD(R0), R0          ;; INDEX TO TABLE
7728 037316 000200          RTS      R0          ;; GO TO ROUTINE

```

;; THIS IS USE TO HANDLE THE "GETPRI" MACRO

```

7733 037320 011646          $TRAP2: MOV      (SP), -(SP)          ;; MOVE THE PC DOWN
7734 037322 016666 000004 000002      MOV      4(SP), 2(SP)          ;; MOVE THE PSW DOWN
7735 037330 000002          RTI          ;; RESTORE THE PSW

```

.SBTTL TRAP TABLE

7737

K11

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7793

.\*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED  
.\*BY THE "TRAP" INSTRUCTION.

```

: ROUTINE
:-----
$TRPAD: .WORD $TRAP2
        $TYPE  ;;CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
        $TYPOC ;;CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $TYPOS ;;CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $TYPON ;;CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)

        $GTSWR ;;CALL=GTSWR TRAP+5(104405) GET SOFT-SWR SETTING

        $CKSWR ;;CALL=CKSWR TRAP+6(104406) TEST FOR CHANGE IN SOFT-SWR
        $RDCHR ;;CALL=RDCHR TRAP+7(104407) TTY TYPEIN CHARACTER ROUTINE
        $SAVREG ;;CALL=SAVREG TRAP+10(104410) SAVE R0-R5 ROUTINE
        $RESREG ;;CALL=RESREG TRAP+11(104411) RESTORE R0-R5 ROUTINE
        .RSET  ;;CALL=RSETUP TRAP+12(104412) ROUTINE TO RESET STACK AND FPS
        .LPER  ;;CALL=LPER TRAP+13(104413) ROUTINE TO SET LOOP ON ERROR ADDRESS

$TERM=.-$TRPAD

```

.SBTTL POWER DOWN AND UP ROUTINES

\*\*\*\*\*

```

:POWER DOWN ROUTINE
$PWRDN: MOV $SILLUP, @PWRVEC ;;SET FOR FAST UP
        MOV #340, @PWRVEC+2 ;;PRIO:7
        MOV R0, -(SP) ;;PUSH R0 ON STACK
        MOV R1, -(SP) ;;PUSH R1 ON STACK
        MOV R2, -(SP) ;;PUSH R2 ON STACK
        MOV R3, -(SP) ;;PUSH R3 ON STACK
        MOV R4, -(SP) ;;PUSH R4 ON STACK
        MOV R5, -(SP) ;;PUSH R5 ON STACK
        MOV @SWR, -(SP) ;;PUSH @SWR ON STACK
        MOV SP, $SAVR6 ;;SAVE SP
        MOV $PWRUP, @PWRVEC ;;SET UP VECTOR
        HALT
        BR .-2 ;;HANG UP

```

\*\*\*\*\*

```

:POWER UP ROUTINE
$PWRUP: MOV $SILLUP, @PWRVEC ;;SET FOR FAST DOWN
        MOV $SAVR6, SP ;;GET SP
        CLR $SAVR6 ;;WAIT LOOP FOR THE TTY
1$: INC $SAVR6 ;;WAIT FOR THE INC
        BNE 1$ OF WORD
        MOV (SP)+, @SWR ;;POP STACK INTO @SWR
        MOV (SP)+, R5 ;;POP STACK INTO R5
        MOV (SP)+, R4 ;;POP STACK INTO R4
        MOV (SP)+, R3 ;;POP STACK INTO R3
        MOV (SP)+, R2 ;;POP STACK INTO R2
        MOV (SP)+, R1 ;;POP STACK INTO R1
        MOV (SP)+, R0 ;;POP STACK INTO R0
        MOV $PWRDN, @PWRVEC ;;SET UP THE POWER DOWN VECTOR
        MOV #340, @PWRVEC+2 ;;PRIO:7

```

```

7794 037514 104401          TYPE          ;;REPORT THE POWER FAILURE
7795 037516 040344          SPWRMG: .WORD POWERM          ;;POWER FAIL MESSAGE POINTER
7796 037520 012716          MOV          (PC)+,(SP)      ;;RESTART AT START
7797 037522 003606          SPWPAD: .WORD START        ;;RESTART ADDRESS
7798 037524 042766 000020 000002  BIC          #20,2(SP)      ;;CLEAR "T" BIT
7799 037532 005067 175304  CLR          STBIT          ;;CLEAR THE "T" BIT FLAG
7800 037536 000002          RTI
7801 037540 000000          $ILLUP: HALT          ;;THE POWER UP SEQUENCE WAS STARTED
7802 037542 000776          BR          .-2          ;;BEFORE THE POWER DOWN WAS COMPLETE
7803 037544 000000          $$AVR6: 0              ;;PUT THE SP HERE

```

.SBTTL ERROR TYPE OUT ROUTINE

```

*****
*****
*THIS ROUTINE IS CALLED TO TYPE AN ERROR MESSAGE WHICH IS INCLUDED
*IN THE ERROR MESSAGE DATA TABLE. IT IS CALLED BY THE SERROR ROUTINE
*OR BY FIRST SETTING $ITEMB EQUAL TO THE ERROR TABLE ITEM TO BE PRINTED
*OUT AND THEN EXECUTING A:
*          JSR      PC,ERTYPE
*
ERTYPE: TYPE          ;TYPE A CRLF
        .WORD        $CRLF
7815 037546 104401          MOV      #2,$STSTNM,$$STMP0
7816 037550 001313          BIC      #177400,$$STMP0
7817 037552 113737 001102 001232  MOV      $$SERRPC,$$STMP1          ;GET PC OF CALL
7818 037560 042737 177400 001232  MOV      RO,-(SP)          ;SAVE RO
7819 037566 013737 001116 001234          ;
7820 037574 010046          ;
7821          MOV      #2,$ITEMB,RO          ;GET THE ITEM NUMBER.
7822 037576 113700 001114          BIC      #177400,RO
7823 037602 042700 177400          BNE      IS
7824 037606 001005          ;
7825          MOV      $$SERRPC,-(SP)          ;IF ZERO THEN JUST
7826 037610 013746 001116          PRINT THE PC
7827 037614 104402          JMP      $$ERTS
7828 037616 000137 040174          ;
7829          IS:  CMP      #377,RO
7830 037622 022700 000377          BNE      20$
7831 037626 001005          MOV      4(SP),RO
7832 037630 016600 000004          MOV      (RO),RO
7833 037634 011000          ADD      #400,RO
7834 037636 062700 000400          ;
7835 037642 005300 20$:  DEC      RO          ;OTHERWISE MAKE RO AN
7836 037644 006300          ASL      RO          ;INDEX FOR THE TABLE.
7837 037646 006300          ASL      RO
7838 037650 006300          ASL      RO
7839 037652 062700 001442  ADD      $$SERRTB,RO
7840          MOV      (RO)+,$$2$          ;PICK UP THE ADDRESS
7841 037656 012037 037666          BEQ      3$          ;OF THE EM, ERROR MESSAGE
7842 037662 001404          TYPE
7843 037664 104401          TYPE
7844 037666 000000 2$:  .WORD 0
7845 037670 104401          TYPE
7846 037672 001313          .WORD $CRLF
7847          ;
7848 037674 012037 037704 3$:  MOV      (RO)+,$$4$          ;GET THE DH,DATA HEADER
7849 037700 001404          BEQ      5$

```

# M11

7850	037702	104401			TYPE		
7851	037704	000000		4\$:	.WORD	0	
7852	037706	104401			TYPE		
7853	037710	001313			.WORD	SCRLF	
7854							
7855	037712	010146		5\$:	MOV	R1,-(SP)	;SAVE R1,R2 AND R3
7856	037714	010246			MOV	R2,-(SP)	
7857	037716	010346			MOV	R3,-(SP)	
7858							
7859	037720	012001			MOV	(R0)+,R1	;GET THE ADDRESS OF THE
7860							;DATA TABLE.
7861	037722	001001			BNE	6\$	
7862	037724	000516			BR	ERT4	;RETURN IF NO DATA.
7863							
7864	037726	011000		6\$:	MOV	(R0),R0	;GET A POINTER TO THE DATA
7865							;FORMAT TABLE.
7866	037730	105710		ERT1:	TSTB	(R0)	;FORMAT ZERO?
7867	037732	001003			BNE	7\$	
7868							
7869	037734	013146			MOV	2(R1)+,-(SP)	;FORMAT ZERO SO TYPE
7870	037736	104402			TYPOC		;AN OCTAL NUMBER.
7871	037740	000502			BR	ERT2	
7872							
7873	037742			7\$:			
7874	037742	122710	000002	8\$:	CMPB	#2,(R0)	;FORMAT TWO?
7875	037746	001010			BNE	9\$	
7876							
7877	037750	013102			MOV	2(R1)+,R2	;FORMAT TWO SO TYPE TWO
7878	037752	012246			MOV	(R2)+,-(SP)	;OCTAL NUMBERS.
7879	037754	104402			TYPOC		
7880	037756	104401			TYPE		
7881	037760	040414			.WORD	SPACE	
7882	037762	011246			MOV	(R2),-(SP)	
7883	037764	104402			TYPOC		
7884	037766	000467			BR	ERT2	
7885							
7886	037770	122710	000003	9\$:	CMPB	#3,(R0)	;FORMAT THREE?
7887	037774	001020			BNE	10\$	
7888							
7889	037776	013102			MOV	2(R1)+,R2	;FORMAT THREE SO TYPE
7890	040000	012246			MOV	(R2)+,-(SP)	;FOUR OCTAL NUMBERS.
7891	040002	104402			TYPOC		
7892	040004	104401			TYPE		
7893	040006	040414			.WORD	SPACE	
7894	040010	012246			MOV	(R2)+,-(SP)	
7895	040012	104402			TYPOC		
7896	040014	104401			TYPE		
7897	040016	040414			.WORD	SPACE	
7898	040020	012246			MOV	(R2)+,-(SP)	
7899	040022	104402			TYPOC		
7900	040024	104401			TYPE		
7901	040026	040414			.WORD	SPACE	
7902	040030	011246			MOV	(R2),-(SP)	
7903	040032	104402			TYPOC		
7904	040034	000444			BR	ERT2	
7905							

# N11

7906	040036	122710	000004	105:	CMPB	#4,(R0)		:FORMAT FOUR?
7907	040042	001004			BNE	115		
7908	040044	013146			MOV	2(R1)+,-(SP)		:FORMAT FOUR SO TYPE
7909	040046	104403			TYPOS			:AN OCTAL NUMBER
7910	040050	016			.BYTE	16		:SUPPRESSING LEADING ZEROES.
7911	040051	000			.BYTE	0		
7912	040052	000435			BR	ERT2		
7913	040054	122710	000005	115:	CMPB	#5,(R0)		:FORMAT FIVE?
7914	040060	001005			BNE	135		
7915	040062	012137	040070		MOV	(R1)+,2#125		:FORMAT FIVE SO TYPE AN
7916	040066	104401			TYPE			:ASCIZ STRING.
7917	040070	000000		125:	.WORD	0		
7918	040072	000427			BR	ERT3		
7919	040074	122710	000011	135:	CMPB	#11,(R0)		:FORMAT ELEVEN?
7920	040100	001005			BNE	155		
7921	040102	013137	040110		MOV	2(R1)+,2#145		:FORMAT ELEVEN SO PICK
7922	040106	104401			TYPE			:A POINTER TO AN ASCIZ
7923	040110	000000		145:	.WORD	0		:STRING.
7924	040112	000417			BR	ERT3		
7925	040114	122710	000012	155:	CMPB	#12,(R0)		:FORMAT TWELVE?
7926	040120	001011			BNE	175		
7927	040122	013102			MOV	2(R1)+,R2		:FORMAT TWELVE SO TYPE
7928	040124	012703	000006		MOV	#6,R3		:TYPE SIX OCTAL NUMBERS
7929	040130	012246		165:	MOV	(R2)+,-(SP)		
7930	040132	104402			TYPOS			
7931	040134	104401			TYPE			
7932	040136	040414			.WORD	SPACE		
7933	040140	077305			SOB	R3,165		
7934	040142	000401			BR	ERT2		
7935	040144	000000		175:	HALT			:UNDEFINED FORMAT FOR DATA????
7936	040146	104401		ERT2:	TYPE			:PRINT A TAB AFTER TYPING
7937	040150	040412			.WORD	STAB		:AN DATA TABLE ENTRY
7938								:OF ALL FORMATS EXCEPT
7939								:ASCIZ, FORMATS 5 OR 11
7940	040152	005200		ERT3:	INC	R0		:POINT TO THE NEXT FORMAT
7941	040154	005711			TST	(R1)		:END OF DATA TABLE.
7942	040156	001401			BEQ	ERT4		
7943	040160	000663			BR	ERT1		
7944	040162	104401		ERT4:	TYPE			:DONE.
7945	040164	001313			.WORD	\$CRLF		
7946	040166	012603			MOV	(SP)+,R3		:RESTORE R1,R2 AND R3
7947	040170	012602			MOV	(SP)+,R2		
7948	040172	012601			MOV	(SP)+,R1		
7949	040174	012600		ERT5:	MOV	(SP)+,R0		:RESTORE R0.
7950	040176	000207			RTS	PC		:AND RETURN.



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.SBTTL FPP SPURIOUS TRAP TO 244 HANDLER

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\*\*\*\*\*  
\*THIS ROUTINE HANDLES UNEXPECTED TRAPS TO THE FPP TRAP VECTOR AT 244.  
\*THE LAST FPP INSTRUCTION EXECUTED AND ITS ADDRESS HAS BEEN RECORDED  
\*THESE ALONG WITH THE FEC, FPS AND PC OF TRAP ARE REPORTED.  
\*\*\*\*\*

```
FPSPUR: MOV (SP), @STMP2 ;SAVE PC OF TRAP.
          CMP (SP)+, (SP)+ ;RESTORE SP.
          STFPS R0 ;GET FPS
          MOV R0, @STMP3
          STST R0 ;GET FEC
          MOV R0, @STMP4
IS: ERROR 211
          RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
                ;SEE IF THE USER HAS EXPRESSED
                ;THE DESIRE TO CHANGE THE SOFTWARE
                ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
                ;THE USER TYPED CONTROL G?).

040226 000137 034556 JMP @SEOP
```

.SBTTL CPU SPURIOUS TRAP TO 4 HANDLER

\*\*\*\*\*  
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\*THIS ROUTINE REPORTS UNEXPECTED CPU TRAPS TO VECTOR 4.  
\*\*\*\*\*

```
CPSPUR: MOV (SP), @STMP2 ;SAVE PC OF TRAP.
          CMP (SP)+, (SP)+
IS: ERROR 212
          RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
                ;SEE IF THE USER HAS EXPRESSED
                ;THE DESIRE TO CHANGE THE SOFTWARE
                ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
                ;THE USER TYPED CONTROL G?).

040244 000137 034556 JMP @SEOP
```

.SBTTL CPU SPURIOUS TRAP TO 10 HANDLER

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\*THIS ROUTINE REPORTS UNEXPECTED CPU TRAPS TO VECTOR 10.  
\*\*\*\*\*

```
CPTWO: MOV (SP), @STMP2 ;SAVE PC OF TRAP.
          CMP (SP)+, (SP)+
IS: ERROR 213
          RSETUP ;GO INITIALIZE THE FPS AND STACK; AND
                ;SEE IF THE USER HAS EXPRESSED
                ;THE DESIRE TO CHANGE THE SOFTWARE
                ;VIRTUAL CONSOLE SWITCH REGISTER (HAS
                ;THE USER TYPED CONTROL G?).

040262 000137 034556 JMP @SEOP
```

(vertical text on the left margin, mostly illegible)

.SBTTL SET LOOP ON ERROR ADDRESS ROUTINE  
 \*\*\*\*\*  
 \*\*\*\*\*

027 000002  
 LPER: MOV (SP),0\$SLPERR  
 RTI

.SBTTL FLAG RESET AND CONSOLE TEST ROUTINE  
 \*\*\*\*\*  
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\*THIS ROUTINE WILL BE CALLED AT THE END OF EACH TEST TO  
 \*RESET THE STACK, CLEAR THE FPS AND SEE IF THE USER HAS TYPED  
 \*CONTROL G ON THE TERMINAL. IF THE USER HAS TYPED CONTROL G AND  
 \*THERE IS NO PHYSICAL CONSOLE SWITCH REGISTER THEN THE CONTENTS  
 \*OF THE SOFTWARE SWITCH REGISTER WILL BE TYPED IN OCTAL ON THE  
 \*TELETYPE AND THE USER CAN MODIFY IT.

```

040274 023727 001140 177570 .RSET: CMP 0$SWR,0177570      :SEE IF THERE IS A PHYSICAL
040300 001001                    :CONSOLE SWITCH REGISTER.
040304 104406                    :BRANCH IF NO.
:                                     :OTHERWISE TYPE THE CONTENTS
:                                     :OF THE PROGRAM VIRTUAL SWITCH REGISTER
:                                     :AND GIVE THE USER A CHANCE TO
:                                     :MODIFY IT.

040306 012737 040200 000244 :S: MOV 0$FPSUR,0$FPVECT
040310 012737 040232 000004  :MOV 0$CPSPUR,0$ERRVECT
040314 012737 040250 000010  :MOV 0$PTWC,0$IC
040318 011600                    :MOV (SP),R0          :SAVE RETURN ADDRESS.
040322 012706 001100                    :MOV 0$STACK,SP     :RESET THE STACK POINTER.
040326 000004                    :CLR R4              :CLEAR THE FPS.
040330 170104                    :LDFPS R4
040334 000110                    :JMP (R0)            :RETURN.
    
```

.NLIST BEX

:SPECIAL MESSAGES:

```

040400 050200 053517 051105 POWERM: .ASCIZ <CRLF>'POWER FAILURE. PROGRAM RESTARTING.'<CRLF>
040404 000  :NULL: .BYTE 0
040408 000011  :TAB: .ASCIZ <TAB>
040412 020040  :SPACE: .ASCIZ ' '
040416 000 041520 047440 LFIEX1: .ASCIZ <CRLF>'PC OF LAST FPP INSTRUCTION EXECUTED: '<TAB>
040420 000 040914 052123 LFIEX2: .ASCIZ <CRLF>'LAST FPP INSTRUCTION EXECUTED: '<TAB>
040424 000 046106 040517 FPSMS: .ASCIZ <CRLF>'FLOATING POINT STATUS REGISTER: '
040428 000 042506 035103 FECMS: .ASCIZ <CRLF>'FEC: '
040432 044124 020105 000 $THE: .ASCIZ 'THE '
040436 011 044440 051516 NOOP1: .ASCIZ <TAB>' INSTRUCTION FAILED.'<CRLF>
040440 044505 044124 051105 NOOP15: .ASCII 'EITHER A BAD CONSTANT WAS GENERATED OR '
040444 115 041511 047522 .ASCIZ 'MICROPROGRAM FLOW WENT '
040448 043200 047522 020115 NOOP2: .ASCIZ <CRLF>'FROM STATE '
040452 124 020117 052123 NCOP3: .ASCIZ 'TO STATE '
040456 200 047111 052123 NOOP4: .ASCIZ <CRLF>'INSTEAD OF '
    
```

042510	042522	NOOPS:	.ASCIZ	<CRLF>'THEREBY EXECUTING A '
047111	052123	NOOP6:	.ASCIZ	<TAB>' INSTEAD OF A '
047111	052123	NOOP7:	.ASCIZ	<TAB>' INSTRUCTION.'<<CRLF>
042524	052123	NOOP10:	.ASCII	' TEST.'<<TAB>'PC OF CALL.'<<TAB>'PC OF ERROR.'<<TAB>
043040	043040		.ASCIZ	'GOT FPS. EXPECTED FPS.'<<CRLF>
041040	042101	NOOP11:	.ASCIZ	'A BAD CONSTANT MAY HAVE BEEN USED.'<<CRLF>
042114	050106	LFPS1:	.ASCIZ	<TAB>'LOFPS'<TAB>'REG'
042104	024011	LD1:	.ASCIZ	<TAB>'LDD'<TAB>'(REG),A'<TAB>'//FSRC#0//'
042104	040411	LD2:	.ASCIZ	<TAB>'LDD'<TAB>'A,A'
052123	050106	STFS1:	.ASCIZ	<TAB>'STFPS'<TAB>'REG'
042124	040411	ST1:	.ASCIZ	<TAB>'STD'<TAB>'A,REG'
052123	004504	ST2:	.ASCIZ	<TAB>'STD'<TAB>'A,A'
041506	000103	CFCC1:	.ASCIZ	<TAB>'CFCC'
052105	000106	SETF1:	.ASCIZ	<TAB>'SETF'
052105	000104	SETD1:	.ASCIZ	<TAB>'SETD'
052105	000111	SETI1:	.ASCIZ	<TAB>'SETI'
052105	000114	SETL1:	.ASCIZ	<TAB>'SETL'
046114	043505	ILL1:	.ASCIZ	<TAB>'ILLEGAL FPP INSTRUCTION'
052123	052123	STST1:	.ASCIZ	<TAB>'STST'<TAB>'REG'
046111	042514	ILL2:	.ASCIZ	<TAB>'ILLEGAL FPP INSTRUCTION (FID=1)'
042524	052123	ILLMS:	.ASCIZ	' TEST.'<<TAB>'PC OF CALL.'<<TAB>'PC OF TRAP.'<<TAB>'FPS.'<<CRLF>
042520	052103	MS1:	.ASCIZ	'EXPECTED'
020124	000000	MS2:	.ASCIZ	'GOT'
047117	042524	MS3:	.ASCIZ	'CONTENTS OF LOCATIONS'
051110	052517	MS4:	.ASCIZ	'THROUGH'
046111	051125	MS5:	.ASCIZ	'FAILURE IN THE MICROPROGRAM FLOW.'
052116	047522	MS6:	.ASCIZ	'CONTROL WENT'
046517	051440	MS7:	.ASCIZ	'FROM STATE'
020117	052123	MS10:	.ASCIZ	'TO STATE'
052125	051440	MS11:	.ASCIZ	'BUT SHOULD HAVE GONE'
052116	047522	MS12:	.ASCIZ	'CONTROL FLOW SHOULD HAVE GONE'
020124	044504	MS13:	.ASCIZ	'BUT DID NOT'
052040	051505	MS14:	.ASCII	' TEST.'<<TAB>'PC OF CALL.'<<TAB>'PC OF ERROR.'<<TAB>
020124	041520		.ASCIZ	'GOT PC.'<<TAB>'EXPECTED PC.'
051516	051124	MS15:	.ASCIZ	'INSTRUCTION TESTED:'
020122	000	MS16:	.ASCIZ	'OR'
051505	044524	MS17:	.ASCIZ	'TESTING ACCUMULATOR'
047522	000040	MNUM0:	.ASCIZ	'ZERO'
020105	000	MNUM1:	.ASCIZ	'ONE'
047527	000040	MNUM2:	.ASCIZ	'TWO'
042522	020105	MNUM3:	.ASCIZ	'THREE'
052517	020122	MNUM4:	.ASCIZ	'FOUR'
053111	020105	MNUM5:	.ASCIZ	'FIVE'
052040	051505	MS20:	.ASCIZ	' TEST.'<<TAB>'PC OF CALL.'<<TAB>'PC OF ERROR.'
040524	024040	MS21:	.ASCIZ	'DATA (FLOATING POINT NUMBER):'
043517	041511	MS22:	.ASCIZ	'LOGICAL AND OF FAILING'
043517	041511	MS23:	.ASCIZ	'LOGICAL OR OF FAILING'
042524	052123	MS24:	.ASCII	' TEST.'<<TAB>'PC OF CALL.'<<TAB>'PC OF ERRORS.'<<TAB>
041115	051105		.ASCIZ	'NUMBER OF ERRORS(OCTAL).'
050130	041505	MS25:	.ASCIZ	'EXPECTED DATA IN'
052117	042040	MS26:	.ASCIZ	'GOT DATA IN'
030103	020075	MS27:	.ASCIZ	<CRLF>'AC0='
041501	036461	MS30:	.ASCIZ	<CRLF>'AC1='
031103	020075	MS31:	.ASCIZ	<CRLF>'AC2='
041501	036463	MS32:	.ASCIZ	<CRLF>'AC3='
032103	020075	MS33:	.ASCIZ	<CRLF>'AC4='

E12

000000	000000	000000	000000	MS34:	.ASCIZ	<CRLF>'ACS= '
000001	000001	000001	000001	MS35:	.ASCIZ	'SET '
000002	000002	000002	000002	MS36:	.ASCIZ	'CLEAR '
000003	000003	000003	000003	MS37:	.ASCIZ	'LOADED DATA: '
000004	000004	000004	000004	MS40:	.ASCIZ	'READ DATA: '
000005	000005	000005	000005	MS415:	.ASCIZ	'EXPECTED DATA: '
000006	000006	000006	000006	MS41:	.ASCIZ	'DATA IN (R0) FSRC: '
000007	000007	000007	000007	MS42:	.ASCIZ	'DATA IN ACO: '
000008	000008	000008	000008	MS43:	.ASCIZ	'GOT RESULT: '
000009	000009	000009	000009	MS44:	.ASCIZ	'EXPECTED RESULT: '

:ERROR MESSAGES:

000010	000010	000010	000010	EM1:	.ASCIZ	'LDFPS AND STFPS TEST FAILED.'
000011	000011	000011	000011	EM2:	.ASCIZ	'LDFPS AND STFPS TEST ERROR SUMMARY.'
000012	000012	000012	000012	EM3:	.ASCIZ	'CFCC TRANSFERED BAD DATA TO THE PSW.'
000013	000013	000013	000013	EM4:	.ASCIZ	'CFCC MODIFIED THE FPS REGISTER.'
000014	000014	000014	000014	EM5:	.ASCIZ	'UNEXPECTED FPP TRAP TO 244.'
000015	000015	000015	000015	EM6:	.ASCIZ	'UNEXPECTED CPU TRAP TO 4.'
000016	000016	000016	000016	EM7:	.ASCIZ	'UNEXPECTED CPU TRAP TO 10.'
000017	000017	000017	000017	EM10=EMC		
000018	000018	000018	000018	EM11:	.ASCIZ	'UNABLE TO DECODE FPP INSTRUCTION. TRAPPED TO 10.'
000019	000019	000019	000019	EM12=0		
000020	000020	000020	000020	EM13=0		
000021	000021	000021	000021	EM14:	.ASCIZ	'LDFPS RD FAILED IN THE FSRC FLOWS.'
000022	000022	000022	000022		.ASCIZ	'TRAPPED TO 4.'
000023	000023	000023	000023		.ASCIZ	<CRLF>'DID NOT GO FROM STATE 400 TO 670.'
000024	000024	000024	000024	EM15:	.ASCIZ	'STFPS R1 FAILED IN THE FDST FLOWS.'
000025	000025	000025	000025		.ASCIZ	'TRAPPED TO 4.'
000026	000026	000026	000026		.ASCIZ	<CRLF>'DID NOT GO FROM STATE 634 TO 710.'
000027	000027	000027	000027	EM16:	.ASCIZ	'AN ILLEGAL FPP INSTRUCTION DID NOT TRAP.'
000028	000028	000028	000028	EM17:	.ASCIZ	'AN ILLEGAL FPP INSTRUCTION'
000029	000029	000029	000029		.ASCIZ	<CRLF>'TRAPPED TO 244, BUT FAILED TO SET '
000030	000030	000030	000030		.ASCIZ	'THE FPS CORRECTLY.'<CRLF>'EITHER A BAD CONSTANT '
000031	000031	000031	000031		.ASCIZ	'WAS GENERATED OR THE ALU LOGICAL OR FUNCTION FAILED.'
000032	000032	000032	000032	EM20:	.ASCIZ	'AN ILLEGAL FPP INSTRUCTION'
000033	000033	000033	000033		.ASCIZ	'TRAPPED TO 244, BUT A SUBSEQUENT '
000034	000034	000034	000034		.ASCIZ	'STST'<CRLF>
000035	000035	000035	000035	EM21:	.ASCIZ	'FAILED TO PICK UP THE CORRECT FEC CODE = 2.'
000036	000036	000036	000036		.ASCIZ	'STST R4 FAILED IN THE DESTINATION FLOWS.'
000037	000037	000037	000037		.ASCIZ	'TRAPPED TO 4.'<CRLF>
000038	000038	000038	000038	EM22:	.ASCIZ	'DID NOT GO FROM STATE 636 TO 710.'
000039	000039	000039	000039		.ASCIZ	'AN ILLEGAL FPP INSTRUCTION.'
000040	000040	000040	000040		.ASCIZ	'WITH INTERRUPTS DISABLED.'
000041	000041	000041	000041	EM23=EM22		
000042	000042	000042	000042	EM24=EM22		
000043	000043	000043	000043	EM25:	.ASCIZ	'SOURCE LOCATIONS MODIFIED BY. LOD.'
000044	000044	000044	000044		.ASCIZ	<CRLF>'A DATO WAS PERFORMED INSTEAD OF A DATI.'
000045	000045	000045	000045	EM26:	.ASCIZ	'LDD (R0), ACO FAILED.'<CRLF>
000046	000046	000046	000046		.ASCIZ	'R0 WAS MODIFIED.'
000047	000047	000047	000047	EM27=EM26		
000048	000048	000048	000048	EM30:	.ASCIZ	'THE PC WAS BAD AFTER '
000049	000049	000049	000049		.ASCIZ	'AN FPP INSTRUCTION.'
000050	000050	000050	000050	EM31:	.ASCIZ	'STD ACO, (R0) FAILED.'<CRLF>
000051	000051	000051	000051		.ASCIZ	'R0 WAS MODIFIED.'

	045036		041501	EM32=EM31	
	052123	020104	052520	EM33:	.ASCII *STD ACC.(RO) FAILED.*<CRLF>
	114	042104	040440		.ASCIZ *OUTPUT BAD.*
	200	044124	020105	EM34:	.ASCII *STD ACC.(RO) FAILED IN THE FDST FLOWS.*
	114	042104	024040		.ASCIZ <CRLF>*THE (BUT GR7) FORK FAILED.*
	200	044124	020105	EM35:	.ASCII *LDD (RO),ACC FAILED IN THE FSRC FLOWS.*
	123	042104	040440		.ASCIZ <CRLF>*THE (BUT GR7) FORK FAILED.*
	200	044124	020105	EM36:	.ASCII *STD ACC.(RO) FAILED IN THE FDST FLOWS.*
	042114	020104	051050		.ASCIZ <CRLF>*THE (BUT FD) FORK FAILED.*
	052200	042510	024040	EM37:	.ASCII *LDD (RO),ACC FAILED IN THE FSRC FLOWS.*
	114	042104	024040		.ASCIZ <CRLF>*THE (BUT FD) FORK FAILED.*
	200	040502	020104	EM40:	.ASCII *LDD (RO),ACC OR THE STD ACC.(RO) FAILED.*
	050106	020123	040502		.ASCIZ <CRLF>*BAD DATA WAS DETECTED AFTER A SEQUENCE OF THE TWO INSTRUCTIONS.*
				EM41:	.ASCII *FPS BAD AFTER EXECUTION OF: *
				EM42:	
	114	042104	024040		.ASCII /LDD (RO),ACC FAILED IN THE FSRC FLOWS./<CRLF>
	044124	020105	041050		.ASCIZ /THE (BUT FSRC) FORK FAILED. TRAPPED TO 4./
				EM43:	
	052123	020104	041501		.ASCII *STD ACC.(RO) FAILED IN THE FDST FLOWS.*<CRLF>
	124	042510	024040		.ASCIZ *THE (BUT FDST) FORK FAILED. TRAPPED TO 4.*
	106	050120	040440	EM44:	.ASCIZ *FPP ACCUMULATORS DATA TEST FAILED.*
	046223			EM45=EM44	
	050106	020120	041501	EM46:	.ASCIZ *FPP ACCUMULATORS DUAL ADDRESSING TEST FAILED.*
				EM47:	
	042114	040440	030503		.ASCII /LD AC1,ACC FAILED IN THE FSRC FLOWS./
	044124	020105	041050		.ASCIZ /THE (BUT FSRC) FORK FAILED. TRAPPED TO 4./
	042114	040440	030503	EM50:	.ASCII *LD AC1,ACC FAILED IN THE FSRC FLOWS.*
	044124	020105	041050		.ASCIZ *THE (BUT FD) FORK FAILED.*
	042114	040440	030503	EM51:	.ASCII *LD AC1,ACC TRANSFERRED BAD DATA.*
				EM52:	
	114	042104	024040		.ASCII /LDD (RO)+,ACC FAILED IN THE FSRC FLOWS./
	044124	020105	041050		.ASCIZ /THE (BUT FSRC) FORK FAILED. TRAPPED TO 4./
				EM53:	
	042114	020104	051050		.ASCII /LDD (RO)+,ACC FAILED IN THE FSRC FLOWS./
	200	030122	053440		.ASCII <CRLF>*RO WAS BAD.*<CRLF>
	044505	044124	051105		.ASCII *EITHER A BAD CONSTANT WAS GENERATED OR*<CRLF>
	104	042111	047040		.ASCIZ \DID NOT GO FROM STATE 627 TO 322.\
				EM54:	
	114	042104	024040		.ASCIZ /LDD (RO)+,ACC TRANSFERRED BAD DATA./
				EM55:	
	114	042104	026440		.ASCII /LDD -(RO),ACC FAILED IN THE FSRC FLOWS./
	044124	020105	041050		.ASCIZ /THE (BUT FSRC) FORK FAILED. TRAPPED TO 4./
				EM56:	
	042114	020104	024055		.ASCII /LDD -(RO),ACC FAILED IN THE FSRC FLOWS./
	200	030122	053440		.ASCII <CRLF>*RO WAS BAD.*<CRLF>
	044505	044124	051105		.ASCII *EITHER A BAD CONSTANT WAS GENERATED OR*<CRLF>
	104	042111	047040		.ASCIZ \DID NOT GO FROM STATE 627 TO 324.\
				EM57:	
	114	042104	026440		.ASCIZ /LDD -(RO),ACC TRANSFERRED BAD DATA./
				EM60:	
	114	043104	024040		.ASCII /LDF (RO)+,ACC FAILED IN THE FSRC FLOWS./
	051200	020060	040527		.ASCII <CRLF>*RO WAS BAD.*<CRLF>
	105	052111	042510		.ASCII *EITHER A BAD CONSTANT WAS GENERATED OR*<CRLF>
	044504	020104	047516		.ASCIZ \DID NOT GO FROM STATE 627 TO 322.\
				EM61:	
	042114	020106	051050		.ASCIZ /LDF (RO)+,ACC TRANSFERRED BAD DATA./

050026	042114	020106	051050	EM62:	.ASCII	'LDF (RO)+,ACD FAILED IN THE FSRC FLOWS.'
050075	044124	020105			.ASCII	<CRLF>'THE (BUT FD) FORK FAILED.'
050130	047111	052116	043040		.ASCII	'WENT FROM STATE 441 TO 077.'
050164	047111	052123	040505		.ASCIZ	'INSTEAD OF FROM 441 TO 076.'
050220	042114	020104	047043	EM63:	.ASCII	'LDD #NUM,ACD FAILED IN THE FSRC FLOWS.'
050266	052200	042510	024040		.ASCII	<CRLF>'THE (BUT GR7) FORK FAILED.'
050322	047111	052116	043040		.ASCII	'WENT FROM STATE 207 TO 174.'
050356	047111	052123	040505		.ASCIZ	'INSTEAD OF FROM 207 TO 176.'
050412	042114	020104	047043	EM64:	.ASCII	'LDD #NUM,ACD FAILED IN THE FSRC FLOWS.'
050460	040506	041040	042101		.ASCIZ	<CRLF>'A BAD CONSTANT WAS USED WHEN THE PC WAS INCREMENTED.'
	050412			EM65=EM64		
				EM66:	.ASCIZ	'LDD #NUM,ACD TRANSFERRED BAD DATA.'
	042114	020104	047043	EM67:	.ASCII	'LDD 2(RO)+,ACD FAILED IN THE FSRC FLOWS.'
	114	042104	040040		.ASCII	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
	200	044124	020105		.ASCII	<CRLF>'WENT FROM STATE 627 TO EITHER 326 OR 326.'
	200	042527	052116		.ASCIZ	<CRLF>'INSTEAD OF FROM 627 TO 323.'
	200	047111	052123	EM70:	.ASCII	'LDD 2(RO)+,ACD FAILED IN THE FSRC FLOWS.'
	042114	020104	024100		.ASCIZ	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
	052200	042510	024040	EM71:	.ASCII	'LDD 2(RO)+,ACD FAILED IN THE FSRC FLOWS.'
	114	042104	040040		.ASCIZ	'THE (BUT FD) FORK FAILED.'
	124	042510	024040	EM72:	.ASCII	'LDD 2(RO)+,ACD'<CRLF>
	114	042104	040040		.ASCIZ	'FAILED TO INCREMENT RO BY 2.'
	040506	046111	042105	EM73:	.ASCIZ	'LDD 2(RO)+,ACD LOADED BAD DATA.'
	114	042104	040040	EM74:	.ASCII	'LDD 2-(RO),ACD FAILED IN THE FSRC FLOWS.'
	114	042104	040040		.ASCII	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
	200	044124	020105		.ASCII	<CRLF>'WENT FROM STATE 627 TO EITHER 326 OR 326.'
	200	042527	052116		.ASCIZ	<CRLF>'INSTEAD OF FROM 627 TO 325.'
	200	047111	052123	EM75:	.ASCII	'LDD 2-(RO),ACD FAILED IN THE FSRC FLOWS.'
	042114	020104	026500		.ASCIZ	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
	052200	042510	024040	EM76:	.ASCII	'LDD 2-(RO),ACD FAILED IN THE FSRC FLOWS.'
	114	042104	040040		.ASCIZ	'THE (BUT FD) FORK FAILED.'
	124	042510	024040	EM77:	.ASCII	'LDD 2-(RO),ACD'<CRLF>
	114	042104	040040		.ASCIZ	'FAILED TO DECREMENT RO BY 2.'
	040506	046111	042105	EM100:	.ASCIZ	'LDD 2-(RO),ACD LOADED BAD DATA.'
	114	042104	040040	EM101:	.ASCII	'LDD NUM(RO),ACD FAILED IN THE FSRC FLOWS.'
	114	042104	047040		.ASCIZ	<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
	052200	042510	024040	EM102:	.ASCII	'LDD NUM(RO),ACD'<CRLF>
	114	042104	047040		.ASCIZ	'FAILED TO AFFECT RO BY 2.'
	106	044501	042514	EM103:	.ASCII	'LDD NUM(RO),ACD FAILED IN THE FSRC FLOWS.'
	114	042104	047040		.ASCIZ	'THE (BUT FD) FORK FAILED.'
	044124	020105	041050	EM104:	.ASCIZ	'LDD NUM(RO),ACD LOADED BAD DATA.'
	042114	020104	052516			

# H12

Address	Hex	Hex	Hex	Hex	Text
0529337	114	042104	040040		EM105:
0529337	200	044124	020105		.ASCII 'LDD ANUM(RO),ACO FAILED IN THE FSRC FLOWS.'
0529611					.ASCIIZ '<CRLF>'THE (BUT FSRC) FORK FAILED. TRAPPED TO 4.'
0529664	042114	020104	047100		EM106:
0529664	106	044501	042514		.ASCII 'LDD ANUM(RO),ACO'<CRLF>
0529706					.ASCIIZ 'FAILED TO AFFECT RO BY 2.'
0529733	114	042104	040040		EM107:
0529733	124	042510	024040		.ASCII 'LDD ANUM(RO),ACO FAILED IN THE FSRC FLOWS.'
0529733					.ASCIIZ 'THE (BUT FC) FORK FAILED.'
0529744	114	042104	040040		EM110:
0529744					.ASCIIZ 'LDD ANUM(RO),ACO LOADED BAD DATA.'
0529744	114	042104	040440		EM111:
0529744	200	041501	020067		.ASCII '/LDD AC7,ACO FAILED TO TRAP TO 244./
053105					.ASCIIZ '<CRLF>'AC7 IS AN ILLEGAL ACCUMULATOR./
053207					EM112=EM111
053207	114	042104	040440		EM113:
053251	200	041501	020066		.ASCII '/LDD AC6,ACO FAILED TO TRAP TO 244./
053207					.ASCIIZ '<CRLF>'AC6 IS AN ILLEGAL ACCUMULATOR./
053105					EM114=EM113
053207					EM115=EM111
053207					EM116=EM113
053311					EM117:
053311	125	042523	047440		.ASCII 'USE OF AN ILLEGAL ACCUMULATOR WITH FSRC MODE ZERO.'
053373	200	051124	050101		.ASCIIZ '<CRLF>'TRAPPED BUT FAILED TO SET FPS CORRECTLY.'
053445					EM120:
053445	125	042523	047440		.ASCII 'USE OF AN ILLEGAL ACCUMULATOR WITH FSRC MODE ZERO.'
053527	200	051124	050101		.ASCIIZ '<CRLF>'TRAPPED BUT FAILED TO SET FPS CORRECTLY.'
053601	123	020124	041501		EM121:
053645	200	044124	020105		.ASCII 'ST ACO,ACI FAILED IN THE FDST FLOWS.'
053720	052123	040440	030103		.ASCIIZ '<CRLF>'THE (BUT FDST) FORK FAILED. TRAPPED TO 4.'
053764	052200	042510	024040		EM122:
054017	123	020124	041501		.ASCII 'ST ACO,ACI FAILED IN THE FDST FLOWS.'
054060					.ASCIIZ '<CRLF>'THE (BUT FD) FORK FAILED.'
054060	050106	020123	040502		EM123:
054113	200	044124	020105		.ASCII 'FPS BAD AFTER LDD (RO),ACO.'
054153					.ASCIIZ '<CRLF>'THE (BUT EZBT Y8) FORK FAILED.'
054153	106	051520	041040		EM125:
054206	052200	042510	024040		.ASCII 'FPS BAD AFTER LDD (RO),ACO.'
054243	114	042104	024040		.ASCIIZ '<CRLF>'THE (BUT ENBT) FORK FAILED.'
054277	040	051506	041522		EM126:
054326	044124	020105	041050		.ASCII 'LDD (RO),ACO TRAPPED TO 244.'
054361	200	042527	052116		.ASCII ' FSRC= -0 AND FIUV= 0.'<CRLF>
054415	200	047111	052123		.ASCII 'THE (BUT FIUV) FORK FAILED.'
054415	040	051506	041522		.ASCII '<CRLF>'WENT FROM STATE 256 TO 354.'
054415	042114	020104	051050		.ASCII '<CRLF>'INSTEAD OF FROM 256 TO 254.'
054415	040	051506	041522		EM127:
054415	052200	042510	024040		.ASCII 'LDD (RO),ACO FAILED TO TRAP TO 244.'
054415	042527	052116	043040		.ASCII ' FSRC= -0, FIUV= 1.'
054415	200	047111	052123		.ASCII '<CRLF>'THE (BUT FIUV) FORK FAILED.'<CRLF>
054415	114	042104	024040		.ASCII 'WENT FROM STATE 256 TO 354.'
054415	106	051123	036503		.ASCII '<CRLF>'INSTEAD OF FROM 256 THE 354.'
054415	052502	020124	042506		EM130:
054415					.ASCII 'LDD (RO),ACO TRAPPED TO 244.'
054415					.ASCII ' FSRC= -0, FIUV= 1.'<CRLF>
054415					.ASCIIZ 'BUT FEC WAS BAD.'
054415	114	041504	042106		EM131:
054415					.ASCIIZ '/LDCFD (RO),ACO LOADED BAD DATA./
054415	114	041504	043104		EM132:
054415					.ASCIIZ '/LDCDF (RO),ACO LOADED BAD DATA./
054415					EM133:

(1)	055065	101	042104	020104	.ASCIZ	/ADD (R0),AC0 WITH (R0)=AC0=0 /
(0)	055124				EM134:	
(1)	055124	042101	043104	024040	.ASCIZ	/ADDF (R0),AC0 WITH (R0)=AC0=0 /
(0)	055163				EM135:	
(1)	055163	123	041125	020104	.ASCIZ	/SUB0 (R0),AC0 WITH (R0)=AC0=0 /
(0)	055222				EM136:	
(1)	055222	052523	043102	024040	.ASCIZ	/SUBF (R0),AC0 WITH (R0)=AC0=0 /
	055065				EM137=EM133	
	055124				EM140=EM134	
	055163				EM141=EM135	
	055222				EM142=EM136	
(0)	055261	101	042104	020104	.ASCIZ	/ADD (R0),AC0 WITH (R0)=0 /
(0)	055314				EM144:	
(1)	055314	052523	042102	024040	.ASCIZ	/SUB0 (R0),AC0 WITH (R0)=0 /
	055261				EM145=EM143	
	055314				EM146=EM144	
(0)	055347	123	041125	020104	.ASCIZ	/SUB0 (R0),AC0 WITH AC0=0 /
(1)	055347				EM150=EM147	
	055347				EM151=EM147	
(0)	055401	101	042104	020104	.ASCIZ	/ADD (R0),AC0 WITH AC0=C /
(1)	055401	055401			EM153=EM152	
(0)	055433				EM154:	
(1)	055433	101	020116	053117	.ASCII	'AN OVERFLOW ERROR OCCURRED ON ADD'<CRLF>
(1)	055475	103	052501	044523	.ASCII	'CAUSING A TRAP TO 244.'
(1)	055523	200	041050	052125	.ASCII	<CRLF>'(BUT EZBT Y9 Y8) FORK IN STATE 420 OF OVER\UNDER FAILED.'
(1)	055614	051600	047510	046125	.ASCIZ	<CRLF>'SHOULD HAVE GONE FROM STATE 420 TO 131.'
(0)	055665				EM155:	
(1)	055665	101	020116	047125	.ASCII	'AN UNDERFLOW ERROR OCCURRED ON ADD'<CRLF>
(1)	055730	040503	051525	047111	.ASCII	'CAUSING A TRAP TO 244.'
(1)	055756	024200	052502	020124	.ASCII	<CRLF>'(BUT EZBT Y9 Y8) FORK IN STATE 420 OF OVER\UNDER FAILED.'
(1)	056047	200	044123	052517	.ASCIZ	<CRLF>'SHOULD HAVE GONE FROM STATE 420 TO 131.'
(0)	056120				EM156:	
(1)	056120	042101	042104	024040	.ASCII	/ADD (R0),AC0 FAILED IN THE ROUND\TRUNK FLOWS./
(1)	056176	052200	042510	024040	.ASCII	<CRLF>'THE (BUT FD) FORK FAILED. WENT'
(1)	056235	106	047522	020115	.ASCII	\FROM STATE 665 TO 113.\<CRLF>
(1)	056264	047111	052123	040505	.ASCIZ	\INSTEAD OF FROM 665 TO 313.\<CRLF>\WITH FT SET.\
(0)	056335				EM157:	
(1)	056335	101	042104	020104	.ASCII	/ADD (R0),AC0 FAILED IN THE ROUND\TRUNK FLOWS./
(1)	056413	200	044124	020105	.ASCII	<CRLF>'THE (BUT FD) FORK FAILED. WENT'
(1)	056452	051106	046517	051440	.ASCII	\FROM STATE 665 TO 313.\<CRLF>
(1)	056501	111	051516	042524	.ASCIZ	\INSTEAD OF FROM 665 TO 113.\<CRLF>\WITH FT CLEAR.\
(0)	056554				EM160:	
(1)	056554	042101	042104	024040	.ASCII	/ADD (R0),AC0 FAILED IN THE ROUND\TRUNK FLOWS.<CRLF>
(1)	056633	124	042510	043040	.ASCII	'THE FLOATING CONSTANT WAS USED INSTEAD OF THE DOUBLE CONSTANT'<CRLF>
(1)	056731	111	020116	044124	.ASCIZ	'IN THE ROUND ALGORITHM.'
(0)	056761				EM161:	
(1)	056761	101	042104	020106	.ASCII	/ADDF (R0),AC0 FAILED IN THE ROUND\TRUNK FLOWS.<CRLF>
(1)	057040	044124	020105	047504	.ASCII	'THE DOUBLE CONSTANT WAS USED INSTEAD OF THE FLOATING CONSTANT'<CRLF>
(1)	057136	047111	02040	042510	.ASCIZ	'IN THE ROUND ALGORITHM.'
(0)	057166				EM162:	
(1)	057166	042101	042104	024040	.ASCIZ	/ADD (R0),AC0 PRODUCED A BAD RESULT./
(0)	057233				EM163:	
(1)	057233	101	042104	020106	.ASCIZ	/ADDF (R0),AC0 PRODUCED A BAD RESULT./



(0)	057300				EM164:	
(1)	057300	044124	020105	050106	.ASCIZ	THE FPS WAS BAD AFTER ADDD (RD),ACD.
(0)	057345				EM165:	
(1)	057345	124	042510	043040	.ASCIZ	THE FPS WAS BAD AFTER ADDF (RD),ACD.
(0)	057412				EM166:	
(1)	057412	042101	042104	024040	.ASCII	ADDD (RD),ACD PRODUCED A BAD RESULT.<<CRLF>
(1)	057457	120	047522	040502	.ASCIZ	PROBABLE ERROR IN THE ALIGN FLOWS.'
(0)	057522				EM167:	
(1)	057522	042101	042104	024040	.ASCII	ADDD (RD),ACD FAILED IN THE ALIGN FLOWS.<<CRLF>
(1)	057573	106	047514	020127	.ASCII	FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 111, TO 014.
(1)	057663	200	020101	040502	.ASCII	<CRLF>A BAD CONSTANT (NOT 57 DEC) \
(1)	057720	040527	020123	051525	.ASCIZ	WAS USED IN THE ALIGN ALGORITHM.'
(0)	057761				EM170:	
(1)	057761	101	042104	020106	.ASCII	ADDF (RD),ACD PRODUCED A BAD RESULT.<<CRLF>
(1)	060026	051120	041117	041101	.ASCIZ	PROBABLE ERROR IN THE ALIGN FLOWS.'
(0)	060071				EM171:	
(1)	060071	101	042104	020106	.ASCII	ADDF (RD),ACD FAILED IN THE ALIGN FLOWS.<<CRLF>
(1)	060142	046106	053517	042040	.ASCII	FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 111, TO 014.\
(1)	060232	040600	041040	042101	.ASCII	<CRLF>A BAD CONSTANT (NOT 25 DEC) \
(1)	060267	127	051501	052440	.ASCIZ	WAS USED IN THE ALIGN ALGORITHM.'
(0)	060330				EM172:	
(1)	060330	042101	042104	024040	.ASCII	ADDD (RD),ACD FAILED IN THE ALIGN FLOWS.<<CRLF>
(1)	060401	106	047514	020127	.ASCII	FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 011, TO 015.\
(1)	060471	200	020101	040502	.ASCII	<CRLF>A BAD CONSTANT (NOT 57 DEC) \
(1)	060526	040527	020123	051525	.ASCIZ	WAS USED IN THE ALIGN ALGORITHM.'
(0)	060567				EM173:	
(1)	060567	101	042104	020104	.ASCII	ADDD (RD),ACD FAILED IN THE ALIGN FLOWS.<<CRLF>
(1)	060640	046106	053517	042040	.ASCII	FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 011, TO 215.\
(1)	060730	040600	041040	042101	.ASCII	<CRLF>A BAD CONSTANT (NOT 57 DEC) \
(1)	060765	127	051501	052440	.ASCIZ	WAS USED IN THE ALIGN ALGORITHM.'
(0)	061026				EM174:	
(1)	061026	042101	043104	024040	.ASCII	ADDF (RD),ACD FAILED IN THE ALIGN FLOWS.<<CRLF>
(1)	061077	106	047514	020127	.ASCII	FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 011, TO 015.\
(1)	061167	200	020101	040502	.ASCII	<CRLF>A BAD CONSTANT (NOT 25 DEC) \
(1)	061224	040527	020123	051525	.ASCIZ	WAS USED IN THE ALIGN ALGORITHM.'
(0)	061265				EM175:	
(1)	061265	101	042104	020106	.ASCII	ADDF (RD),ACD FAILED IN THE ALIGN FLOWS.<<CRLF>
(1)	061336	046106	053517	042040	.ASCII	FLOW DID NOT FOLLOW THE PATH: STATE 476, TO 011, TO 215.\
(1)	061426	040600	041040	042101	.ASCII	<CRLF>A BAD CONSTANT (NOT 25 DEC) \
(1)	061463	127	051501	052440	.ASCIZ	WAS USED IN THE ALIGN ALGORITHM.'
(0)	061524				EM176:	
(1)	061524	042101	042104	024040	.ASCII	ADDD (RD),ACD FAILED IN THE ADD-SUB FLOWS.<<CRLF>
(1)	061577	104	042111	047040	.ASCIZ	DID NOT TAKE THE PATH: STATE 216, TO 442, TO 500.\
(0)	061661				EM177:	
(1)	061661	101	042104	020104	.ASCII	ADDD (RD),ACD FAILED IN THE ADD-SUB FLOWS.<<CRLF>
(1)	061734	044504	020104	047516	.ASCIZ	DID NOT TAKE THE PATH: STATE 216, TO 042, TO 121.\
(0)	062016				EM200:	
(1)	062016	042101	042104	024040	.ASCII	ADDD (RD),ACD FAILED IN THE ADD-SUB FLOWS.<<CRLF>
(1)	062071	104	042111	047040	.ASCIZ	DID NOT TAKE THE PATH: STATE 216, TO 440, TO 121.\
(0)	062153				EM201:	
(1)	062153	101	042104	020104	.ASCII	ADDD (RD),ACD FAILED IN THE ADD-SUB FLOWS.<<CRLF>
(1)	062226	044504	020104	047516	.ASCIZ	DID NOT TAKE THE PATH: STATE 216, TO 440, TO 101.\
(0)	062310				EM202:	
(1)	062310	042101	042104	024040	.ASCII	ADDD (RD),ACD FAILED IN THE ADD-SUB FLOWS.<<CRLF>
(1)	062363	104	042111	047040	.ASCIZ	DID NOT TAKE THE PATH: STATE 216, TO 042, TO 101.\
(0)	062445				EM203:	

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(1) 062445 101 042104 020104 .ASCII *ADD (RD),ACD FAILED IN THE ADD-SUB FLOWS.<CRLF>
(1) 062520 044504 020104 047516 .ASCIZ \DID NOT TAKE THE PATH: STATE 216, TO 440, TO 141.\
(0) 062602 EM204:
(1) 062602 042101 042104 024040 .ASCII *ADD (RD),ACD FAILED IN THE ADD-SUB FLOWS.<CRLF>
(1) 062655 104 042111 047040 .ASCIZ \DID NOT TAKE THE PATH: STATE 216, TO 042, TO 141.\
(0) 062737 EM205:
(1) 062737 124 042510 043040 .ASCIZ \THE FPS WAS BAD AFTER SUBD (RD),ACD.\
(0) 063004 EM206:
(1) 063004 052523 042102 024040 .ASCIZ /SUBD (RD),ACD PRODUCED A BAD RESULT./
063051 123 041125 020104 EM207: .ASCII *SUBD (RD),ACD PRODUCED A BAD RESULT.*
063115 200 044124 020105 .ASCIZ <CRLF>*THE XOR OF THE SIGN BIT FAILED IN STATE 024.*
063173 101 042104 020104 EM210: .ASCIZ *ADD (RD),ACD FAILED IN THE NORMALIZE FLOWS.*
043233 EM211=EM5
043267 EM212=EM6
043321 EM213=EM7

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;DATA HEADERS

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063250 020040 042524 052123 DH1: .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.*
063310 043411 047522 042524 .ASCIZ <TAB>*WROTE.<TAB>*READ.<TAB>*EXPECTED.*
063340 020040 042524 052123 DH2: .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.*
063400 047101 020104 040502 .ASCIZ *AND BAD DATA.<TAB>*OR BAD DATA.*
063433 040 052040 051505 DH3: .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.*
063473 011 042522 042101 .ASCIZ <TAB>*READ PSW.<TAB>*EXPECTED PSW.*
063524 020040 042524 052123 DH4: .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.*
063564 043411 047522 042524 .ASCIZ <TAB>*WROTE FPS.<TAB>*FPS AFTER CFCC.*
063620 020040 042524 052123 DH5: .ASCIZ * TEST.<TAB>*PC OF CALL.<TAB>*PC OF TRAP.*
063620 DH6=DH5
063620 DH7=DH5
063620 DH10=DH5
063620 DH11=DH5
000000 DH12=0
000000 DH13=0
063620 DH14=DH5
063620 DH15=DH5
063660 020040 042524 052123 DH16: .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.*
063720 047411 020120 047503 .ASCIZ <TAB>*OP CODE. FPS.*
063740 020040 042524 052123 DH17: .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.*
064000 043411 052117 043040 .ASCIZ <TAB>*GOT FPS.<TAB>*EXPECTED FPS.*
064030 020040 042524 052123 DH20: .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF TRAP.*
064067 011 041520 047440 .ASCIZ <TAB>*PC OF STST.<TAB>*READ FEC.*
064116 063620 DH21=DH5
064153 040506 046111 042105 DH22: .ASCIZ *FAILED TO CORRECTLY SET FPS.*
064222 106 044501 042514 DH23: .ASCII *FAILED TO CORRECTLY SET FEC TO 000002.<CRLF>
064262 020040 042524 052123 .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.*
064311 050011 020103 043117 .ASCIZ <TAB>*PC OF STST.<TAB>*READ FEC.*
064377 124 040522 050120 DH24: .ASCII *TRAPPED TO 244. FLOW WENT FROM STATE 554 TO STATE 430.*
064450 200 047111 052123 .ASCIZ <CRLF>*INSTEAD OF FROM STATE 554 TO STATE 432.*
064512 020040 042524 052123 DH25: .ASCIZ * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.<TAB>
064552 043411 052117 051040 DH26: .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.*
064512 .ASCIZ <TAB>*GOT RD.<TAB>*EXPECTED RD.*
000000 DH27=DH26
064512 DH30=0
064512 DH31=DH26
064512 DH32=DH26
064600 020040 042524 052123 DH33: .ASCII * TEST.<TAB>*PC OF CALL.<TAB>*PC OF ERROR.*

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064640 051011 020060 052050 .ASCIZ '(TAB)'RO (TARGET LOCATIONS FOR OUTPUT).'
      064600 DH34=DH33
      064600 DH35=DH33
      064600 DH36=DH33
      064600 DH37=DH33
      064600 DH40=DH33
      000000 DH41=0
064703 040 052040 051505 DH42: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF TRAP.'
064742 051011 020060 052050 .ASCIZ '(TAB)'RO (TARGET LOCATIONS FOR OUTPUT).'
      064703 DH43=DH42
      000000 DH44=0
065005 105 051122 051117 DH45: .ASCIZ 'ERROR SUMMARY.'
065024 020040 042524 052123 DH46: .ASCIZ ' TEST.'(TAB)'CALL AT PC.'
      065005 DH47=DH45
065050 020040 042524 052123 DH50: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
065110 053411 052111 020110 .ASCIZ '(TAB)'WITH FD.'
      065050 DH51=DH50
      063620 DH52=DH5
      064512 DH53=DH26
065122 020040 042524 052123 DH54: .ASCIZ ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
      063620 DH55=DH5
      064512 DH56=DH26
      065122 DH57=DH54
      064512 DH60=DH26
      065122 DH61=DH54
      065122 DH62=DH54
      065122 DH63=DH54
065163 122 051505 046125 DH65: .ASCII 'RESULTING IN AN ODD ADDRESS TRAP TO 4.'
065231 200 .ASCII '<CRLF>'
065232 020040 042524 052123 DH64: .ASCII ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
065272 043411 052117 050040 .ASCIZ '(TAB)'GOT PC.'(TAB)'EXPECTED PC.'
      065122 DH66=DH54
      063620 DH67=DH5
      063620 DH70=DH5
      064450 DH71=DH25
      064512 DH72=DH26
      065122 DH73=DH54
      063620 DH74=DH5
      063620 DH75=DH5
      064450 DH76=DH25
      064512 DH77=DH26
      065122 DH100=DH54
      063620 DH101=DH5
      064512 DH102=DH26
      064450 DH103=DH25
      065122 DH104=DH54
      063620 DH105=DH5
      064512 DH106=DH26
      064450 DH107=DH25
      065122 DH110=DH54
      064450 DH111=DH25
065320 044124 020105 041050 DH112: .ASCII 'THE (BUT FSRC) FORK FAILED.'(CRLF)
065354 047503 052116 047522 .ASCII 'CONTROL WENT FROM STATE 762 TO STATE 627.'
065425 200 047111 052123 .ASCII '(CRLF)'INSTEAD OF FROM STATE 762 TO STATE 637.'(CRLF)
06547E 020040 042524 052123 .ASCIZ ' TEST.'(TAB)'PC OF CALL.'(TAB)'PC OF ERROR.'
      064450 DH113=DH25

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M12

065537	065320			DH114=DH112	
065540	124	042510	024040	DH115: .ASCII	'THE (BUT FSPC) FORK FAILED RESULTING IN AN ODD ADDRESS TRAP TO 4.'
065713	041600	047117	051124	.ASCII	<CRFL>'CONTRCL WENT FROM STATE 762 TO STATE 627.'<CRFL>
065763	111	051516	042524	.ASCII	'INSTEAD OF FROM STATE 762 TO STATE 627.'<CRFL>
	040	052040	051505	.ASCIZ	' TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF TRAP.'
	065537			DH116=DH115	
	063740			DH117=DH17	
066023	040	052040	051505	DH120: .ASCII	' TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF ERROR.'
066063	011	047507	020124	.ASCIZ	<TAB>'GOT FEC.'<TAB>'EXPECTED FEC.'
	063620			DH121=DH5	
	065050			DH122=DH50	
	065050			DH123=DH50	
	063740			DH124=DH17	
	063740			DH125=DH17	
	063620			DH126=DH5	
	065122			DH127=DH54	
	066023			DH130=DH120	
	065122			DH131=DH54	
	065122			DH132=DH54	
066113	106	044501	042514	DH133: .ASCII	'FAILED TO PRODUCE THE CORRECT RESULTS.'<CRFL>
066162	020040	042524	052123	.ASCIZ	' TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF ERROR.'
	066113			DH134=DH133	
	066113			DH135=DH133	
	066113			DH136=DH133	
066223	120	047522	052504	DH137: .ASCII	'PRODUCED THE CORRECT RESULT BUT FAILED TO SET THE FPS CORRECTLY.'
066323	040	052040	051505	.ASCII	' TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF ERROR.'
066363	011	047507	020124	.ASCIZ	<TAB>'GOT FPS.'<TAB>'EXPECTED FPS.'
	066223			DH140=DH137	
	066223			DH141=DH137	
	066223			DH142=DH137	
	066113			DH143=DH133	
	066113			DH144=DH133	
	066223			DH145=DH137	
	066223			DH146=DH137	
	065122			DH147=DH54	
066413	130	051117	047440	DH150: .ASCII	'XOR OF SIGN BIT FAILED.'<CRFL>
066443	040	052040	051505	.ASCIZ	' TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF ERROR.'
	066223			DH151=DH137	
	066113			DH152=DH133	
	066223			DH153=DH137	
066504	020040	042524	052123	DH154: .ASCIZ	' TEST.'<TAB>'PC OF CALL.'<TAB>'PC OF TRAP.'
	066504			DH155=DH154	
	065122			DH156=DH54	
	065122			DH157=DH54	
	065122			DH160=DH54	
	065122			DH161=DH54	
	065122			DH162=DH54	
	065122			DH163=DH54	
	063740			DH164=DH17	
	063740			DH165=DH17	
	065122			DH166=DH54	
	065122			DH167=DH54	
	065122			DH170=DH54	
	065122			DH171=DH54	
	065122			DH172=DH54	
	065122			DH173=DH54	

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042524 052123

DH174=DH54  
DH175=DH54  
DH176=DH54  
DH177=DH54  
DH200=DH54  
DH201=DH54  
DH202=DH54  
DH203=DH54  
DH204=DH54  
DH205=DH17  
DH206=DH54  
DH207=DH54  
DH210=DH54  
DH211:  
DH212=DH5  
DH213=DH5

.ASCIZ 'TEST.'  
'TAB' PC OF CALL.  
'TAB' PC OF TRAP.  
'TAB' 'FEC.'

;DATA FORMATS:

066611	004	000	005	DF1:	.BYTE	4,0,5,0,5,0,0,0
066621	004	000	005	DF2:	.BYTE	4,0,5,4,5,0,5,0
066631	004	000	005	DF3:	.BYTE	4,0,5,0,5,0,5,0
066641	066631	000	005	DF4=DF3		
	004			DF5:	.BYTE	4,0,5,0,5,0,5,11,5,0,5,0
	066641			DF6=DF5		
	066641			DF7=DF5		
	066641			DF10=DF5		
	066641			DF11=DF5		
066655	005	011	005	DF12:	.BYTE	5,11,5,5,5,4,5,4,5,5,4,5,4,5,11,5,11,5,5,4,0,5,0,5,0,0
066707	005	011	005	DF13:	.BYTE	5,11,5,5,5,4,0,5,0,5,0,0
	066641			DF14=DF6		
	066641			DF15=DF6		
066723	004	000	005	DF16:	.BYTE	4,0,5,0,5,0,0
	066631			DF17=DF3		
066732	004	000	005	DF20:	.BYTE	4,0,5,0,5,0,5,0
066742	004	000	005	DF21:	.BYTE	4,0,5,0,5,0,5,0
066746	005	005	004	DF22:	.BYTE	5,5,4,0,5,0,5,0,5,0
066760	004	000	005	DF23:	.BYTE	4,0,5,0,5,0,5,0
066770	005	004	000	DF24:	.BYTE	5,4,0,5,0,5,0,5,0
066777	004	000	005	DF25:	.BYTE	4,0,5,0,5,0,5,0,5,0,5,0,5,5,5,0,5,0,5,0
067023	004	000	005	DF26:	.BYTE	4,0,5,0,5,0,5,0,5,5,5,5,4,5,4,5,5,5,4,5,4
067050	004	000	005	DF27:	.BYTE	4,0,5,0,5,0,5,0,5,5,5,5,4,5,4,5,5
067070	005	011	005	DF30:	.BYTE	5,11,5,5,5,4,0,5,0,5,0,0
	067023			DF31=DF26		
	067050			DF32=DF27		
067104	004	000	005	DF33:	.BYTE	4,0,5,0,5,0,5,5,5,0,5,0,5,12,5,5,5,0,5,0,5,12
067132	004	000	005	DF34:	.BYTE	4,0,5,0,5,0,5,5,5,5,4,5,4,5,5,5,5,4,5,4
	067132			DF35=DF34		
	067132			DF36=DF34		
	067132			DF37=DF34		
067156	004	000	005	DF40:	.BYTE	4,0,5,0,5,0,5,5,5,0,5,0,5,3,5,5,5,0,5,0,5,3
067204	011	005	005	DF41:	.BYTE	4,0,5,0,5,0,5,5,5,0,5,0,5,3,5,5,5,0,5,0,5,3
067220	004	000	005	DF42:	.BYTE	4,0,5,0,5,0,5,5,5,5,4,5,4,11,4,5,5,5,5,4,5,4
	067220			DF43=DF42		
067246	005	011	005	DF44:	.BYTE	5,11,5,5,5,4,0,5,0,5,5,5,5,3,5,5,5,5,3

067427	067427	000	005	DF45: .BYTE	5.1.1.5.5.5.4.0.5.0.5.4.5.5.5.3.5.5.5.3
067428	067428	000	005	DF46: .BYTE	4.0.5.5.5.5.3.5.3.5.3.5.3.5.3.5.3.5.3.5.3
067429	067429	000	005	DF47: .BYTE	4.0.5.5.5.5.1.5.5.5.4.5.5.5.5.4.5.4
067430	067430	000	005	DF48: .BYTE	4.0.5.0.5.1.5.5.5.3.5.5.5.3
067431	067431	000	005	DF49: .BYTE	4.0.5.0.5.0.0
067432	067432	000	005	DF50: .BYTE	4.0.5.0.5.5.5.5.3.5.5.5.5.3
067433	067433	000	005	DF51: .BYTE	4.0.5.0.5.0.0
067434	067434	000	005	DF52=DF47	
067435	067435	000	005	DF53: .BYTE	4.0.5.0.5.0.0
067436	067436	000	005	DF54: .BYTE	4.0.5.0.5.5.5.5.3.5.5.5.5.3
067437	067437	000	005	DF55=DF47	
067438	067438	000	005	DF56=DF53	
067439	067439	000	005	DF57=DF54	
067440	067440	000	005	DF60=DF53	
067441	067441	000	005	DF61=DF54	
067442	067442	000	005	DF62=DF54	
067443	067443	000	005	DF63=DF54	
067454	067454	000	005	DF64: .BYTE	4.0.5.0.5.0.0
067455	067455	000	005	DF65=DF64	
067456	067456	000	005	DF66=DF54	
067457	067457	000	005	DF67=DF21	
067458	067458	000	005	DF70: .BYTE	4.0.5.0.5.5.5.5.4.5.4.5.5.5.5.4.5.4
067459	067459	000	005	DF71=DF70	
067505	067505	000	005	DF72: .BYTE	4.0.5.0.5.0.0
067506	067506	000	005	DF73=DF54	
066741	066741	000	005	DF74=DF21	
067460	067460	000	005	DF75=DF70	
067461	067461	000	005	DF76=DF70	
067462	067462	000	005	DF77=DF72	
067463	067463	000	005	DF100=DF54	
067464	067464	000	005	DF101=DF70	
067508	067508	000	005	DF102=DF72	
067465	067465	000	005	DF103=DF70	
067466	067466	000	005	DF104=DF54	
067467	067467	000	005	DF105=DF70	
067509	067509	000	005	DF106=DF72	
067468	067468	000	005	DF107=DF70	
067510	067510	000	005	DF110=DF54	
067514	067514	000	005	DF111: .BYTE	4.0.5.0
067515	067515	000	005	DF112=DF111	
067516	067516	000	005	DF113=DF111	
067517	067517	000	005	DF114=DF111	
067518	067518	000	005	DF115=DF111	
067519	067519	000	005	DF116=DF111	
066631	066631	000	005	DF117=DF3	
066632	066632	000	005	DF120=DF3	
067350	067350	000	005	DF121=DF47	
067351	067351	000	005	DF122=DF50	
067411	067411	000	005	DF123=DF51	
067520	067520	000	005	DF124: .BYTE	4.0.5.0.5.0.0.5.5.5.5.4.5.4.5.5.5.5.4.5.4.5.5.5.3
067521	067521	000	005	DF125=DF124	
067522	067522	000	005	DF126=DF111	
067523	067523	000	005	DF127=DF111	
066631	066631	000	005	DF130=DF3	
067551	067551	000	005	DF131: .BYTE	4.0.5.0.5.5.5.3.5.5.5.3.5.5.5.3
067552	067552	000	005	DF132=DF131	
067571	067571	000	005	DF133: .BYTE	4.0.5.0.5.5.5.3.5.5.5.3.5.5.5.3.5.5.5.3
067572	067572	000	005	DF134=DF133	

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067615 06751: 000 005 DF135=DF133
06751: DF136=DF133
06751: DF137: .BYTE 4,0,5,0,5,0,5,0
06751: DF140=DF137
06751: DF141=DF137
06751: DF142=DF137
06751: DF143=DF133
06751: DF144=DF133
06751: DF145=DF137
06751: DF146=DF137
06751: DF147=DF133
06751: DF150=DF133
06751: DF151=DF137
06751: DF152=DF133
067625 06753: 000 005 DF153=DF137
06753: DF154: .BYTE 4,0,5,0
06753: DF155=DF154
06753: DF156=DF133
06753: DF157=DF133
067631 06757: 000 005 DF160=DF133
06757: DF161: .BYTE 4,0,5,0,5,5,5,2,5,5,5,2,5,5,5,2,5,5,5,2
06757: DF162=DF133
06757: DF163=DF161
06663: DF164=DF3
06553: DF165=DF3
06757: DF166=DF133
06757: DF167=DF133
06757: DF170=DF161
06757: DF171=DF161
06757: DF172=DF133
06757: DF173=DF133
06757: DF174=DF161
06757: DF175=DF161
06757: DF176=DF133
06757: DF177=DF133
06757: DF200=DF133
06757: DF201=DF133
06757: DF202=DF133
06757: DF203=DF133
06757: DF204=DF133
06663: DF205=DF3
06757: DF206=DF133
06757: DF207=DF133
06757: DF210=DF133
067655 06757: 000 005 DF211: .BYTE 4,0,5,0,5,0
06757: DF212=DF211
06757: DF213=DF211

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067664 .EVEN
;DATA TABLES:
067564 001232 001234 040412 DT1: .WORD $TMP0,$TMP1,$TAB,$TMP2,$TAB,$TMP3
067700 001242 001244 000000 .WORD $TMP4,$TMP5,0
067706 001232 001234 040412 DT2: .WORD $TMP0,$TMP1,$TAB,$RERFLG,$TAB,$TMP2,$TAB,$TMP3,0
067730 001232 001234 040412 DT3: .WORD $TMP0,$TMP1,$TAB,$TMP2,$TAB,$TMP3
067744 040412 001242 000000 .WORD $TAB,$TMP4,0

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067752	067770	070004	067730	001234	040412	DT4=DT3			
			001232	040531	001240	DT5:	.WORD	STMP0,STMP1,STAB,STMP2,LFIX1,STMP21,LFIX2	
			001232	040412		DT6:	.WORD	STMP20,FPMS,STMP3,FECS,STMP4,0	
			070004			DT7=DT6		STMP0,STMP1,STAB,STMP2,LFIX1,STMP21,LFIX2,STMP20,C	
			070004			DT10=DT6			
			070004			DT11=DT6			
070026	070042	070064	040606	001252	040613	DT12:	.WORD	STHE,STMP10,NOOP1,NOOP15,NOOP2,STMS	
			040755	001246	040767		.WORD	NOOP3,STMP6,NOOP4,NOOP2,STMS,NOOP3,STMP7,NOOP5,STMP11	
			041032	001252	041052		.WORD	NOOP6,STMP10,NOOP7,NOOP10,STMP0,STMP1,STAB,STMP2	
			040412	001240	001242		.WORD	STAB,STMP3,STMP4,0	
			040606	001252	040613	DT13:	.WORD	STHE,STMP10,NOOP1,NOOP11,NOOP10,STMP0,STMP1,STAB	
			001236	040412	001240		.WORD	STMP2,STAB,STMP3,STMP4,0	
			070004			DT14=DT6			
			070004			DT15=DT6			
070146			001232	001234	040412	DT16:	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMS,STMP3,0	
			067730			DT17=DT3			
070166			001232	001234	040412	DT20:	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			040412	001234	000000		.WORD	STAB,STMP4,0	
			001232	001234	040412	DT21:	.WORD	STMP0,STMP1,STAB,STMP2,C	
			063433	001313		DT22:	.WORD	DH3,SCRF	
			001232	001234	040412		.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			040412	001242	000000		.WORD	STAB,STMP4,0	
			001232	001234	040412	DT23:	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			040412	001242	000000		.WORD	STAB,STMP4,0	
			041506			DT24:	.WORD	ILLMS	
			001232	001234	040412		.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			040412	001242	000000		.WORD	STAB,STMP4,0	
			001232	001234	040412	DT25:	.WORD	STMP0,STMP1,STAB,SCRF,MS1,MS3,STMP3,MS4,STMP4,SCRF	
			001242	001313	041566		.WORD	STMP4,SCRF,MS2,MS3,STMS,MS4,STMP6,SCRF,STMS,0	
			001232	001234	040412	DT26:	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3,STMP4,SCRF	
			041676	001313	041714		.WORD	MS6,SCRF,MS7,STMS,MS10,STMP6,SCRF	
			041743	001313	041714		.WORD	MS11,SCRF,MS7,STMS,MS10,STMP7,0	
			001232	001234	040412	DT27:	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			001242	001313	041770		.WORD	STMP4,SCRF,MS12,SCRF,MS7,STMS,MS10,STMP7,SCRF,MS13,0	
			042131	001272	001313	DT30:	.WORD	MS15,STMP20,SCRF,MS14,SCRF	
			001232	001234	040412		.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			001242	000000			.WORD	STMP4,0	
			070366			DT31=DT26			
			070442			DT32=DT27			
070536			001232	001234	040412	DT33:	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			001313	041554	041573		.WORD	SCRF,MS1,MS3,STMP4,MS4,STMS,SCRF,STMP6,SCRF	
			041566	041573	001242		.WORD	MS2,MS3,STMP4,MS4,STMS,SCRF,STMP4,0	
			001232	001234	040412	DT34:	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			001313	041676	001313		.WORD	SCRF,MS6,SCRF,MS7,STMS,MS10,STMP6,SCRF	
			041743	001313	041714		.WORD	MS11,SCRF,MS7,STMS,MS10,STMP7,0	
			070614			DT35=DT34			
			070614			DT36=DT34			
			070614			DT37=DT34			
			070536			DT40=DT33			
070666			001272	001313	063433	DT41:	.WORD	STMP20,SCRF,DH3,SCRF	
			001232	001234	040412		.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			040412	001242	000000		.WORD	STAB,STMP4,0	
			001232	001234	040412	DT42:	.WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3	
			001313	041676	001313		.WORD	SCRF,MS6,SCRF,MS7,STMS,MS10,STMP6,STMP15,STMP10	
			001313	041743	001313		.WORD	SCRF,MS11,SCRF,MS7,STMS,MS10,STMP7,0	



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070776	070720	001244	001313	DT43=DT42	
071020	042163	041554	042314	DT44: .WORD	MS17, STMP5, SCRLF, MS20, SCRLF, STMP0, STMP1, STAB, STMP2
071046	042163	001244	001313		SCRLF, MS1, MS21, SCRLF, STMP3, SCRLF, MS2, MS21, SCRLF, STMP4, 0
071072	001246	001313	042353	DT45: .WORD	MS17, STMP5, SCRLF, MS24, SCRLF, STMP0, STMP1, STAB, STMP2, STAB
071110	042403	042314	001313		STMP6, SCRLF, MS22, MS21, SCRLF, STMP3, SCRLF
071122	001232	001234	001313	DT46: .WORD	MS23, MS21, SCRLF, STMP4, 0
071142	042611	001242	042620		STMP0, STMP1, SCRLF, MS25, MS30, STMP2, MS31, STMP3
071162	042573	001250	042602		MS32, STMP4, MS33, STMP5, MS34, STMP6, SCRLF, MS26
071172	042611	001254	042620		MS30, STMP7, MS31, STMP10
071210	001232	001234	040412	DT47: .WORD	MS32, STMP11, MS33, STMP12, MS34, STMP13, 0
071232	001242	001313	042026		STMP0, STMP1, STAB, STMP2, SCRLF, MS12, MS7, STMP3, MS10
	070614				STMP4, SCRLF, MS13, 0
071242	001232	001234	040412	DT50=DT34	
071256	001313	042525	042564	DT51: .WORD	STMP0, STMP1, STAB, STMP2, STAB, STMP3
	071210				SCRLF, MS25, MS27, STMP4, SCRLF, MS26, MS27, STMP5, 0
071300	001232	001234	040412	DT52=DT47	
071314	001242	000000		DT53: .WORD	STMP0, STMP1, STAB, STMP2, STAB, STMP3
071320	001232	001234	040412		STMP4, 0
071342	001313	041566	042314	DT54: .WORD	STMP0, STMP1, STAB, STMP2, SCRLF, MS1, MS21, SCRLF, STMP3
	071210				SCRLF, MS2, MS21, SCRLF, STMP3, 0
	071300			DT55=DT47	
	071320			DT56=DT53	
	071320			DT57=DT54	
	071300			DT60=DT53	
	071320			DT61=DT54	
	071320			DT62=DT54	
	071320			DT63=DT54	
071356	001232	001234	040412	DT64: .WORD	STMP0, STMP1, STAB, STMP2, STAB, STMP3
071372	001242	000000			STMP4, 0
	071356			DT65=DT64	
	071320			DT66=DT54	
	070210			DT67=DT21	
071376	001232	001234	040412	DT70: .WORD	STMP0, STMP1, STAB, STMP2, SCRLF, MS6, SCRLF, MS7, STMP5
071420	041730	001246	001313		MS10, STMP6, SCRLF, MS11, SCRLF, MS7, STMP5, MS10, STMP7, 0
	071376			DT71=DT70	
071444	001232	001234	040412	DT72: .WORD	STMP0, STMP1, STAB, STMP2, STAB, STMP3, STMP4, 0
	071320			DT73=DT54	
	070210			DT74=DT21	
	071376			DT75=DT70	
	071376			DT76=DT70	
	071444			DT77=DT72	
	071320			DT100=DT54	
	071376			DT101=DT70	
	071376			DT102=DT71	
	071376			DT103=DT70	
	071320			DT104=DT54	
	071376			DT105=DT70	
	071444			DT106=DT72	
	071376			DT107=DT70	
	071320			DT110=DT54	
071464	001232	001234	040412	DT111: .WORD	STMP0, STMP1, STAB, STMP2, 0
	071464			DT112=DT111	
	071464			DT113=DT111	
	071464			DT114=DT111	
	071464			DT115=DT111	
	071464			DT116=DT111	

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071476	001234	040412	DT117=DT3	STMP0,STMP1,STAB,STMP2,STAB,STMP3,STMP4,SCRLF
071516	001313	041714	DT120=DT3	MS6,SCRLF,MS7,STMP5,MS10,STMP6,SCRLF
071534	001313	041714	DT121=DT47	MS11,SCRLF,MS7,STMP5,MS10,STMP7,SCRLF,MS37,SCRLF,STMP10,C
			DT122=DT34	
			DT123=DT51	
			DT124: .WORD	
			DT125=DT124	
			DT126=DT111	
			DT127=DT111	
			DT130=DT3	
071562	001234	040412	DT131: .WORD	STMP0,STMP1,STAB,STMP2,SCRLF,MS37,SCRLF,STMP3
071602	042670	001313	.WORD	SCRLF,MS40,SCRLF,STMP4,SCRLF,MS415,SCRLF,STMP5,0
			DT132=DT131	
071624	001234	040412	DT133: .WORD	STMP0,STMP1,STAB,STMP2,SCRLF,MS41,SCRLF,STMP3
071644	042750	001313	.WORD	SCRLF,MS42,SCRLF,STMP4,SCRLF,MS43,SCRLF,STMP5
071664	043003	001313	.WORD	SCRLF,MS44,SCRLF,STMP6,0
			DT134=DT133	
			DT135=DT133	
			DT136=DT133	
071676	001234	040412	DT137: .WORD	STMP0,STMP1,STAB,STMP2,STMP10,STAB,STMP11,0
			DT140=DT137	
			DT141=DT137	
			DT142=DT137	
			DT143=DT133	
			DT144=DT133	
			DT145=DT137	
			DT146=DT137	
			DT147=DT133	
			DT150=DT133	
			DT151=DT137	
			DT152=DT133	
			DT153=DT137	
071716	001234	040412	DT154: .WORD	STMP0,STMP1,STAB,STMP2,C
			DT155=DT154	
			DT156=DT133	
			DT157=DT133	
			DT160=DT133	
			DT161=DT133	
			DT162=DT133	
			DT163=DT133	
			DT164=DT3	
			DT165=DT3	
			DT166=DT133	
			DT167=DT133	
			DT170=DT133	
			DT171=DT133	
			DT172=DT133	
			DT173=DT133	
			DT174=DT133	
			DT175=DT133	
			DT176=DT133	
			DT177=DT133	
			DT200=DT133	
			DT201=DT133	

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MAINDEC-11-DFFPA-A PDP 11 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 162  
DFFPA.P11 01-NOV-76 21:03 FLAG RESET AND CONSOLE TEST ROUTINE

	071624			DT202=DT133	
	071624			DT203=DT133	
	071624			DT204=DT133	
	067730			DT205=DT3	
	071624			DT206=DT133	
	071624			DT207=DT133	
	071624			DT210=DT133	
071730	001232	001234	040412	DT211: .WORD	STMP0,STMP1,STAB,STMP2,STAB,STMP3,0
071746	001232	001234	040412	DT212: .WORD	STMP0,STMP1,STAB,STMP2,0
	071746			DT213=DT212	

00000: :12345 .END

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MAINDEC-11-DFFPA-A POP 11:34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE 164  
DFFPA.P11 01-NOV-76 21:03 SYMBOL TABLE

ACDAR0	026236	ADDW0 =	000000	ACS	004476	BB30	030650	CCER3	027456
ACDAR1	026336	ADDW1 =	000000	A1	004350	BB31	030660	CCER4	027474
ACDAR2	025570	ADDW10 =	000000	A11	004350	BB32	030672	CCER44	027510
ACDAR3	025756	ADDW11 =	000000	A12	004264	BB33	030722	CCER5	027532
ACDAR4	026012	ADDW12 =	000000	A2	004422	BB34	030744	CCER50	027376
ACDAR5	026046	ADDW13 =	000000	A3	004426	BB35	030754	CCER55	027546
ACDAR6	026056	ADDW14 =	000000	A4	004474	BB4	030210	CCER6	027566
ACDAR7	026112	ADDW15 =	000000	A5	004500	BB5	030220	CCER7	027622
ACDAR8	026146	ADDW2 =	000000	A6	004526	BB6	030232	CCER8	027640
ACDAR9	026202	ADDW3 =	000000	A7	004552	BB7	030262	CCER90	027344
ACDAR10	025764	ADDW4 =	000000	BBDAT0	031300	BDONE	004772	CCP0	027774
ACDAR11	026246	ADDW5 =	000000	BBONE	031430	BERR	004726	CCP1	030004
ACDAR12	026256	ADDW6 =	000000	BBER0	030766	BERR1	004756	CCP10	030074
ACDAR13	026266	ADDW7 =	000000	BBER1	031026	BIT0 =	000001	CCP11	030104
ACDAR14	026276	ADDW8 =	000000	BBERIC	031006	BIT00 =	000001	CCP12	030114
ACDAR15	026306	ADDW9 =	000000	BBER11	031042	BIT01 =	000002	CCP2	030014
ACDAR16	026316	ADVCT =	000000	BBER2	031064	BIT02 =	000004	CCP3	030024
ACDAR17	026326	ADVM =	000000	BBER3	031102	BIT03 =	000010	CCP4	030034
ACDAR18	026326	ADVNE =	004670	BBER4	031136	BIT04 =	000020	CCP5	030044
ACDAR19	025310	ADV =	000000	BBER40	031152	BIT05 =	000040	CCP6	030054
ACDAR20	025424	ADV =	000000	BBER5	031172	BIT06 =	000100	CCP7	030064
ACDAR21	025454	ADV =	004560	BBER6	031210	BIT07 =	000200	CC1	026342
ACDAR22	025474	ADV =	004562	BBER7	031244	BIT08 =	000400	CC10	025556
ACDAR23	025514	ADV =	004574	BBER8	031262	BIT09 =	001000	CC11	025566
ACDAR24	025534	ADV =	004626	BBPATO	031310	BIT1 =	000002	CC12	025574
ACDAR25	025554	ADV =	000000	BBPAT1	031320	BIT10 =	002000	CC13	025506
ACDAR26	025564	ADV =	000000	BBPAT2	031330	BIT11 =	004000	CC14	025636
ACDAR27	025574	ADV =	000000	BBPAT3	031340	BIT12 =	010000	CC15	025662
ACDAR28	025584	ADV =	000000	BBPAT4	031350	BIT13 =	020000	CC16	025702
ACDAR29	025594	ADV =	000000	BBPAT5	031360	BIT14 =	040000	CC17	025712
ACDAR30	025606	AMAMS1 =	000000	BBPAT6	031370	BIT15 =	100000	CC18	025720
ACDAR31	025616	AMAMS2 =	000000	BBP10	031410	BIT2 =	000004	CC19	025732
ACDAR32	025630	AMAMS3 =	000000	BBP11	031420	BIT3 =	000010	CC2	025372
ACDAR33	025656	AMAMS4 =	000000	BBP7	031400	BIT4 =	000020	CC20	025762
ACDAR34	025656	AMSGAD =	000000	BB1	030130	BIT5 =	000040	CC21	027006
ACDAR35	025656	AMSGLG =	000000	BB10	030304	BIT6 =	000100	CC22	027026
ACDAR36	025656	AMSGTY =	000000	BB11	030324	BIT7 =	000200	CC23	027036
ACDAR37	025666	AMTYP1 =	000000	BB12	030334	BIT8 =	000400	CC24	027044
ACDAR38	025676	AMTYP2 =	000000	BB13	030342	BIT9 =	001000	CC25	027056
ACDAR39	025686	AMTYP3 =	000000	BB14	030354	BPTVEC =	000014	CC26	027106
ACDAR40	025696	AMTYP4 =	000000	BB15	030404	B1	004702	CC27	027130
ACDAR41	025706	APASS =	000000	BB16	030430	B2	004704	CC28	027150
ACDAR42	025716	APRIOR =	000000	BB17	030440	B3	004722	CC29	027160
ACDAR43	025726	APTCSU =	000040	BB2	030166	CCDAT0	027764	CC3	026414
ACDAR44	025736	APTEV =	000001	BB20	030452	CCDONE	030124	CC30	027166
ACDAR45	025746	APTSIZ =	000200	BB21	030502	CCER0	027326	CC31	027200
ACDAR46	025756	APTSPO =	000100	BB22	030526	CCER1	027362	CC32	027230
ACDAR47	025766	ASWREG =	000000	BB23	030546	CCER10	027674	CC33	027252
ACDAR48	025776	ATESTN =	000000	BB24	030556	CCER11	027712	CC34	027272
ACDAR49	025786	AUNIT =	000000	BB25	030564	CCER12	027730	CC35	027302
ACDAR50	025796	AUSWR =	000000	BB26	030576	CCER13	027746	CC36	027310
ACDAR51	025806	AVECT1 =	000000	BB27	030626	CCER2	027420	CC37	027322
ACDAR52	025816	AVECT2 =	000000	BB3	030172	CCER22	027434	CC4	026434



0633740	CH203	0651222	DH7	0636220	DT142	071676	DT27	072442
0633740	CH203	0651222	DH70	0636220	DT143	071624	DT3	067730
0633740	CH204	0651222	DH71	064450	DT144	071624	DT30	072504
0651222	CH205	063740	DH72	064512	DT145	071676	DT31	072366
0000000	CH206	0651222	DH73	0651222	DT146	071676	DT32	070442
0651222	CH207	0651222	DH74	0636220	DT147	071624	DT33	072536
0651222	CH21	0636220	DH75	0636220	DT15	070004	DT34	072614
0651222	CH210	0651222	DH76	064450	DT150	071624	DT35	072614
0651222	CH211	066544	DH77	064512	DT151	071676	DT36	070614
0651222	CH212	0636220	DISP	001142	DT152	071624	DT37	072614
0651222	CH213	0636220	DISPRE	000174	DT153	071676	DT4	067730
0651222	CH22	064116	DPAT3	016446	DT154	071716	DT40	072536
0651222	CH23	064153	DSWR	177570	DT155	071716	DT41	072666
0651222	CH24	064311	DT1	067664	DT156	071624	DT42	070720
0651222	CH25	064450	DT10	070004	DT157	071624	DT43	070720
0651222	CH26	064512	DT100	071320	DT16	070146	DT44	070776
0651222	CH27	064512	DT101	071376	DT160	071624	DT45	071046
0651222	CH3	063433	DT102	071376	DT161	071624	DT46	071122
0651222	CH30	000000	DT103	071376	DT162	071624	DT47	071210
0651222	CH31	064512	DT104	071320	DT163	071624	DT5	067752
0651222	CH32	064512	DT105	071376	DT164	067730	DT50	070614
0651222	CH33	064600	DT106	071444	DT165	067730	DT51	071242
0651222	CH34	064600	DT107	071376	DT166	071624	DT52	071210
0651222	CH35	064600	DT11	070004	DT167	071624	DT53	071300
0651222	CH36	064600	DT110	071320	DT168	071624	DT54	071320
0651222	CH37	064600	DT111	071464	DT170	071624	DT55	071210
0651222	CH4	063524	DT112	071464	DT171	071624	DT56	071300
0651222	CH40	064600	DT113	071464	DT172	071624	DT57	071320
0651222	CH41	000000	DT114	071464	DT173	071624	DT6	070004
0651222	CH42	064703	DT115	071464	DT174	071624	DT60	071300
0651222	CH43	064703	DT116	071464	DT175	071624	DT61	071320
0651222	CH44	000000	DT117	067730	DT176	071624	DT62	071320
0651222	CH45	065005	DT12	070026	DT177	071624	DT63	071320
0651222	CH46	065024	DT120	067730	DT2	067706	DT64	071356
0651222	CH47	065005	DT121	071210	DT20	070166	DT65	071356
0651222	CH5	0636220	DT122	070614	DT200	071624	DT65	071320
0651222	CH50	065050	DT123	071242	DT201	071624	DT67	070210
0651222	CH51	065050	DT124	071476	DT202	071624	DT7	070004
0651222	CH52	0636220	DT125	071476	DT203	071624	DT70	071376
0651222	CH53	064512	DT126	071464	DT204	071624	DT71	071376
063740	CH54	0651222	DT127	071464	DT205	067730	DT72	071444
0651222	CH55	0636220	DT13	070114	DT206	071624	DT73	071320
0651222	CH56	064512	DT130	067730	DT207	071624	DT74	070210
0651222	CH57	0651222	DT131	071562	DT21	070210	DT75	071376
0651222	CH6	0636220	DT132	071562	DT210	071624	DT76	071376
0651222	CH60	064512	DT133	071624	DT211	071730	DT77	071444
0651222	CH61	0651222	DT134	071624	DT212	071746	DT1	005724
0651222	CH62	0651222	DT135	071624	DT213	071746	DT10	006126
0651222	CH63	0651222	DT136	071624	DT22	070222	DT2	005750
0633740	CH64	065232	DT137	071676	DT23	070250	DT3	005752
064030	CH65	065163	DT14	070004	DT24	070272	DT4	005756
0651222	CH66	0651222	DT140	071676	DT25	070316	DT5	005770
0651222	CH67	0636220	DT141	071676	DT26	070366	DT6	006204

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07	006014	EM120	053445	EM64	050412	FERR4	007070
08	006064	EM121	053601	EM65	= 050412	FERR5	007174
09	006076	EM122	053720	EM66	050546	FERR6	007230
EDONE	006300	EM123	054017	EM67	050611	FERR7	007364
EMDATO	034116	EM124	054060	EM7	043321	FER2	007042
EM007	034200	EM125	054153	EM70	051042	FFDATO	034476
EM010	033710	EM126	054243	EM71	051165	FFDOEM	034556
EM011	033726	EM127	054452	EM72	051267	FFERO	034366
EM012	033764	EM128	= 000000	EM73	051343	FFERI	034402
EM013	033800	EM129	054665	EM74	051403	FFERR2	034440
EM014	033800	EM130	=	EM75	051634	FFPO	034506
EM015	033800	EM131	054765	EM76	051757	FFP1	034516
EM016	033800	EM132	055025	EM77	052061	FFP2	034526
EM017	033800	EM133	055065	ERM10	035564	FFP3	034536
EM018	033800	EM134	055124	ERRVEC=	000004	FFP4	034546
EM019	033800	EM135	055163	ERTYPE	037546	FF1	034204
EM020	033800	EM136	055222	ERT1	037730	FF1C	034354
EM021	033800	EM137	= 055065	ERT2	040146	FF11	034364
EM022	033800	EM14	043435	ERT3	040152	FF2	034234
EM023	033800	EM140	055124	ERT4	040162	FF3	034256
EM024	033800	EM141	055163	ERT5	040174	FF4	034264
EM025	033800	EM142	= 055222	E1	006154	FF5	034274
EM026	033800	EM143	055261	E2	006170	FF6	034324
EM027	033800	EM144	055314	E3	006170	FF7	034346
EM028	033800	EM145	055361	E4	006172	FPSMS	040531
EM029	033800	EM146	055314	FOATIO	010110	FPSPUR	040200
EM030	033800	EM147	055347	FOAT11	010112	FPVECT=	000244
EM031	033800	EM15	043560	FOAT12	010114	FXDATO	010154
EM032	033800	EM150	055347	FOAT13	010116	FXDAT1	010156
EM033	033800	EM151	= 055347	FOAT14	010120	FXDAT2	010160
EM034	033800	EM152	055401	FOAT15	010122	FXDAT3	010162
EM035	033800	EM153	= 055401	FOAT16	010124	FXDAT4	010164
EM036	033800	EM154	055433	FOAT17	010126	FXDAT5	010166
EM037	033800	EM155	055665	FOAT00	010132	FXDAT6	010170
EM038	033800	EM156	056120	FOAT01	010134	FXDAT7	010172
EM039	033800	EM157	056335	FOAT02	010136	F1	006304
EM040	033800	EM16	043702	FOAT03	010140	F1C	006516
EM041	033800	EM160	056554	FOAT04	010142	F11	006520
EM042	033800	EM161	056761	FOAT05	010144	F12	006536
EM043	033800	EM162	057166	FOAT06	010146	F13	006570
EM044	033800	EM163	057233	FOAT07	010150	F135	006550
EM045	033800	EM164	057300	FOONE	010174	F14	006600
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EM049	033800	EM17	043754	FERR10	007520	F2	006336
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EM051	033800	EM171	060071	FERR12	007036	F21	006650
EM052	033800	EM172	060330	FERR20	007574	F22	006660
EM053	033800	EM173	060567	FERR21	007712	F23	006676
EM054	033800	EM174	061026	FERR25	007742	F3	006356
EM055	033800	EM175	061265	FERR26	010060	F4	006360
EM056	033800	EM176	061524	FERR3	007076	F5	006376
EM057	033800	EM177	061661				

G36	0136452	G31	013130	IDATI1	011030	JDATI2	016132	LDAT00	016762
G37	006476	G32	013162	IDATI2	011032	JDATI3	016134	LDAT01	016764
G3OR	014200	G33	013164	IDATI3	011034	JDAT00	016136	LDATC2	016766
G3NO0	014150	G34	013252	IDAT00	011016	JDAT0	016146	LDATC3	016770
G3NO1	014152	G35	013304	IDAT01	011020	JDAT01	016140	LDONE	016772
G3NO2	014154	G36	013306	IDAT02	011022	JDAT02	016142	LD1	041242
G3NO3	014156	G37	013374	IDAT03	011024	JCAT03	016144	LD2	041272
GCMP	013704	G4	012032	IDONE	011036	JDAT1	016150	LEPR1	016604
GDAT00	014170	G40	013426	IERR0	010576	JDAT2	016152	LEPR2	016656
GDAT01	014172	G41	013430	IERR1	010660	JDAT3	016154	LEPR3	016630
GDAT02	014174	G42	013516	IERR2	010700	JCONE	016156	LF	= 000012
GDAT03	014176	G43	013550	IERR25	010722	JERR0	015776	LFIX1	040417
GDONE	014202	G44	013552	IERR3	010752	JERR1	016044	LFIX2	040467
GEERR1	013732	G5	012064	IERR4	010726	JERR2	015070	LFPS1	041227
GEFLAG1	014124	G6	012066	ILLMS	041506	J1	015670	LOOP	004304
GEFLAG2	014126	G7	012154	ILL1	041402	J10	016016	LPAT10	016726
GEOR0	014160	H00R	014732	ILL2	041445	J2	015714	LPAT11	016730
GEOR1	014162	H01R	015006	IOTVEC =	000020	J3	015716	LPAT12	016732
GEOR2	014164	H01W	014736	IPAT10	010776	J4	015720	LPAT13	016734
GEOR3	014166	H02R	015016	IPAT11	011000	J5	015754	LPAT20	016736
GEAT00	014130	H02W	014746	IPAT12	011002	J6	015762	LPAT21	016740
GEAT01	014132	H03R	015026	IPAT13	011004	J7	015774	LPAT22	016742
GEAT02	014134	H03W	014756	IPAT20	011006	KBJFO	016420	LPAT23	016744
GEAT03	014136	H04R	015036	IPAT21	011010	KBUF1	016422	LPERR =	104413
GEAT10	014140	H04W	014766	IPAT22	011012	KBJF2	016424	L1	016456
GEAT11	014142	H05R	015046	IPAT23	011014	KBUF3	016426	L2	016516
GEAT12	014144	H05W	014776	I1	010202	KDAT10	016410	L3	016520
GEAT13	014146	HCLR	014662	I10	010364	KDAT11	016412	L4	016522
GESEF+	013664	HCLR1	014672	I105	010372	KDAT12	016414	L5	016572
GESEFUP	013606	HCMP	014624	I106	010366	KDAT13	016416	L6	016600
GESUM	014022	HCMP1	014644	I11	010374	KDAT00	016430	MDAT00	017312
GES1	013636	HCMP2	014654	I12	010376	KDAT01	016432	MDAT01	017314
GSWR =	104405	HDAT1	015056	I13	010412	KDAT02	016434	MDAT02	017316
G61	011710	HDAT2	015066	I14	010454	KDAT03	016436	MDAT03	017320
G610	012206	HDAT3	015076	I15	010456	KDONE	016450	MDONE	017322
G611	012210	HDAT4	015106	I16	010460	KERR0	016272	MERR0	017146
G612	012276	HDAT5	015116	I17	010476	KERR1	016336	MERR1	017204
G613	012330	HDONE	015126	I2	010220	KERR2	016362	MERR2	017120
G614	012332	HERROR	014700	I20	010536	KPAT0	016440	MERR3	017232
G615	012420	HFLAG	014734	I21	010552	KPAT1	016442	MNUMB =	000213
G616	012452	HSTD	014546	I22	010556	KPAT2	016444	MNUM0	042210
G617	012454	HT =	000011	I23	010572	K1	016164	MNUM1	042216
G620	011742	H1	014210	I3	010266	K10	016312	MNUM2	042223
G621	012542	H10	014452	I4	010270	K2	016210	MNUM3	042230
G622	012574	H11	014504	I5	010272	K3	016212	MNUM4	042237
G623	012576	H12	014536	I6	010316	K4	016214	MNUM5	042245
G624	012664	H2	014230	I7	010332	K5	016250	MPAT10	017272
G625	012716	H3	014240	JBUFO	016116	K6	016256	MPAT11	017274
G626	012720	H4	014312	JBUF1	016120	K7	016270	MPAT12	017276
G627	013006	H5	014334	JBUF2	016122	LDAT10	016750	MPAT13	017300
G628	013040	H6	014366	JBUF3	016124	LDATI1	016752	MPAT20	017302
G629	011744	H7	014420	JDAT10	016126	LDATI2	016754	MPAT21	017304
G630	013042	IDATIO	011026	JDATI1	016130	LDATI3	016756	MPAT22	017306



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POWERM 040344  
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PPAT13 021166  
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PR6 = 000300  
PR7 = 000340  
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SAVREG = 104410  
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SCAT01 015654  
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SCAT03 015660  
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SERR5 015604  
SERR6 015514  
SERR7 015540  
SETD1 041360  
SETF1 041352  
SETI1 041366  
SETL1 041374  
SPACE 040414  
SPAT10 015642  
SPAT11 015644



SYMBOL TABLE

SEB	001122	SETABL	001336	SPWRDN	037362	STMP1	001234
SEB001	001126	SETENC	001442	SPWRMG	037516	STMP10	001252
SEBELL	001306	SFATAL	001320	SPWRUP	037434	STMP11	001254
SECDM1	001376	SFFLG	036636	\$LES	001312	STMP12	001256
SECDM2	001400	SFILLC	001156	\$ROCHR	037122	STMP13	001260
SECDM3	036140	SFILLS	001155	\$ROSZ =	000001	STMP14	001262
SECDM4	036640	\$CDADR	001120	\$REGC	001160	STMP15	001264
SECDM5	034760	\$CODAT	001124	\$REGD	001162	STMP16	001266
SECDM6	001100	\$GET42	034742	\$REGI	001164	STMP17	001270
SECDM7	000024	\$GYSWR	036710	\$REG10	001202	STMP2	001236
SECDM8	000050	\$HD =	000003	\$REG11	001204	STMP20	001272
SECDM9	000024	\$HIBTS	003572	\$REG12	001206	STMP21	001274
SECDM10	000024	\$ICNT	001104	\$REG13	001210	STMP22	001276
SECDM11	037247	\$ILLUP	037540	\$REG14	001212	STMP23	001300
SECDM12	037242	\$INTAG	001135	\$REG15	001214	STMP3	001240
SECDM13	001344	\$ITEMB	001114	\$REG16	001216	STMP4	001242
SECDM14	001313	\$LF	001314	\$REG17	001220	STMP5	001244
SECDM15	001402	\$LFLG	036635	\$REG2	001166	STMP6	001246
SECDM16	001404	\$LOOP	035036	\$REG20	001222	STMP7	001250
SECDM17	001426	\$LPADR	001106	\$REG21	001224	STN =	000037
SECDM18	001430	\$LPERR	001110	\$REG22	001226	STPB	001152
SECDM19	001432	\$MADR1	001350	\$REG23	001230	STPFLG	001157
SECDM20	001434	\$MADR2	001354	\$REG3	001170	STPS	001150
SECDM21	001436	\$MADR3	001360	\$REG4	001172	STRAP	037276
SECDM22	001440	\$MADR4	001364	\$REG5	001174	STRAP2	037320
SECDM23	001406	\$MAIL	001316	\$REG6	001176	STRP =	000014
SECDM24	001410	\$MAMS1	001346	\$REG7	001200	STRPAD	037332
SECDM25	001412	\$MAMS2	001352	\$RESRE	035624	\$STM	003576
SECDM26	001414	\$MAMS3	001356	\$RTNAD	035040	\$STNM	001102
SECDM27	001416	\$MAMS4	001362	\$RTRN	035034	\$TYPE	035662
SECDM28	001420	\$MBAOR	003574	\$SAVRE	035566	\$TYPEC	036074
SECDM29	001422	\$MFLG	036634	\$SAVR6	037544	\$TYPEX	036142
SECDM30	001424	\$MNEW	037265	\$SCOPE	035050	\$TYPOC	036170
SECDM31	001326	\$MSGAD	001332	\$SETUP =	000137	\$TYPON	036204
SECDM32	001374	\$MSGLG	001334	\$STUP =	177777	\$TYPOS	036144
SECDM33	035000	\$MSGTY	001316	\$SVLAD	035256	\$UNIT	001330
SECDM34	034770	\$MSWR	037254	\$SVPC =	003572	\$UNITH	003602
SECDM35	034612	\$MTYP1	001347	\$SWR =	177400	\$USWR	001342
SECDM36	035044	\$MTYP2	001353	\$SWREG	001340	\$VECT1	001366
SECDM37	001336	\$MTYP3	001357	\$SWRMK =	000000	\$VECT2	001370
SECDM38	001337	\$MTYP4	001363	\$SWRMS =	000200	\$XSTR	035062
SECDM39	034556	\$MXCNT	035326	\$TAB	040412	\$SET4 =	000001
SECDM40	034604	\$NULL	001154	\$TAB	035042	\$OFILL	036367
SECDM41	001103	\$NWTST =	000001	\$TERM =	000030	.	= 071760
SECDM42	001115	\$OCNT	036366	\$TESTN	001322	.LPER	040266
SECDM43	035330	\$OMODE	036370	\$THE	040606	.RSET	040274
SECDM44	001116	\$OVER	035312	\$TIMES	001302	.SX =	003572
SECDM45	001442	\$PASS	001324	\$TKB	001146		
SECDM46	001112	\$PASTH	003600	\$TKS	001144		
SECDM47	001304	\$PWRAD	037522	\$TMP0	001232		

.ABS. 071760 000

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

C14

MAINDEC-11-DFFPA-A POP :: 34 FPP DIAGNOSTIC PART 1 MACY11 27(1006) 01-NOV-76 21:09 PAGE .72  
DFFPA.P11 01-NOV-76 21:03 SYMBOL TABLE

DSK2:DFFPA.BIN.DSK2:DFFPA.SEG SOL+DFFPA.P11  
RUN-TIME: 86.73 SECONDS  
RUN-TIME RATIO: 24.167=1.4  
CORE USED: 3.7 MB PAGES

